

FIPS 204

Federal Information Processing Standards Publication

Module-Lattice-Based Digital Signature Standard

Category: Computer Security **Subcategory:** Cryptography

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U.S. Department of Commerce Gina M. Raimondo, Secretary

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Foreword

The Federal Information Processing Standards Publication Series of the National Institute of Standards and Technology is the official series of publications relating to standards and guidelines developed under 15 U.S.C. 278g-3, and issued by the Secretary of Commerce under 40 U.S.C. 11331.

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> Kevin M. Stine, Director Information Technology Laboratory

Abstract

Digital signatures are used to detect unauthorized modifications to data and to authenticate the identity of the signatory. In addition, the recipient of signed data can use a digital signature as evidence in demonstrating to a third party that the signature was, in fact, generated by the claimed signatory. This is known as non-repudiation since the signatory cannot easily repudiate the signature at a later time. This standard specifies ML-DSA, a set of algorithms that can be used to generate and verify digital signatures. ML-DSA is believed to be secure, even against adversariesin possession of a large-scale quantum computer.

Keywords: cryptography; digital signatures; Federal Information Processing Standards; lattice; postquantum; public-key cryptography.

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Announcing the Module-Lattice-Based Digital Signature Standard

Federal Information Processing Standards (FIPS) publications are developed by the National Institute of Standards and Technology (NIST) under 15 U.S.C. 278g-3 and issued by the Secretary of Commerce under 40 U.S.C. 11331.

- 1. **Name of Standard.** Module-Lattice-Based Digital Signature Standard (FIPS 204).
- 2. **Category of Standard.** Computer Security. **Subcategory.** Cryptography.
- 3. **Explanation.** Thisstandard specifies ML-DSA, a lattice-based digitalsignature algorithm for applications that require a digital signature rather than a written signature. Additional digital signature schemes are specified and approved in other NIST Special Publications and FIPS publications (e.g., FIPS 186-5 [\[1\]](#page-56-0)). A digital signature is represented in a computer as a string of bits and computed using a set of rules and parameters that allow the identity of the signatory and the integrity of the data to be verified. Digital signatures may be generated on both stored and transmitted data.

Signature generation uses a private key to generate a digital signature. Signature verification uses a public key that corresponds to but is not the same as the private key. Each signatory possesses a key-pair composed of a private key and a corresponding public key. Public keys may be known by the public, but private keys must be kept secret. Anyone can verify the signature by employing the signatory's public key. Only the user who possesses the private key can generate a signature that can be verified by the corresponding public key.

The digital signature is provided to the intended verifier along with the signed data. The verifying entity verifies the signature by using the claimed signatory's public key. Similar procedures may be used to generate and verify signatures for both stored and transmitted data.

This standard specifies several parameter sets for ML-DSA that are **approved** for use. Additional parameter sets may be specified and approved in future NIST Special Publications.

- 4. **Approving Authority.** Secretary of Commerce.
- 5. **Maintenance Agency.** Department of Commerce, National Institute of Standards and Technology, Information Technology Laboratory (ITL).
- 6. **Applicability.** This standard is applicable to all federal departments and agencies for the protection of sensitive unclassified information that is not subject to section 2315 of Title 10, United States Code, or section 3502 (2) of Title 44, United States Code. Either this standard, FIPS 205, FIPS 186-5, or NIST Special Publication 800-208 **shall** be used in designing and implementing public-key-based signature systems that federal departments and agencies operate or that are operated for them under contract. In the future, additional digital signature schemes may be specified and approved in FIPS or NIST Special Publications.

The adoption and use of this standard are available to private and commercial organizations.

- 7. **Applications.** A digital signature algorithm allows an entity to authenticate the integrity of signed data and the identity of the signatory. The recipient of a signed message can use a digital signature as evidence in demonstrating to a third party that the signature was, in fact, generated by the claimed signatory. This is known as non-repudiation since the signatory cannot easily repudiate the signature at a later time. A digital signature algorithm is intended for use in electronic mail, electronic funds transfer, electronic data interchange, software distribution, data storage, and other applications that require data integrity assurance and data origin authentication.
- 8. **Implementations.** A digital signature algorithm may be implemented in software, firmware, hardware, or any combination thereof. NIST will develop a validation program to test implementations for conformance to the algorithm in this standard. For every computational procedure that is specified in this standard, a conforming implementation may replace the given set of steps with any mathematically equivalent set of steps. In other words, different procedures that produce the correct output for every input are permitted.

Information about validation programs is available at [https://csrc.nist.gov/projects/cmvp.](https://csrc.nist.gov/projects/cmvp) Examples for digital signature algorithms are available at [https://csrc.nist.gov/projects/cryptographic-standards](https://csrc.nist.gov/projects/cryptographic-standards-and-guidelines/example-values) [-and-guidelines/example-values.](https://csrc.nist.gov/projects/cryptographic-standards-and-guidelines/example-values)

Agencies are advised that digital signature key pairs **shall not** be used for other purposes.

- 9. **Other Approved Security Functions.** Digital signature implementations that comply with this standard **shall** employ cryptographic algorithms that have been approved for protecting Federal Governmentsensitive information. **Approved** cryptographic algorithms and techniques include those that are either:
	- a. Specified in a Federal Information Processing Standards (FIPS) publication,
	- b. Adopted in a FIPS or NIST recommendation, or
	- c. Specified in the list of **approved** security functions in SP 800-140C.
- 10. **Export Control.** Certain cryptographic devices and technical data regarding them are subject to federal export controls. Exports of cryptographic modules that implement this standard and technical data regarding them must comply with these federal regulations and be licensed by the Bureau of Industry and Security of the U.S. Department of Commerce. Information about export regulations is available at [https://www.bis.doc.gov.](https://www.bis.doc.gov)
- 11. **Patents.** The algorithm in this standard may be covered by U.S. or foreign patents.
- 12. **Implementation Schedule.** This standard becomes effective immediately upon final publication.
- 13. **Specifications.** Federal Information Processing Standards (FIPS) 204, Module-Lattice-Based Digital Signature Standard (affixed).
- 14. **Qualifications.** The security of a digital signature system depends on maintaining the secrecy of the signatory's private keys. Signatories **shall**, therefore, guard against the disclosure of their private keys. While it is the intent of this standard to specify general security requirements for generating digital signatures, conformance to this standard does not ensure that a particular implementation is secure. It is the responsibility of an implementer to ensure that any module that implements a digital signature capability is designed and built in a secure manner.

Similarly, the use of a product containing an implementation that conforms to this standard does not guarantee the security of the overall system in which the product is used. The responsible authority in each agency or department **shall** ensure that an overall implementation provides an acceptable level of security.

Since a standard of this nature must be flexible enough to adapt to advancements and innovations in science and technology, this standard will be reviewed every five years in order to assess its adequacy.

- 15. **Waiver Procedure.** The Federal Information Security Management Act (FISMA) does not allow for waivers to Federal Information Processing Standards (FIPS) that are made mandatory by the Secretary of Commerce.
- 16. **Where to Obtain Copies of the Standard.** This publication is available by accessing [https://csrc.nist.](https://csrc.nist.gov/publications) [gov/publications.](https://csrc.nist.gov/publications) Other computer security publications are available at the same website.
- 17. **How to Cite This Publication.** NIST has assigned **NIST FIPS 204** as the publication identifier for this FIPS, per the NIST Technical Series [Publication](https://www.nist.gov/document/publication-identifier-syntax-nist-technical-series-publications) Identifier Syntax. NIST recommends that it be cited as follows:

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18. **Inquiries and Comments.** Inquiries and comments about this FIPS may be submitted to [fips-204](mailto:fips-204-comments@nist.gov) [comments@nist.gov.](mailto:fips-204-comments@nist.gov)

Federal Information Processing Standards Publication 204

Specification for the Module-Lattice-Based Digital Signature Standard

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1. Introduction

1.1 Purpose and Scope

This standard defines a digital signature scheme, which includes a method for digital signature generation that can be used for the protection of binary data (commonly called a "message") and a method for the verification and validation of those digital signatures. NIST Special Publication (SP) 800-175B [\[2\]](#page-56-2), *Guideline for Using Cryptographic Standards in the Federal Government: Cryptographic Mechanisms*, includes a general discussion of digital signatures.

This standard specifies the mathematical steps that need to be performed for key generation, signature generation, and signature verification. In order for digital signatures to be valid, additional assurances are required, such as assurance of identity and of private key possession. SP 800-89, *Recommendation for Obtaining Assurances for Digital Signature Applications* [\[3\]](#page-56-3), specifies the required assurances and the methods for obtaining them.

The digital signature scheme approved in this standard is the Module-Lattice-Based Digital Signature Algorithm (ML-DSA), which is based on the Module Learning With Errors problem [\[4\]](#page-56-4). ML-DSA is believed to be secure, even against adversaries in possession of a large-scale fault-tolerant quantum computer. In particular, ML-DSA is believed to be strongly unforgeable, which implies that the scheme can be used to detect unauthorized modifications to data and to authenticate the identity of the signatory (one bound to the possession of the private-key). In addition, a signature generated by this scheme can be used as evidence in demonstrating to a third party that the signature was, in fact, generated by the claimed signatory. The latter property is known as non-repudiation since the signatory cannot easily repudiate the signature at a later time.

This standard gives algorithms for ML-DSA key generation, signature generation, and signature verification (Section [5\)](#page-26-0), and for supporting algorithms used by them (Section [7\)](#page--1-1). ML-DSA is standardized with three possible parameter sets, each of which corresponds to a different security strength. Section [4](#page-24-0) describes the global parameters used by these algorithms and enumerates the parameter sets for ML-DSA that are approved by this standard. ML-DSA can be used in place of other digital signature schemes specified in NIST FIPS and Special Publications (e.g., FIPS 186-5, *Digital Signature Standard (DSS)* [\[1\]](#page-56-0)).

1.2 Context

Over the past several years, there has been steady progress toward building quantum computers. The security of many commonly used public-key cryptosystems will be at risk if large-scale quantum computers are ever realized. This would include key-establishment schemes and digital signatures that are based on integer factorization and discrete logarithms(both over finite fields and elliptic curves). As a result, in 2016, NIST initiated a public Post-Quantum Cryptography (PQC) Standardization process to select quantumresistant public-key cryptographic algorithms for standardization. A total of 82 candidate algorithms were submitted to NIST for consideration.

After three rounds of evaluation and analysis, NIST selected the first four algorithms for standardization. ML-DSA is derived from one of the selected schemes, CRYSTALS-DILITHIUM [\[5,](#page-56-5) [6\]](#page-56-6), and isintended to protect sensitive U.S. Government information well into the foreseeable future, including after the advent of cryptographically relevant quantum computers. For the differences between ML-DSA and CRYSTALS-DILITHIUM, see Appendix [D.](#page-63-0)

2. Glossary of Terms, Acronyms, and Symbols

2.1 Terms and Definitions

2.2 Acronyms

2.3 Mathematical Symbols

The following symbols and mathematical expressions are used in this standard.

¹In this specification, bitlen a is only ever called with a small finite number of values for a , so it may be helpful to precompute bitlen a for these values and hard code the results, rather than computing them on the fly.

2.4 Notation

2.4.1 Rings

Elements of the rings \mathbb{Z} , \mathbb{Z}_q , \mathbb{Z}_2 , R , and R_q are denoted by italicized lowercase letters (e.g., w). Elements of the ring T_q are length-256 arrays of elements of \mathbb{Z}_q , and they are denoted by italicized letters with a hat symbol (e.g., \hat{w}). The addition and multiplication of elements of T_q are performed entry-wise. Thus, the *i*th entry of the product of two elements \hat{u} and \hat{v} of T_q is $\hat{u}[i] \cdot \hat{v}[i] \in \mathbb{Z}_q$. The multiplication operation in T_a is denoted by the symbol ∘ (see Section [2.3\)](#page-14-1).

When a product $a \cdot b$ or a sum $a + b$ is written and either a or b is a congruence class modulo m (i.e., if either a or b is an element of \mathbb{Z}_m or R_m), then the product or sum is also understood to be a congruence class modulo m (i.e., an element of \mathbb{Z}_m or R_m). Likewise, an element of R or $\mathbb Z$ may be "typecast" to an element of R_m or \mathbb{Z}_m , respectively, and may be used as the input of a function specified to act on an element of R_m or \mathbb{Z}_m , respectively. In both cases, the element itself or its coefficients are mapped from $\mathbb Z$ to $\mathbb Z_m$ by taking the unique congruence class modulo m that contains the integer.

The coefficients of an element w of R or R_m are denoted by w_i so that $w = w_0 + w_1 X + ... + w_{255} X^{255}$. If w is in R (respectively, R_m) and t is in $\mathbb Z$ (respectively, $\mathbb Z_d$), then $w(t)$ denotes the polynomial $w=$ $w_0 + w_1 X + ... + w_{255} X^{255}$ evaluated at $X = t$.

2.4.2 Vectors and Matrices

Vectors with elements in R or R_m are denoted by bold lowercase letters (e.g., v). Matrices with elements in R or R_m are denoted by bold uppercase letters (e.g., A).

If S is a ring and \bf{v} is a length-L vector over S , then the entries in the vector \bf{v} are expressed as

$$
v[0], v[1], \dots, v[L-1].
$$

The entries of a $K \times L$ matrix A over S are denoted as $A[i, j]$, where $0 \le i \le K$ and $0 \le j \le L$.

The set of all length-L vectors over S is denoted by S^L . The set of all $K \times L$ matrices over S is denoted by $S^{K\times L}$. A length-L vector can also be treated as an $L\times 1$ matrix.

2.5 NTT Representation

The Number Theoretic Transform (NTT) is a specific isomorphism between the rings R_q and T_q . Let $\zeta = 1753 \in \mathbb{Z}_q$, which is a 512^{th} root of unity. If $w \in R_q$, then

$$
NTT(w) = (w(\zeta_0), w(\zeta_1), \dots, w(\zeta_{255})) \in T_q,
$$
\n(2.1)

where $\zeta_i = w(\zeta^{2\text{BitRev}_8(i)+1})$ mod q . See Section [7.5](#page-51-0) for an implementation discussion for NTT and NTT⁻¹.

The motivation for using NTT is that multiplication is considerably faster in the ring T_a . Since NTT is an isomorphism, for any $a, b \in R_{a}$,

$$
NTT(ab) = NTT(a) \circ NTT(b).
$$
 (2.2)

If ${\bf A}$ is a matrix with entries from R_q , then NTT(${\bf A}$) denotes the matrix computed via the entry-wise application of NTT to A. The symbol ∘ is also used to denote the matrix multiplication of matrices with entries in T_q . Thus, NTT $(AB) = NTT(A) \circ NTT(B)$. Explicit algorithms for linear algebra over T_q are given in Section [7.6.](#page-54-0)

3. Overview of the ML-DSA Signature Scheme

ML-DSA is a digital signature scheme based on CRYSTALS-DILITHIUM [\[6\]](#page-56-6). It consists of three main algorithms: [ML-DSA](#page-26-3).KeyGen (Algorithm [1\)](#page-26-3), [ML-DSA](#page-27-2).Sign (Algorithm [2\)](#page-27-2), and [ML-DSA](#page-27-3).Verify (Algorithm [3\)](#page-27-3). The ML-DSA scheme uses the Fiat-Shamir With Aborts construction [\[10,](#page-56-10) [11\]](#page-56-11) and bears the most resemblance to the schemes proposed in [\[12,](#page-56-12) [13\]](#page-56-13).

This document also defines a closely related but domain-separated signature scheme, HashML-DSA, which differs from ML-DSA in that it includes an additional pre-hashing step before signing. It consists of three main algorithms: [ML-DSA](#page-26-3).KeyGen (Algorithm [1\)](#page-26-3), which is the same key generation algorithm used for ML-DSA; [HashML-DSA](#page--1-3).Sign (Algorithm [4\)](#page--1-3); and [HashML-DSA](#page--1-4).Verify (Algorithm [5\)](#page--1-4).

3.1 Security Properties

ML-DSA is designed to be strongly existentially unforgeable under chosen message attack (SUF-CMA). That is, it is expected that even if an adversary can get the honest party to sign arbitrary messages, the adversary cannot create any additional valid signatures based on the signer's public key, including on messages for which the signer has already provided a signature.

Beyond unforgeability, ML-DSA is designed to satisfy additional security properties described in [\[14\]](#page-57-0).

3.2 Computational Assumptions

Security for lattice-based digital signature schemes is typically related to the Learning With Errors (LWE) problem and the short integer solution (SIS) problem. The LWE problem [\[15\]](#page-57-1) is to recover a vector $\mathbf{s}\in\mathbb{Z}_q^n$ given a set of random "noisy" linear equations² satisfied by s. The SIS problem is to find a non-zero solution $\mathbf{t}\in\mathbb{Z}_q^n$ for a given linear system over \mathbb{Z}_q of the form $\mathbf{At}=\mathbf{0}$ such that $\|\mathbf{t}\|_\infty$ is small. For appropriate choices of parameters, these problems are intractable for the best known techniques, including Gaussian elimination.

When the module \mathbb{Z}_q^n in LWE and SIS is replaced by a module over a ring larger than \mathbb{Z}_q (e.g., R_q), the resulting problems are called Module Learning With Errors (MLWE) [\[4\]](#page-56-4) and Module Short Integer Solution (MSIS). The security of ML-DSA is based on the MLWE problem over R_a and a nonstandard variant of MSIS called SelfTargetMSIS [\[16\]](#page-57-2).

3.3 ML-DSA Construction

ML-DSA is a Schnorr-like signature with several optimizations. The Schnorr signature scheme applies the Fiat-Shamir heuristic to an interactive protocol between a verifier who knows q (the generator of a group in which discrete logs are believed to be difficult) and the value $y = q^x$ and a prover who knows q and x. The interactive protocol, where the prover demonstrates knowledge of x to the verifier, consists of three steps:

- 1. Commitment: The prover generates a random positive integer r that is less than the order of g and commits to its value by sending q^r to the verifier.
- 2. Challenge: The verifier sends a random positive integer c that is less than the order of g to the prover.

²Specifically, the LWE problem is to solve a system of equations of the form $\bf{As}+\bf{e}=b$, where \bf{A} and b are given and e is not given but known to be small.

3. Response: The prover returns $s = r - cx$ reduced modulo the order of g, and the verifier checks whether $q^s \cdot y^c = q^r$.

This protocol is made noninteractive and turned into a signature scheme by replacing the verifier's random choice of c in step 2 with a deterministic process that pseudorandomly derives c from a hash of the commitment g^r concatenated with the message to be signed. For this signature scheme, y is the public key, and x is the private key.

The basic idea of ML-DSA and similar lattice signature schemes is to build a signature scheme from an analogous interactive protocol, where a prover who knows matrices $\mathbf{A}\in\mathbb{Z}_q^{K\times L}$, $\mathbf{S}_1\in\mathbb{Z}_q^{L\times n}$, and ${\bf S}_2\in\mathbb{Z}_q^{K\times n}$ with small coefficients (for ${\bf S}_1$ and ${\bf S}_2$) demonstrates knowledge of these matrices to a verifier who knows ${\bf A}$ and ${\bf T}\in \mathbb{Z}_q^{K\times n}={\bf A}{\bf S}_1+{\bf S}_2.$ Such an interactive protocol would proceed as follows:

- 1. Commitment: The prover generates $\mathbf{y} \in \mathbb{Z}_q^L$ with small coefficients and commits to its value by sending $w_{\sf Approx} = {\bf A} {\bf y} + {\bf y}_2$ to the verifier, where ${\bf y}_2 \in \mathbb{Z}_q^K$ is a vector with small coefficients.
- 2. Challenge: The verifier sends a vector $\mathbf{c}\in\mathbb{Z}_q^n$ with small coefficients to the prover.
- 3. Response: The prover returns $z = y + S_1 c$, and the verifier checks that z has small coefficients and that ${\bf Az-Tc} \approx {\bf w}_{\text{Approx}}.$

As written, the above protocol has a security flaw: the response z will be biased in a direction related to the private value S_1 . Likewise $r = w_{Aoprox} - Az + Tc = y_2 + S_2c$ is biased in a direction related to the private value \mathbf{S}_2 . However, this flaw can be corrected when converting the interactive protocol into a signature scheme. As with Schnorr signatures, the signer derives the challenge by a pseudorandom process from a hash of the commitment concatenated with the message. To correct the bias, the signer applies rejection sampling to z; if coefficients of z fall outside of a specified range, the signing process is aborted, and the signer starts over from a new value of y. Likewise, similar rejection sampling must also be applied to r. These checks are analogous to those done at Line [23](#page-34-1) of Algorithm [7.](#page-34-1) In the simplified Fiat-Shamir With Aborts signature, the public key is (A, T) , and the private key is (S_1, S_2) .

In the ML-DSA standard, a number of tweaks and modifications are added to this basic framework for security or efficiency reasons:

- To reduce key and signature size and to use fast NTT-based polynomial multiplication, ML-DSA uses module-structured matrices. Relative to the basic scheme described above, it replaces dimension- $n \times n$ blocks of matrices and dimension- n blocks of vectors with polynomials in the ring $R_q.$ Thus, instead of $\mathbf{A}\in\mathbb{Z}_q^{K\times L}$, $\mathbf{T}\in\mathbb{Z}_q^{K\times n}$, $\mathbf{S}_1\in\mathbb{Z}_q^{L\times n}$, $\mathbf{S_2}\in\mathbb{Z}_q^{K\times n}$, $\mathbf{y}\in\mathbb{Z}_q^L$, $\mathbf{c}\in\mathbb{Z}_q^n$, ML-DSA has $\mathbf{A}\in R^{k\times\ell}_{q}$, $\mathbf{t}\in R^{k}_{q}$, $\mathbf{s}_{1}\in R^{k}_{q}$, $\mathbf{s}_{2}\in R^{k}_{q}$, $\mathbf{y}\in R^{\ell}_{q}$, $c\in R_{q}$, where $\ell=L/n$ and $k=K/n$.
- To further reduce the size of the public key, the matrix \bf{A} is pseudorandomly derived from a 256-bit public seed ρ , which is included in the ML-DSA public key in place of A.
- For a still further reduction in public key size, the ML-DSA public key substitutes a compressed value t_1 for t, which drops the d low-order bits of each coefficient.
- To obtain beyond unforgeability (BUFF) properties noted in [\[14\]](#page-57-0), ML-DSA does not directly sign the message M but rather signs a message representative μ that is obtained by hashing the concatenation of a hash of the public key and M .
- To reduce signature size, rather than including the commitment $w_{Approx} = Ay + y_2$ in the signature, the ML-DSA signature uses a rounded version of $w = Ay$ as a commitment w_1 and includes only the hash \tilde{c} of $\mathbf{w}_1||\mu$.
- To ensure that w_1 can be reconstructed by the verifier from z and the compressed value t_1 , the signature must also include a *hint* $\mathbf{h} \in R_2^k$ computed by the signer using the signer's private key.
- Additionally, to ensure correctness, a second stage of rejection sampling must be included (Line [28](#page-34-1) of Algorithm [7\)](#page-34-1)

In this document, the abbreviations ML-DSA-44, ML-DSA-65, and ML-DSA-87 are used to refer to ML-DSA with the parameter choices given in Table [1.](#page-24-1) In these abbreviations, the numerical suffix refers to the dimension of the matrix A. For example, in ML-DSA-65, the matrix A is a 6×5 matrix over R_a .

3.4 Hedged and Deterministic Signing

For ML-DSA to be secure, the signer's commitment value y must not be used to sign more than one message, and it must not be easily guessed by an attacker. This requires randomness. In principle, the randomness leading to y can be produced either with the use of fresh randomness at signing time or pseudorandomly from the message being signed and a precomputed random value included in the signer's private key.

By default, this standard specifies the signing algorithm to use both types of randomness. This is referred to as the "hedged" variant of the signing procedure. The use of fresh randomness during signing helps mitigate side-channel attacks, while the use of precomputed randomness protects against the possibility that there may be flaws in the random number generator used by the signer at signing time.

This document also permits a fully deterministic variant of the signing procedure in case the signer has no access to a fresh source of randomness at signing time. However, the lack of randomness in the deterministic variant makes the risk of side-channel attacks (particularly fault attacks) more difficult to mitigate. Therefore, this variant**should not** be used on platforms where side-channel attacks are a concern and where they cannot be otherwise mitigated.

Only implementing the hedged variant (i.e., without the deterministic variant) is sufficient to guarantee interoperability. The same verification algorithm will work to verify signatures produced by either variant, so implementing the deterministic variant in addition to the hedged variant does not enhance interoperability. The hedged and deterministic signing procedure differ only at line [5](#page-27-2) of Algorithm [2](#page-27-2) and line [5](#page--1-3) of Algorithm [4.](#page--1-3)

3.5 Use of Digital Signatures

Secure key management is an essential requirement for the use of digital signatures. This is contextdependent and involves more than the key generation, signing, and signature verification algorithms in this document. Guidance for key management is provided in the SP 800-57 series [\[9,](#page-56-9) [17,](#page-57-3) [18\]](#page-57-4).

Digital signatures are most useful when bound to an identity. Binding a public key to an identity requires proof of possession of the private key. In the PKI context, issuing certificates involves assurances of identity and proof of possession. When a public-key certificate is not available, users of digital signatures should determine whether a public key needs to be bound to an identity. Methods for obtaining assurances of identity and proof of possession are provided in [\[3\]](#page-56-3).

3.6 Additional Requirements

This section describes several required assurances when implementing ML-DSA. These are in addition to the considerations in Section [3.5.](#page-20-1)

3.6.1 Randomness Generation

Algorithm [1,](#page-26-3) implementing key generation for ML-DSA, uses an RBG to generate the 256-bit random seed . The seed **shall** be a fresh (i.e., not previously used) random value generated using an **approved** RBG, as prescribed in SP 800-90A, SP 800-90B, and SP 800-90C [\[19,](#page-57-5) [20,](#page-57-6) [21\]](#page-57-7). Moreover, the RBG used **shall** have a security strength of at least 192 bits for ML-DSA-65 and 256 bits for ML-DSA-87. For ML-DSA-44, the RBG **should** have a security strength of at least 192 bits and **shall** have a security strength of at least 128 bits. If an **approved** RBG with at least 128 bits of security but less than 192 bits of security is used, then the claimed security strength of ML-DSA-44 is reduced from category 2 to category 1.

Additionally, the value rnd is generated using an RBG in the default "hedged" variants of Algorithms [2](#page-27-2) and [4](#page--1-3) for ML-DSA and HashML-DSA signing, respectively. While this value **should** ideally be generated by an **approved** RBG, other methods for generating fresh random values may be used. The primary purpose of rnd is to facilitate countermeasures to side-channel attacks and fault attacks on deterministic signatures, such as [\[22,](#page-57-8) [23,](#page-57-9) [24\]](#page-57-10).^{[3](#page-21-4)} For this purpose, even a weak RBG may be preferable to the fully deterministic variants of Algorithms [2](#page-27-2) and [4.](#page--1-3)

3.6.2 Public-Key and Signature Length Checks

Algorithm [3,](#page-27-3) implementing verification for ML-DSA, and Algorithm [5,](#page--1-4) implementing verification for HashML-DSA, specify the length of the signature σ and the public key pk in terms of the parameters described in Table [1.](#page-24-1) If an implementation of ML-DSA can accept inputs for σ or pk of any other length, it **shall** return false whenever the lengths of either of these inputs differ from their lengths specified in this standard. Failing to check the length of pk or σ may interfere with the security properties that ML-DSA is designed to have, like strong unforgeability.

3.6.3 Intermediate Values

The data used internally by the key generation and signing algorithms in intermediate computation steps could be used by an adversary to gain information about the private key and thereby compromise security. The data used internally by verification algorithms is similarly sensitive for some applications, including the verification of signatures that are used as bearer tokens (i.e., authentication secrets) or the verification of signatures on plaintext messages that are intended to be confidential. Intermediate values of the verification algorithm may reveal information about its inputs (i.e., the message, signature, and public key), and in some applications, security or privacy requires one or more of these inputs to be confidential. Therefore, implementations of ML-DSA **shall** ensure that any potentially sensitive intermediate data is destroyed as soon as it is no longer needed.

Two particular cases in which implementations may refrain from destroying intermediate data are:

[1](#page-26-3). The seed ξ generated in step 1 of [ML-DSA](#page-26-3). KeyGen can be stored for the purpose of later expansion

 3 In addition, when signing is deterministic, there is leakage through timing side channels of information about the message but not the private key). If the signer does not want to reveal the message being signed, hedged signatures should be used (see Section 3.2 in $[6]$).

using [ML-DSA](#page-32-1). KeyGen internal. As the seed can be used to compute the private key, it is sensitive data and **shall** be treated with the same safeguards as a private key.

2. The matrix \overline{A} generated in step [3](#page-32-1) of [ML-DSA](#page-32-1).KeyGen internal can be stored so that it need not be recomputed in later operations. Likewise, the matrix \bf{A} generated in step [5](#page-36-0) of the verification algorithm [ML-DSA](#page-36-0). Verify internal can also be stored. In either case, the matrix \bf{A} is data that is easily computed from the public key and does not require any special protections.

In certain situations (e.g., deterministic signing and the verification of confidential messages and signatures), additional care must be taken to protect implementations against side-channel attacks or fault attacks. A cryptographic device may leak critical information through side channels, which allows internal data or keying material to be extracted without breaking the cryptographic primitives.

3.6.4 No Floating-Point Arithmetic

Implementations of ML-DSA **shall not** use floating-point arithmetic, as rounding errors in floating point operations may lead to incorrect results in some cases. Either $|x/y|$ or $[x/y]$ is used in all pseudocode in this standard in which division is performed (e.g., x/y), and y may not divide x. Both of these can be computed without floating-point arithmetic, as ordinary integer division x/y computes $|x/y|$, and $\lceil x/y \rceil = \lceil (x + y - 1)/y \rceil$ for non-negative integers x and positive integers y. If y is a power of two, it may be more efficient to use bit shift operations than integer division.

3.7 Use of Symmetric Cryptography

This standard makes use of the functions SHAKE256 and SHAKE128, as defined in FIPS 202 [\[7\]](#page-56-7). While FIPS 202 specifies these functions as inputting and outputting bit strings, most implementations treat inputs and outputs as byte strings.

This standard will always call these functions with an output length of a multiple of eight bits and treat the output of these functions as a byte string, which will be the same byte string that would result from taking the bit string expected from a literal reading of FIPS 202 and processing it with [BitsToBytes](#page--1-8). However, to allow the signing of messages that are not a whole number of bytes, this document will overload SHAKE256 so that its input may be a bit string but will usually be a byte string. The following equivalence will hold for any byte string str and integer $\ell \geq 1$:

$$
SHAKE256(str, 8\ell) = SHAKE256(BytesToBits(str), 8\ell).
$$

In addition to using a mostly byte-oriented variant of the API defined in FIPS 202 for SHAKE256 and SHAKE128, this standard sometimes makes use of the incremental API defined in SP 800-185 [\[25\]](#page-57-11). This API consists of three functions for each variant of SHAKE. These functions can be used to absorb a sequence of arbitrary-length strings and squeeze a sequence of arbitrary-length strings. These functions perform buffering to handle any incomplete data blocks while absorbing or squeezing. For example, for SHAKE256:

- $ctx \leftarrow$ SHAKE256.Init() Initializes a hash function context.
- $ctx \leftarrow$ SHAKE256.Absorb(ctx, str) Injects data to be used in the absorbing phase of SHAKE256 and updates context ctx.

• $(\text{ctx}, \text{out}) \leftarrow \text{SHAKE256.Squeeze}(\text{ctx}, 8\ell)$ Extracts ℓ output bytes produced during the squeezing phase of SHAKE256 and updates context ctx.

While the above functions are specified in terms of the Keccak- f permutation rather than the eXtendable-Output Function (XOF), SHAKE256, they are defined so that any series of commands of the following form:

- 1. $ctx \leftarrow SHAKE256.Init()$
- 2. **For** $i = 1$ to m : ctx \leftarrow SHAKE256.Absorb(ctx, str_i)
- 3. **For** $j = 1$ to k : (ctx, out_i) \leftarrow SHAKE256.Squeeze(ctx, $8b_j$)
- 4. output \leftarrow out₁ $||...||$ out_k

will yield the same output as a single SHAKE256 call:

output ← SHAKE256(str₁|| ... ||str_m, $8b_1 + ... + 8b_k$).

This equivalence holds whether or not $|\text{str}_i|$ and b_i are multiples of the SHAKE256 block length.

Since all outputs of SHAKE128 and SHAKE256 in this document give a whole number of bytes, the wrapper functions [H](#page-22-1) and [G](#page-22-1) are defined as follows:

- 1. [H](#page-22-1)(str, ℓ) = SHAKE256(str, 8ℓ)
- 2. $G(str, \ell) = SHAKE128(str, 8\ell)$ $G(str, \ell) = SHAKE128(str, 8\ell)$
- 3. $H .Init() = SHAKE256.Init()$
- 4. $G .Init() = SHAKE128.Init()$ $G .Init() = SHAKE128.Init()$ $G .Init() = SHAKE128.Init()$
- 5. [H.Absorb](#page-22-1)(ctx, str) = SHAKE256.Absorb(ctx, str)
- 6. G.[Absorb](#page-22-1)(ctx, str) = SHAKE128.Absorb(ctx, str)
- 7. [H.Squeeze](#page-22-1)(ctx, ℓ) = SHAKE256.Squeeze(ctx, 8ℓ)
- 8. G.[Squeeze](#page-22-1)(ctx, ℓ) = SHAKE128.Squeeze(ctx, 8ℓ)

In addition to SHAKE128 and SHAKE256, [HashML-DSA](#page--1-3).Sign and [HashML-DSA](#page--1-4).Verify may call other **approved** hash functions for pre-hashing. The pseudocode in this standard also treats these functions as returning a byte string as output while supporting either a bit string or a byte string as input. Here, it should be noted that the hash functions defined in $[8]$ use different rules (i.e., big-endian ordering) to relate bits, bytes, and words.

4. Parameter Sets

Table 1. ML-DSA parameter sets

Three ML-DSA parameter sets are included in Table [1.](#page-24-1) Each parameter set assigns values for all of the parameters used in the ML-DSA algorithms for key generation, signing, and verification. For informational purposes, some parameters used in the analysis of these algorithms are also included in the table. In particular, "repetitions" refers to the expected number of repetitions of the main loop in the signing algorithm from eq. 5 in [\[5\]](#page-56-5). The names of the parameter sets are of the form "ML-DSA- $k\ell$," where (k, ℓ) are the dimensions of the matrix \bf{A} .

These parameter sets were designed to meet certain security strength categories defined by NIST in its original Call for Proposals [\[26\]](#page-57-12). These security strength categories are explained further in SP 800-57, Part 1 [\[9\]](#page-56-9).

Using this approach, security strength is not described by a single number, such as "128 bits of security." Instead, each ML-DSA parameter set is claimed to be at least as secure as a generic block cipher with a prescribed key size or a generic hash function with a prescribed output length. More precisely, it is claimed that the computational resources needed to break ML-DSA are greater than or equal to the computational resources needed to break the block cipher or hash function when these computational resources are estimated using any realistic model of computation. Different models of computation can be more or less realistic and, accordingly, lead to more or less accurate estimates of security strength. Some commonly studied models are discussed in [\[27\]](#page-58-0).

Concretely, the parameter set ML-DSA-44 is claimed to be in security strength category 2, ML-DSA-65 is claimed to be in category 3, and ML-DSA-87 is claimed to be in category 5 [\[6\]](#page-56-6). For additional discussion of the security strength of MLWE-based cryptosystems, see [\[28\]](#page-58-1).

The sizes of keys and signatures that correspond to each parameter set are given in Table [2.](#page-25-0) Certain optimizations are possible when storing ML-DSA public and private keys. If additional space is available, one can precompute and store $\bf A$ to speed up signing and verifying. Alternatively, if one wants to reduce the space needed for the private key, one can store only the 32-byte seed ξ , which is sufficient to generate the other parts of the private key. For additional details, see Section 3.1 in [\[6\]](#page-56-6).

	Private Key	Public Key	Signature Size
ML-DSA-44	2560	1312	2420
ML-DSA-65	4032	1952	3309
ML-DSA-87	4896	2592	4627

Table 2. Sizes (in bytes) of keys and signatures of ML-DSA

5. External Functions

The signing, verifying, and key generation functions can be split into "external" and "internal" components to simplify APIs and Cryptographic Algorithm Validation Program (CAVP) testing. The external components generate randomness and perform various checks before calling their internal counterparts. The internal components are deterministic and can assume that the external components did not encounter error conditions.

The distinction between external and internal functions also simplifies the presentation of algorithms for signing and verification by grouping the operations that are shared between [ML-DSA](#page-27-2). Sign and [HashML-DSA](#page--1-3).Sign in [ML-DSA](#page-34-1).Sign_internal and grouping the operations that are shared between [ML-DSA](#page-27-3).Verify and [HashML-DSA](#page--1-4).Verify in [ML-DSA](#page-36-0).Verify_internal.

5.1 ML-DSA Key Generation

The key generation algorithm [ML-DSA](#page-26-3).KeyGen takes no input and outputs a public key and a private key, which are both encoded as byte strings.

The algorithm uses an **approved** RBG to generate a 256-bit (32-byte) random seed ξ that is given as input to [ML-DSA](#page-32-1).KeyGen_internal (Algorithm [6\)](#page-32-1), which produces the public and private keys.

Algorithm 1 [ML-DSA](#page-26-3).KeyGen()

Generates a public-private key pair.

```
Output: Public key pk \in \mathbb{B}^{32+32k(\text{bitten } (q-1)-d)}
```
and private key $sk \in \mathbb{B}^{32+32+64+32\cdot((\ell+k)\cdot \text{bitlen}(2\eta)+dk)}$.

1: $\xi \leftarrow \mathbb{B}^{32}$

```
2: if \xi = NULL then
```
return ⊥ **beturn** an error indication if random bit generation failed

 \triangleright choose random seed

```
4:
end if
```
3:

5: **return** [ML-DSA](#page-32-1).KeyGen_internal (ξ)

5.2 ML-DSA Signing

The signing algorithm [ML-DSA](#page-27-2).Sign (Algorithm [2\)](#page-27-2) takes a private key, a message, and a context string as input^{[4](#page-26-4)}. It outputs a signature that is encoded as a byte string.

For the default "hedged" version of ML-DSA signing, the algorithm (at line [5\)](#page-27-2) uses an **approved** RBG to generate a 256-bit (32-byte) random seed $rnd.$ If the deterministic variant is desired, then rnd is set to the fixed zero string $\{0\}^{32}$. The value rnd , the private key, and the encoded message are input to [ML-DSA](#page-34-1).Sign_internal (Algorithm [7\)](#page-34-1), which produces the signature.

 $4By$ default, the context is the empty string, though applications may specify the use of a non-empty context string.

Algorithm 2 [ML-DSA](#page-27-2).Sign(sk, M, ctx)

Generates an ML-DSA signature.

```
Input: Private key sk \in \mathbb{B}^{32+32+64+32 \cdot ((\ell+k) \cdot \text{bitlen}(2\eta)+dk)}, message M \in \{0,1\}^*,
context string ctx (a byte string of 255 or fewer bytes).
Output: Signature \sigma \in \mathbb{B}^{\lambda/4+\ell \cdot 32 \cdot (1+\text{bitlen}(\gamma_1-1))+\omega+k}1: if |ctx| > 255 then
 2: return ⊥ b return an error indication if the context string is too long
 3: end if
 4:
 5: rnd \leftarrow \mathbb{B}^{32} > for the optional deterministic variant, substitute rnd \leftarrow \{0\}^{32}6: if rnd = NULL then
 7: return ⊥ b return an error indication if random bit generation failed
 8: end if
 9:
10: M' \leftarrowBytesToBitsIntegerToBytes(0, 1) || IntegerToBytes(|ctx|, 1) || ctx) || M11: ML-DSA.Sign\_internal}(sk, M', rnd)12: return \sigma
```
5.3 ML-DSA Verifying

The verification algorithm [ML-DSA](#page-27-3).Verify (Algorithm [3\)](#page-27-3) takes a public key, a message, a signature, and a context string as input. The public key, signature, and context string are all encoded as byte strings, while the message is a bit string. [ML-DSA](#page-27-3).Verify outputs a Boolean value that is true if the signature is valid with respect to the message and the public key and false if the signature is invalid. The verification is accomplished by calling [ML-DSA](#page-36-0). Verify internal (Algorithm [8\)](#page-36-0) with the public key, the encoded message, and the signature.

```
Algorithm 3ML-DSA.Verify(pk, M, \sigma, ctx)
```

```
Verifies a signature \sigma for a message M.
Input: Public key pk \in \mathbb{B}^{32+32k(\text{bitlen } (q-1)-d))}, message M \in \{0,1\}^*,
signature \sigma \in \mathbb{B}^{\lambda/4+\ell \cdot 32 \cdot (1+\textsf{bitlen}\,(\gamma_1-1))+\omega+k},context string ctx (a byte string of 255 or fewer bytes).
Output: Boolean.
 1: if |ctx| > 255 then
 2: return ⊥ beturn 2: return an error indication if the context string is too long
 3: end if
 4:
 5: M' \leftarrowBytesToBitsIntegerToBytes(0, 1) || IntegerToBytes(|ctx|, 1) || ctx) || M6: returnML-DSA. Verify_internal(pk, M', \sigma)
```
5.4 Pre-Hash ML-DSA

For some cryptographic modules that generate ML-DSA signatures, hashing the message in step [6](#page-34-1) of [ML-DSA](#page-34-1). Sign internal may result in unacceptable performance if the message M is large. For example, the platform may require hardware support for hashing to achieve acceptable performance but lack hardware support for SHAKE256 specifically. For some use cases, this may be addressed by signing a digest of the message along with some domain separation information rather than signing the message directly. This version of ML-DSA is known as "pre-hash" ML-DSA or HashML-DSA . In general, the "pure" ML-DSA version is preferred.

While key generation for HashML-DSA is the same as for ML-DSA, it is not the same for the signing algorithm [HashML-DSA](#page--1-3).Sign or the verification algorithm [HashML-DSA](#page--1-4).Verify. Like ML-DSA, the signing algorithm of HashML-DSA takes the content to be signed, the private key, and a context as input, as well as a hash function or XOF that is to be used to pre-hash the content to be signed. The context string has a maximum length of 255 bytes. By default, the context is the empty string, though applications may specify the use of a non-empty context string.

The identifier for a signature (e.g., the object identifier [OID]) **should** indicate whether the signature is a ML-DSA signature or a pre-hash HashML-DSA signature. In the case of pre-hash signatures, the identifier should also indicate the hash function or XOF used to compute the pre-hash. ^{[5](#page--1-13)} While a single key pair may be used for both ML-DSA and HashML-DSA signatures, it is recommended that each key pair only be used for one version or the other. If a non-empty context string is to be used, this should either be indicated by the signature's identifier or by the application with which the signature is being used.

If the default "hedged" variant of is used, the 32-byte random value **shall** be generated by the cryptographic module that generates the signature (i.e., that runs [ML-DSA](#page-34-1).Sign_internal). However, all other steps of signing may be performed outside of the cryptographic module that generates the signature. In the case of pre-hashing, the hash or XOF of the content to be signed must be computed within a FIPS 140-validated cryptographic module, but it may be a different cryptographic module than the one that generates the signature.

If the content to be signed is large, hashing of the content is often performed at the application level. For example, in the Cryptographic Message Syntax [\[29\]](#page-58-2), a digest of the content may be computed, and that digest is signed along with other attributes. If the content is not hashed at the application level, the pre-hash version of ML-DSA signing may be used.

In order to maintain the same level of security strength when the content is hashed at the application level or using HashML-DSA , the digest that is signed needs to be generated using an **approved** hash function or XOF (e.g., from FIPS 180 [\[8\]](#page-56-8) or FIPS 202 [\[7\]](#page-56-7)) that provides at least λ bits of classical security strength against both collision and second preimage attacks [\[7,](#page-56-7) Table 4].^{[6](#page--1-12)} The verification of a signature that is created in this way will require the verify function to generate a digest from the message in the same way to be used as input for the verification function.

5.4.1 HashML-DSA Signing and Verifying

In the Hash[ML-DSA](#page-34-1) version, the message input to $ML-DSA.Sign$ internal is the result of applying either a hash function or a XOF to the content to be signed. The output of the hash function or XOF is prepended by a one-byte domain separator, one byte that indicates the length of the context string, the context string, and the distinguished encoding rules (DER) encoding of the hash function or XOF's OID. The domain separator has a value of one for "pre-hash" signing. The DER encoding of the OID includes the tag and length.

⁵In the case of a XOF this would also include the length of the output from the XOF.

 6 Obtaining at least λ bits of classical security strength against collision attacks requires that the digest to be signed be at least 2λ bits in length.

Algorithm [4](#page--1-3) shows the DER encodings of the OIDs for SHA-256, SHA-512, and SHAKE128. However, it may be used with other hash functions or XOFs.

Algorithm 4 [HashML-DSA](#page--1-3).Sign(sk, M, ctx , PH)

```
Generate a "pre-hash" ML-DSA signature.
```

```
Input: Private key sk \in \mathbb{B}^{32+32+64+32 \cdot ((\ell+k) \cdot \text{bitten} (2\eta)+dk)}, message M \in \{0,1\}^*,
```

```
context string ctx (a byte string of 255 or fewer bytes), pre-hash function PH.
```

```
Output: ML-DSA signature \sigma \in \mathbb{B}^{\lambda/4+\ell \cdot 32 \cdot (1+\text{bitlen}(\gamma_1-1))+\omega+k}.
```

```
1: if |ctx| > 255 then
 2: return ⊥ b return an error indication if the context string is too long
 3: end if
 4:
 5: rnd \leftarrow \mathbb{B}^{32} \triangleright for the optional deterministic variant, substitute rnd \leftarrow \{0\}^{32}6: if rnd = NULL then
 7: return ⊥ b return an error indication if random bit generation failed
 8: end if
 9:
10: switch PH do
11: case SHA-256:
12: OID \leftarrow 0x06, 0x09, 0x60, 0x86, 0x48, 0x01, 0x65, 0x03, 0x04, 0x02, 0x01> 2.16.840.1.101.3.4.2.113: PH<sub>M</sub> \leftarrow SHA256(M)
14: case SHA-512:
15: OID ← 0x06, 0x09, 0x60, 0x86, 0x48, 0x01, 0x65, 0x03, 0x04, 0x02, 0x03
                                                                        > 2.16.840.1.101.3.4.2.3
16: PH<sub>M</sub> \leftarrow SHA512(M)17: case SHAKE128:
18: OID \leftarrow 0x06, 0x09, 0x60, 0x86, 0x48, 0x01, 0x65, 0x03, 0x04, 0x02, 0x08> 2.16.840.1.101.3.4.2.11
19: PH<sub>M</sub> \leftarrow SHAKE128(M, 256)
20: case …
21: …
22: end switch
23: M' \leftarrowBytesToBitsIntegerToBytes(1, 1) || IntegerToBytes(|ctx|, 1) || ctx || OID || PH<sub>M</sub>)
24: \sigma \leftarrow \text{ML-DSA}. Sign internal(sk, M', rnd)
25: return \sigma
```
Algorithm [5](#page--1-4) presents the signature verification for HashML-DSA . This function constructs M' in the same way as Algorithm [4](#page--1-3) and passes the resulting M' to Algorithm [ML-DSA](#page-36-0). Verify internal for verification. As with the pre-hash signature generation, M' may be constructed outside of the cryptographic module that performs [ML-DSA](#page-36-0). Verify internal. However, in the case of HashML-DSA, the hash or XOF of the content must be computed within a FIPS 140-validated cryptographic module, which may be a different cryptographic module than the one that performs [ML-DSA](#page-36-0). Verify internal.

As noted in Section [5.4,](#page-27-1) the identifier associated with the signature should indicate whether ML-DSA or

the pre-hash version HashML-DSA of signature verification should be used, as well as the hash function or XOF to be used to compute the pre-hash. A non-empty context string should be used in verification if one is specified either in the signature's identifier or by the application with which the signature is being used.

```
Algorithm 5HashML-DSA.Verify(pk, M, \sigma, ctx, \text{PH})
```
Verifies a pre-hash HashML-DSA signature. **Input**: Public key $pk \in \mathbb{B}^{32+32k(\text{bitlen } (q-1)-d))}$, message $M \in \{0,1\}^*$, signature $\sigma \in \mathbb{B}^{\lambda/4+\ell \cdot 32 \cdot (1+\textsf{bitlen}\,(\gamma_1-1))+\omega+k},$ context string ctx (a byte string of 255 or fewer bytes), pre-hash function PH. **Output**: Boolean. 1: **if** $|ctx| > 255$ then 2: **return** false 3: **end if** 4: 5: **switch** PH **do** 6: **case** SHA-256: 7: ○ OID ← 0x06, 0x09, 0x60, 0x86, 0x48, 0x01, 0x65, 0x03, 0x04, 0x02, 0x01 $>$ 2.16.840.1.101.3.4.2.1 8: PH_M \leftarrow SHA256(*M*) 9: **case** SHA-512: 10: $OID \leftarrow 0x06, 0x09, 0x60, 0x86, 0x48, 0x01, 0x65, 0x03, 0x04, 0x02, 0x03$ $>$ 2.16.840.1.101.3.4.2.3 11: PH_M \leftarrow SHA $512(M)$ 12: **case** SHAKE128: 13: $OID \leftarrow 0x06, 0x09, 0x60, 0x86, 0x48, 0x01, 0x65, 0x03, 0x04, 0x02, 0x08$ $> 2.16.840.1.101.3.4.2.11$ 14: PH_M ← SHAKE128(M, 256) 15: **case** … 16: … 17: **end switch** 18: $M' \leftarrow$ [BytesToBits](#page--1-9)([IntegerToBytes](#page--1-7)(1, 1) || IntegerToBytes($|ctx|, 1)$ || ctx || OID || PH_M) 19: **return** [ML-DSA](#page-36-0). Verify_internal (pk, M', σ)

6. Internal Functions

This section describes the functions for ML-DSA key generation, signature generation, and signature verification. Where randomness is required, the random values are provided as inputs to the functions. The interfaces specified in this section will be used when testing of ML-DSA implementations is performed through the CAVP.

Other than for testing purposes, the interfaces for key generation and signature generation specified in this section **should not** be made available to applications, as any random values required for key generation and signature generation **shall** be generated by the cryptographic module. Section [5](#page-26-0) provides guidance on the interfaces to be made available to applications.^{[7](#page-31-2)}

6.1 ML-DSA Key Generation (Internal)

The internal key generation algorithm [ML-DSA](#page-32-1).KeyGen_internal takes a 32-byte random seed as input and outputs a public key and a private key that are both encoded as byte strings.

The seed ζ is expanded as needed using an XOF (i.e., a byte-variant of [SHAKE256\)](#page-22-1) denoted by [H](#page-22-1) to produce other random values.^{[8](#page-31-3)} In particular:

- A 32 -byte public random seed $\rho.$ Using this seed, a polynomial matrix ${\bf A}\in R^{k\times \ell}_q$ is pseudorandomly sampled^{[9](#page-31-4)} from $R_q^{k\times \ell}$.
- A 64-byte private random seed ρ' . Using this seed, the polynomial vectors $\mathbf{s}_1\in R^{\ell}_q$ and $\mathbf{s}_2\in R^k_q$ are pseudorandomly sampled from the subset of polynomial vectors whose coordinate polynomials have short coefficients (i.e., in the range $[-\eta, \eta]$).
- A 32-byte private random seed K for use during signing.

The core cryptographic operation computes the public value

$$
\mathbf{t} = \mathbf{A}\mathbf{s}_1 + \mathbf{s}_2.
$$

The vector t together with the matrix A may be considered an expanded form of the public key. The vector t is compressed in the actual public key by dropping the d least significant bits from each coefficient, thus producing the polynomial vector t_1 . This compression is an optimization for performance, not security. The low-order bits of t can be reconstructed from a small number of signatures and, therefore, need not be regarded as secret.

The ML-DSA public key pk is a byte encoding of the public random seed ρ and the compressed polynomial vector t_1 .

The ML-DSA private key sk is a byte encoding of the public random seed ρ , a private random seed K for use during signing, a 64-byte hash tr of the public key for use during signing, the secret polynomial

 $⁷$ In some cases, it is permissible to modify the format of the private key in these interfaces (see Sections [4](#page-24-0) and [3.6.3.](#page-21-3))</sup> ⁸Single-byte encodings of the parameters k and ℓ are included in the XOF input for domain separation. For implementations that use the seed in place of the private key, this ensures that the expansion will produce an unrelated key if the seed is mistakenly expanded using a parameter set other than the one originally intended.

[̂] 9 More precisely, since only the NTT form of ${\bf A}$, $\hat{\bf A}\in T^{k\times \ell}_q=$ NTT $({\bf A})$ is needed in subsequent calculations, the code actually computes $\hat{\bf A}$ as a pseudorandom sample over $T^{k\times \ell}_q$, and the sampling of ${\bf A}=$ NTT $^{-1}(\hat{\bf A})$ is only implicit (i.e., it could be computed but is not).

vectors s_1 and s_2 , and a polynomial vector t_0 encoding the d least significant bits of each coefficient of the uncompressed public-key polynomial t.

Algorithm 6 [ML-DSA](#page-32-1).KeyGen internal(ξ) *Generates a public-private key pair from a seed.* **Input**: Seed $\mathcal{E} \in \mathbb{B}^{32}$ **Output**: Public key $pk \in \mathbb{B}^{32+32k(\text{bitlen } (q-1)-d)}$ and private key $sk \in \mathbb{B}^{32+32+64+32\cdot((\ell+k)\cdot \text{bitten}\,(2\eta)+dk)}$. 1: $(\rho, \rho', K) \in \mathbb{B}^{32} \times \mathbb{B}^{64} \times \mathbb{B}^{32} \leftarrow H(\xi||\text{IntegerToBytes}(k, 1)||\text{IntegerToBytes}(\ell, 1), 128)$ $(\rho, \rho', K) \in \mathbb{B}^{32} \times \mathbb{B}^{64} \times \mathbb{B}^{32} \leftarrow H(\xi||\text{IntegerToBytes}(k, 1)||\text{IntegerToBytes}(\ell, 1), 128)$ $(\rho, \rho', K) \in \mathbb{B}^{32} \times \mathbb{B}^{64} \times \mathbb{B}^{32} \leftarrow H(\xi||\text{IntegerToBytes}(k, 1)||\text{IntegerToBytes}(\ell, 1), 128)$ $(\rho, \rho', K) \in \mathbb{B}^{32} \times \mathbb{B}^{64} \times \mathbb{B}^{32} \leftarrow H(\xi||\text{IntegerToBytes}(k, 1)||\text{IntegerToBytes}(\ell, 1), 128)$ $(\rho, \rho', K) \in \mathbb{B}^{32} \times \mathbb{B}^{64} \times \mathbb{B}^{32} \leftarrow H(\xi||\text{IntegerToBytes}(k, 1)||\text{IntegerToBytes}(\ell, 1), 128)$ 2:
 $\hat{A} \leftarrow \text{ExpandA}(\rho)$ $\hat{A} \leftarrow \text{ExpandA}(\rho)$ $\hat{A} \leftarrow \text{ExpandA}(\rho)$ \Rightarrow A is generated and stored in NTT representation as \hat{A} 4: $(\mathbf{s}_1, \mathbf{s}_2) \leftarrow \mathsf{ExpandS}(\rho')$ $(\mathbf{s}_1, \mathbf{s}_2) \leftarrow \mathsf{ExpandS}(\rho')$ $(\mathbf{s}_1, \mathbf{s}_2) \leftarrow \mathsf{ExpandS}(\rho')$ 5: $\mathbf{t} \leftarrow \text{NTT}^{-1}(\hat{\mathbf{A}} \circ \text{NTT}(\mathbf{s}_1)) + \mathbf{s}_2$ $\mathbf{t} \leftarrow \text{NTT}^{-1}(\hat{\mathbf{A}} \circ \text{NTT}(\mathbf{s}_1)) + \mathbf{s}_2$ $\mathbf{t} \leftarrow \text{NTT}^{-1}(\hat{\mathbf{A}} \circ \text{NTT}(\mathbf{s}_1)) + \mathbf{s}_2$ \triangleright compute $\mathbf{t} = \mathbf{A}\mathbf{s}_1 + \mathbf{s}_2$ 6: $(\mathbf{t}_1, \mathbf{t}_0) \leftarrow \text{Power2Round}(\mathbf{t})$ $(\mathbf{t}_1, \mathbf{t}_0) \leftarrow \text{Power2Round}(\mathbf{t})$ $(\mathbf{t}_1, \mathbf{t}_0) \leftarrow \text{Power2Round}(\mathbf{t})$ \triangleright compress t 7: \triangleright PowerTwoRound is applied componentwise (see explanatory text in Section [7.4\)](#page-48-0) 8: $pk \leftarrow$ [pkEncode](#page-42-1)(ρ, t_1) 9: $tr \leftarrow H(pk, 64)$ $tr \leftarrow H(pk, 64)$ $tr \leftarrow H(pk, 64)$ 10: $sk \leftarrow \text{skEncode}(\rho, K, tr, s_1, s_2, t_0)$ $sk \leftarrow \text{skEncode}(\rho, K, tr, s_1, s_2, t_0)$ $sk \leftarrow \text{skEncode}(\rho, K, tr, s_1, s_2, t_0)$ $\triangleright K$ and tr are for use in signing 11: **return** $(\mathit{pk}, \mathit{sk})$

6.2 ML-DSA Signing (Internal)

[ML-DSA](#page-34-1). Sign_internal (Algorithm [7\)](#page-34-1) outputs a signature encoded as a byte string. It takes a private key sk encoded as a byte string, a formatted message M' encoded as a bit string, and a 32-byte string rnd as input. There are two ways that a signing algorithm can use [ML-DSA](#page-34-1).Sign internal: "hedged" and "deterministic." The default "hedged" variants of [ML-DSA](#page-27-2).Sign and [HashML-DSA](#page--1-3).Sign use a fresh random value for rnd , while the optional deterministic variants use the constant byte string $\{0\}^{32}$ (see Section [3\)](#page-18-0).

In both variants, the signer first extracts the following from the private key: the public random seed ρ , the 32-byte private random seed K, the 64-byte hash of the public key tr , the secret polynomial vectors s_1 and s_2 , and the polynomial vector t_0 encoding the d least significant bits of each coefficient of the uncompressed public-key polynomial t. ρ is then expanded to the same matrix **A** as in key generation.

Before the message M is signed, it is concatenated with the public-key hash tr and hashed down to a 64-byte message representative μ using [H.](#page-22-1)

The signer produces an additional 64-byte seed ρ'' for private randomness during each signing operation. ρ'' is computed as $\rho'' \leftarrow H(K||rnd||\mu, 64)$ $\rho'' \leftarrow H(K||rnd||\mu, 64)$ $\rho'' \leftarrow H(K||rnd||\mu, 64)$. In the default hedged variant, rnd is the output of an RBG, while in the deterministic variant, rnd is a 32-byte string that consists entirely of zeros. This is the only difference between the deterministic and hedged variant of [ML-DSA](#page-27-2).Sign.

The main part of the signing algorithm consists of a rejection sampling loop in which each iteration of the loop either produces a valid signature or an invalid signature whose release would leak information about the private key. The loop is repeated until a valid signature is produced, which can then be encoded as a byte string and output.^{[10](#page-32-2)} The rejection sampling loop follows the Fiat-Shamir With Aborts paradigm [\[10\]](#page-56-10)

 10 Implementations may limit the number of iterations in this loop to not exceed a finite maximum value. If this

and (aside from the rejection step) is similar in structure to Schnorr signatures [\[30\]](#page-58-3) (e.g., EdDSA [\[31\]](#page-58-4)). The signer first produces a "commitment" w_1 and then pseudorandomly derives a "challenge" c from w_1 and the message representative μ . Finally, the signer computes a response z.

In more detail, the main computations involved in the rejection sampling loop are as follows:

- Using the [ExpandMask](#page-47-2) function (Algorithm [34\)](#page-47-2), the seed ρ'' , and a counter κ , a polynomial vector $\mathbf{y} \in R^{\ell}_q$ is pseudorandomly sampled from the subset of polynomial vectors whose coefficients are moderately short (i.e., in the range $[-\gamma_1 + 1, \gamma_1]$).
- From y, the signer computes the commitment w_1 by computing $w = Ay$ and then rounding to a nearby multiple of $2\gamma_2$ using [HighBits](#page-49-2) (Algorithm [37\)](#page-49-2).
- w_1 and μ are concatenated and hashed to produce the commitment hash \tilde{c} . This uses the function [w1Encode](#page-44-2) (Algorithm [28\)](#page-44-2). The byte string \tilde{c} is used to pseudorandomly sample a polynomial $c \in R_a$ that has coefficients in $\{-1, 0, 1\}$ and Hamming weight τ . The sampling is done with the function [SampleInBall](#page-45-1) (Algorithm [29\)](#page-45-1).^{[11](#page-33-0)}
- The signer computes the response $z = y + cs_1$ and performs various validity checks. If any of the checks fails, the signer will continue the rejection sampling loop.
- If the checks pass, the signer can compute a hint polynomial h, which will allow the verifier to reconstruct w_1 using the compressed public key along with the other components of the signature. This uses the function [MakeHint](#page-50-1) (Algorithm [39\)](#page-50-1). The signer will then output the final signature, which is a byte encoding of the commitment hash \tilde{c} , the response z, and the hint h.

In addition, there is an alternative way of implementing the validity checks on z and the computation of h, which is described in Section 5.1 of $[6]$. This method may also be used in implementations of ML-DSA.

In Algorithm [7,](#page-34-1) variables are sometimes used to store products to avoid recomputing them later in the signing algorithm. These precomputed products are denoted in the pseudocode by a pair of double angle brackets enclosing the variables being multiplied (e.g., $\langle\langle c\mathbf{s}_1\rangle\rangle$).

option is used and the maximum number of iterations is exceeded without producing a valid signature, the signing algorithm **shall** return a constant that represents an error and no other output, destroying the results of the unsuccessful signing attempts. See Appendix [C.](#page-61-0)

¹¹The length of \tilde{c} is determined by the desired security with respect to the "message-bound signatures" property described in [\[14\]](#page-57-0). Here, a length of $\lambda/4$ bytes or equivalently 2λ bits is required for λ bits of classical security.

 $\overline{\mathsf{Algorithm}}$ **7** [ML-DSA](#page-34-1).Sign_internal (sk, M', rnd)

Deterministic algorithm to generate a signature for a formatted message ′ *.*

Input: Private key $sk \in \mathbb{B}^{32+32+64+32\cdot((\ell+k)\cdot \text{bitlen}(2\eta)+dk)}$, formatted message $M' \in \{0,1\}^*$, and per message randomness or dummy variable $rnd \in \mathbb{B}^{32}$. **Output:** Signature $\sigma \in \mathbb{B}^{\lambda/4+\ell \cdot 32 \cdot (1+\text{bitlen}(\gamma_1-1))+\omega+k}$.

1: $(\rho, K, tr, \mathbf{s}_1, \mathbf{s}_2, \mathbf{t}_0) \leftarrow \text{skDecode}(sk)$ $(\rho, K, tr, \mathbf{s}_1, \mathbf{s}_2, \mathbf{t}_0) \leftarrow \text{skDecode}(sk)$ $(\rho, K, tr, \mathbf{s}_1, \mathbf{s}_2, \mathbf{t}_0) \leftarrow \text{skDecode}(sk)$ 2: $\hat{\mathbf{s}}_1 \leftarrow \text{NTT}(\mathbf{s}_1)$ $\hat{\mathbf{s}}_1 \leftarrow \text{NTT}(\mathbf{s}_1)$ $\hat{\mathbf{s}}_1 \leftarrow \text{NTT}(\mathbf{s}_1)$ 3: $\hat{s}_2 \leftarrow \text{NTT}(\mathbf{s}_2)$ $\hat{s}_2 \leftarrow \text{NTT}(\mathbf{s}_2)$ $\hat{s}_2 \leftarrow \text{NTT}(\mathbf{s}_2)$ 4: $\mathbf{\hat{t}}_0 \leftarrow \mathsf{NTT}(\mathbf{t}_0)$ $\mathbf{\hat{t}}_0 \leftarrow \mathsf{NTT}(\mathbf{t}_0)$ $\mathbf{\hat{t}}_0 \leftarrow \mathsf{NTT}(\mathbf{t}_0)$ 5: \mathbf{A} ← [ExpandA](#page-47-0)(ρ) \Box \Box \mathbf{A} is generated and stored in NTT representation as \mathbf{A} 6: $\mu \leftarrow H(B$ $\mu \leftarrow H(B$ $\mu \leftarrow H(B$ ytesToBits $(tr)||M', 64)$ \triangleright message representative that may optionally be computed in a different cryptographic module 7: $\rho'' \leftarrow H(K||rnd||\mu, 64)$ $\rho'' \leftarrow H(K||rnd||\mu, 64)$ $\rho'' \leftarrow H(K||rnd||\mu, 64)$ \triangleright compute private random seed 8: $\kappa \leftarrow 0$ \triangleright initialize counter κ 9: $(\mathbf{z}, \mathbf{h}) \leftarrow \perp$ 10: **while** $(z, h) = \perp$ **do** \triangleright rejection sampling loop 11: $\mathbf{y} \in R^{\ell}_q \leftarrow \mathsf{ExpandMask}(\rho'', \kappa)$ $\mathbf{y} \in R^{\ell}_q \leftarrow \mathsf{ExpandMask}(\rho'', \kappa)$ $\mathbf{y} \in R^{\ell}_q \leftarrow \mathsf{ExpandMask}(\rho'', \kappa)$ 12: $\mathbf{w} \leftarrow \text{NTT}^{-1}(\hat{\mathbf{A}} \circ \text{NTT}(\mathbf{y}))$ $\mathbf{w} \leftarrow \text{NTT}^{-1}(\hat{\mathbf{A}} \circ \text{NTT}(\mathbf{y}))$ $\mathbf{w} \leftarrow \text{NTT}^{-1}(\hat{\mathbf{A}} \circ \text{NTT}(\mathbf{y}))$ 13: $\mathbf{w}_1 \leftarrow \text{HighBits}(\mathbf{w})$ $\mathbf{w}_1 \leftarrow \text{HighBits}(\mathbf{w})$ $\mathbf{w}_1 \leftarrow \text{HighBits}(\mathbf{w})$ \triangleright signer's commitment 14: \triangleright HighBits is applied componentwise (see explanatory text in Section [7.4\)](#page-48-0) 15: $\tilde{c} \leftarrow H(\mu||\mathbf{w1Encode(w_1)}, \lambda/4)$ \triangleright commitment hash $c \in R_a \leftarrow$ Sample In Ball (\tilde{c}) \triangleright verifier's challenge 16: $c \in R_a \leftarrow$ [SampleInBall](#page-45-1) (\tilde{c}) 17: $\hat{c} \leftarrow \text{NTT}(c)$ $\hat{c} \leftarrow \text{NTT}(c)$ $\hat{c} \leftarrow \text{NTT}(c)$ 18: $\langle\langle c\mathbf{s}_1\rangle\rangle\leftarrow \mathsf{NTT}^{-1}(\hat{c}\circ \hat{\mathbf{s}}_1)$ $\langle\langle c\mathbf{s}_1\rangle\rangle\leftarrow \mathsf{NTT}^{-1}(\hat{c}\circ \hat{\mathbf{s}}_1)$ $\langle\langle c\mathbf{s}_1\rangle\rangle\leftarrow \mathsf{NTT}^{-1}(\hat{c}\circ \hat{\mathbf{s}}_1)$ 19: $\langle\langle c\mathbf{s}_2^2\rangle\rangle \leftarrow \mathsf{NTT}^{-1}(\hat{c} \circ \hat{\mathbf{s}_2})$ $\langle\langle c\mathbf{s}_2^2\rangle\rangle \leftarrow \mathsf{NTT}^{-1}(\hat{c} \circ \hat{\mathbf{s}_2})$ $\langle\langle c\mathbf{s}_2^2\rangle\rangle \leftarrow \mathsf{NTT}^{-1}(\hat{c} \circ \hat{\mathbf{s}_2})$ 20: $\mathbf{z} \leftarrow \mathbf{y} + \langle \langle c\mathbf{s}_1 \rangle \rangle$ > signer's response 21: $\mathbf{r}_0 \leftarrow \textsf{LowBits}(\mathbf{w} - \langle \langle c\mathbf{s}_2 \rangle \rangle)$ $\mathbf{r}_0 \leftarrow \textsf{LowBits}(\mathbf{w} - \langle \langle c\mathbf{s}_2 \rangle \rangle)$ $\mathbf{r}_0 \leftarrow \textsf{LowBits}(\mathbf{w} - \langle \langle c\mathbf{s}_2 \rangle \rangle)$ 22: \triangleright LowBits is applied componentwise (see explanatory text in Section [7.4\)](#page-48-0) 23: **if** $||\mathbf{z}||_{\infty} \geq \gamma_1 - \beta$ or $||\mathbf{r}_0||_{\infty} \geq \gamma_2 - \beta$ then $(\mathbf{z}, \mathbf{h}) \leftarrow \bot$ \triangleright validity checks 24: **else** 25: $\langle\langle ct_0 \rangle\rangle \leftarrow \text{NTT}^{-1}(\hat{c} \circ \hat{\mathbf{t}}_0)$ $\langle\langle ct_0 \rangle\rangle \leftarrow \text{NTT}^{-1}(\hat{c} \circ \hat{\mathbf{t}}_0)$ $\langle\langle ct_0 \rangle\rangle \leftarrow \text{NTT}^{-1}(\hat{c} \circ \hat{\mathbf{t}}_0)$ 26: $h \leftarrow \text{MakeHint}(-\langle \langle ct_0 \rangle \rangle, w - \langle \langle cs_2 \rangle \rangle + \langle \langle ct_0 \rangle \rangle)$ $h \leftarrow \text{MakeHint}(-\langle \langle ct_0 \rangle \rangle, w - \langle \langle cs_2 \rangle \rangle + \langle \langle ct_0 \rangle \rangle)$ $h \leftarrow \text{MakeHint}(-\langle \langle ct_0 \rangle \rangle, w - \langle \langle cs_2 \rangle \rangle + \langle \langle ct_0 \rangle \rangle)$ \triangleright Signer's hint 27: \triangleright MakeHint is applied componentwise (see explanatory text in Section [7.4\)](#page-48-0) 28: **if** $\left\| \left(\left\langle c t_0 \right\rangle \right\rangle \right\|_{\infty} \geq \gamma_2$ or the number of 1's in h is greater than ω , then $(z, h) \leftarrow \bot$ 29: **end if** 30: **end if** 31: $\kappa \leftarrow \kappa + \ell$ > increment counter 32: **end while** 33: $\sigma \leftarrow$ [sigEncode](#page-44-0)(\tilde{c} , z mod $\pm q$, h) 34: **return**

6.3 ML-DSA Verifying (Internal)

The algorithm [ML-DSA](#page-36-0). Verify_internal (Algorithm [8\)](#page-36-0) takes a public key pk encoded as a byte string, a message M encoded as a bit string, and a signature σ encoded as a byte string as input. No randomness is required for [ML-DSA](#page-36-0). Verify internal. It produces a Boolean value (i.e., a value that is true if the signature is valid with respect to the message and public key and false if the signature is invalid) as output. Algorithm [8](#page-36-0) specifies the lengths of the signature σ and the public key pk in terms of the parameters described in Table [1.](#page-24-1) If an implementation of [ML-DSA](#page-36-0).Verify_internal can accept inputs for σ or pk of any other length, it **shall** return false whenever the length of either of these inputs differs from its specified length.

The verifier first extracts the public random seed ρ and the compressed polynomial vector t_1 from the public key pk and then extracts the signer's commitment hash \tilde{c} , response z, and hint **h** from the signature σ . The verifier may find that the hint was not properly byte-encoded, denoted by the symbol " \perp ," in which case the verification algorithm will immediately return false to indicate that the signature is invalid.

Assuming that the signature is successfully extracted from its byte encoding, the verifier pseudorandomly derives A from ρ , as is done in key generation and signing, and creates a message representative μ by hashing the concatenation of tr (i.e., the hash of the public key pk) and the message M. The verifier then attempts to reconstruct the signer's commitment (i.e., the polynomial vector w_1) from the public key pk and the signature σ . In [ML-DSA](#page-34-1).Sign_internal, \mathbf{w}_1 is computed by rounding $\mathbf{w} = \mathbf{A} \mathbf{y}$. In [ML-DSA](#page-36-0).Verify_internal, the reconstructed value of w_1 is called w'_1 since it may have been computed in a different way if the signature is invalid. This \mathbf{w}'_1 is computed through the following process:

- Derive the challenge polynomial c from the signer's commitment hash \tilde{c} , just as similarly is done in [ML-DSA](#page-34-1).Sign_internal.
- \bullet Use the signer's response z to compute

$$
\mathbf{w}_{\text{Approx}}' = \mathbf{A}\mathbf{z} - c\mathbf{t}_1 \cdot 2^d.
$$

Assuming the signature was computed correctly, as in [ML-DSA](#page-34-1). Sign internal, it follows that

$$
\mathbf{w} = \mathbf{A}\mathbf{y} = \mathbf{A}\mathbf{z} - c\mathbf{t} + c\mathbf{s}_2 \approx \mathbf{w}_{\mathsf{Approx}}' = \mathbf{A}\mathbf{z} - c\mathbf{t}_1 \cdot 2^d
$$

because c and s_2 have small coefficients, and $t_1 \cdot 2^d \approx t$.

• Use the signer's hint $\bf h$ to obtain ${\bf w}'_1$ from ${\bf w}'_{\text{Approx}}$.

Finally, the verifier checks that the signer's response z and the signer's hint h are valid and that the reconstructed \mathbf{w}'_1 is consistent with the signer's commitment hash $\tilde{c}.$ More precisely, the verifier checks that all of the coefficients of z are sufficiently small (i.e., in the range $(-(\gamma_1 - \beta), \gamma_1 - \beta)$), h contains no more than ω nonzero coefficients, and \tilde{c} matches the hash \tilde{c}' of the message representative μ concatenated with $\bf w'_1$ (represented as a byte string). If all of these checks succeed, then $\sf ML\text{-}DSA.$ Verify_internal returns true. Otherwise, it returns false.

 $\overline{\mathsf{Algorithm}\ 8\ \mathsf{ML}\text{-} \mathrm{DSA}.\mathsf{Verify}_\mathsf{internal}(pk, M', \sigma)}$

Internal function to verify a signature σ for a formatted message $M'.$

Input: Public key $pk \in \mathbb{B}^{32+32k(\text{bitlen } (q-1)-d))}$ and message $M' \in \{0,1\}^*$. **Input**: Signature $\sigma \in \mathbb{B}^{\lambda/4+\ell \cdot 32 \cdot (1+\text{bitten}(\gamma_1-1))+\omega+k}$. **Output**: Boolean 1: $(\rho, \mathbf{t}_1) \leftarrow \text{pkDecode}(pk)$ $(\rho, \mathbf{t}_1) \leftarrow \text{pkDecode}(pk)$ $(\rho, \mathbf{t}_1) \leftarrow \text{pkDecode}(pk)$
2: $(\tilde{c}, \mathbf{z}, \mathbf{h}) \leftarrow \text{sigDecode}(\sigma)$ \triangleright signer's commitment hash \tilde{c} , response z, and hint h 3: **if** = ⊥ **then return** false ▷ hint was not properly encoded 4: **end if**

5: \hat{A} ← [ExpandA](#page-47-0)(ρ) \geq A is generated and stored in NTT representation as \hat{A} 6: $tr \leftarrow H(pk, 64)$ $tr \leftarrow H(pk, 64)$ $tr \leftarrow H(pk, 64)$ 7: $\mu \leftarrow (H(BytesToBits(tr)||M', 64))$ $>$ message representative that may optionally be computed in a different cryptographic module 8: $c \in R_a \leftarrow$ [SampleInBall](#page-45-1) (\tilde{c}) > \Box \Box compute verifier's challenge from \tilde{c} $\mathbf{v}'_{\mathsf{Approx}} \leftarrow \mathsf{NTT}^{-1}(\mathbf{\hat{A}} \circ \mathsf{NTT}(\mathbf{z}) - \mathsf{NTT}(c) \circ \mathsf{NTT}(\mathbf{t}_1 \cdot 2^d)) \quad \rhd \mathbf{w}'_{\mathsf{Approx}} = \mathbf{A}\mathbf{z} - c\mathbf{t}_1 \cdot 2^d$ $\mathbf{v}'_{\mathsf{Approx}} \leftarrow \mathsf{NTT}^{-1}(\mathbf{\hat{A}} \circ \mathsf{NTT}(\mathbf{z}) - \mathsf{NTT}(c) \circ \mathsf{NTT}(\mathbf{t}_1 \cdot 2^d)) \quad \rhd \mathbf{w}'_{\mathsf{Approx}} = \mathbf{A}\mathbf{z} - c\mathbf{t}_1 \cdot 2^d$ $\mathbf{v}'_{\mathsf{Approx}} \leftarrow \mathsf{NTT}^{-1}(\mathbf{\hat{A}} \circ \mathsf{NTT}(\mathbf{z}) - \mathsf{NTT}(c) \circ \mathsf{NTT}(\mathbf{t}_1 \cdot 2^d)) \quad \rhd \mathbf{w}'_{\mathsf{Approx}} = \mathbf{A}\mathbf{z} - c\mathbf{t}_1 \cdot 2^d$ 10: $\mathbf{w}'_1 \leftarrow \mathsf{UseHint}(\mathbf{h}, \mathbf{w}'_{\text{Approx}})$ \triangleright reconstruction of signer's commitment 11: \triangleright UseHint is applied componentwise (see explanatory text in Section [7.4\)](#page-48-0) 12: $\tilde{c}' \leftarrow H(\mu||\textsf{w1Encode}(\textbf{w}_1')$ \triangleright hash it; this should match \tilde{c} 13: $\,$ **return** $[[\, ||\mathbf{z}||_{\infty} < \gamma_1 - \bar{\beta}]]$ and $[[\tilde{c} = \tilde{c}']]$

7. Auxiliary Functions

This section provides pseudocode for subroutines utilized by ML-DSA, including functions for data-type conversions, arithmetic, and sampling.

7.1 Conversion Between Data Types

While the primary data type in ML-DSA is a byte string, other data types are used as well. The goal in this section is to construct procedures for translating between the various algebraic objects defined in Section [2.3.](#page-14-1) Algorithms [9](#page--1-5)-13 are intermediate procedures for converting between bit strings, byte strings, and integers.

Algorithm 9 [IntegerToBits](#page--1-5) (x, α)

Computes a base-2 representation of x *mod* 2^α *using little-endian order.*

Input: A nonnegative integer x and a positive integer α .

Output: A bit string y of length α .

```
1: x' \leftarrow x2: for i from 0 to \alpha - 1 do
3: y[i] \leftarrow x' \mod 24: x' \leftarrow |x'/2|5: end for
6: return
```
Algorithm 10 Bits To Integer (y, α)

Computes the integer value expressed by a bit string using little-endian order.

Input: A positive integer α and a bit string y of length α .

Output: A nonnegative integer x .

```
1: x \leftarrow 02: for i from 1 to \alpha do
3: x \leftarrow 2x + y[\alpha - i]4: end for
5: return
```
Algorithm 11 Integer To Bytes (x, α)

Computes a base-256 representation of x *mod* 256^α *using little-endian order.*

Input: A nonnegative integer x and a positive integer α . **Output**: A byte string y of length α .

```
1: x' \leftarrow x2: for i from 0 to \alpha - 1 do
3: y[i] \leftarrow x' \mod 2564: x' \leftarrow |x'/256|5: end for
6: return
```
Algorithm 12 Bits To Bytes (y)

Converts a bit string into a byte string using little-endian order.

Input: A bit string y of length α . **Output**: A byte string z of length $\lceil \alpha/8 \rceil$. 1: $z \in \mathbb{B}^{\lceil \alpha/8 \rceil} \leftarrow 0^{\lceil \alpha/8 \rceil}$ 2: **for** i **from** 0 **to** $\alpha - 1$ **do** 3: $z[[i/8]] \leftarrow z[|i/8|| + y[i] \cdot 2^{i \mod 8}$ 4: **end for** 5: **return**

Algorithm 13 Bytes $ToBits(z)$

Converts a byte string into a bit string using little-endian order.

Input: A byte string z of length α . **Output**: A bit string y of length 8α .

Algorithms [14](#page--1-10) and [15](#page-39-0) translate byte strings into coefficients of polynomials in R . [CoeffFromThreeBytes](#page--1-10) uses a 3-byte string to either generate an element of $\{0, 1, ..., q - 1\}$ or return the blank symbol \perp . [CoeffFromHalfByte](#page-39-0) uses an element of $\{0, 1, ..., 15\}$ to either generate an element of $\{-\eta, -\eta+1, ..., \eta\}$ or return \perp . These two procedures will be used in the uniform sampling algorithms [RejNTTPoly](#page-46-0) and [RejBoundedPoly](#page-46-1), which are discussed in Section [7.3.](#page-45-0)

Algorithm 14 [CoeffFromThreeBytes](#page--1-10) (b_0, b_1, b_2)

Generates an element of $\{0, 1, 2, ..., q - 1\} \cup \{\perp\}$ *.* **Input**: Bytes b_0 , b_1 , b_2 . **Output**: An integer modulo q or \perp . 1: $b'_2 \leftarrow b_2$ 2: **if** $b'_2 > 127$ **then** 3: $b'_2 \leftarrow b'_2$ $\gamma_2' - 128$ $\quad \rhd$ set the top bit of b'_2 to zero 4: **end if** 5: $z \leftarrow 2^{16} \cdot b'_2 + 2^8 \cdot b_1 + b_0$ $\triangleright 0 \le z \le 2^{23} - 1$ 6: **if** $z < q$ then return z 7: **else return** ⊥ 8: **end if**

Algorithm 15 [CoeffFromHalfByte](#page-39-0)(b)

Let $\eta \in \{2, 4\}$ *. Generates an element of* $\{-\eta, -\eta + 1, ..., \eta\} \cup \{\perp\}$ *.* **Input**: Integer $b \in \{0, 1, ..., 15\}$. **Output**: An integer between $-\eta$ and η , or \perp . 1: **if** $\eta = 2$ and $b < 15$ then return $2 - (b \mod 5)$ \triangleright rejection sampling from $\{-2, ..., 2\}$ 2: **else** 3: 4: **if** $\eta = 4$ **and** $b < 9$ **then return** $4 - b$ **else return** ⊥ \triangleright rejection sampling from $\{-4, ..., 4\}$ 5: **end if** 6: **end if**

Algorithms [16–](#page-39-1)[19](#page-40-1) efficiently translate an element $w \in R$ into a byte string and vice versa under the assumption that the coefficients of w are in a restricted range. [SimpleBitPack](#page-39-1) assumes that $w_i \in [0, b]$ for some positive integer b and packs w into a byte string of length 32 ⋅ bitlen b. [BitPack](#page-39-2) allows for the more general restriction $w_i \in [-a, b]$. The [BitPack](#page-39-2) algorithm works by merely subtracting w from the polynomial $\sum_{i=0}^{255} bX^i$.

Algorithm 16 [SimpleBitPack](#page-39-1) (w, b)

Encodes a polynomial into a byte string.

Input: $b \in \mathbb{N}$ and $w \in R$ such that the coefficients of w are all in [0, b].

Output: A byte string of length $32 \cdot$ bitlen b.

```
1: z \leftarrow () > set z to the empty bit string
2: for  from 0 to 255 do
3: z \leftarrow z||IntegerToBits(w_i, \text{b}itlen b)4: end for
5: returnBitsToBytes(z)
```
Algorithm 17 [BitPack](#page-39-2) (w, a, b)

Encodes a polynomial into a byte string.

Input: $a, b \in \mathbb{N}$ and $w \in R$ such that the coefficients of w are all in $[-a, b]$. **Output**: A byte string of length $32 \cdot$ bitlen $(a + b)$.

1: $z \leftarrow ()$ > set z to the empty bit string

```
2: for  from 0 to 255 do
```

```
3: z \leftarrow z||IntegerToBits(b - w_i, bitlen (a + b))
```
4: **end for**

```
5: returnBitsToBytes(z)
```
[SimpleBitUnpack](#page-40-0) and [BitUnpack](#page-40-1) are used to decode the byte strings produced by the above functions. For some choices of a and b , there exist malformed byte strings that will cause [SimpleBitUnpack](#page-40-0) and [BitUnpack](#page-40-1) to output polynomials whose coefficients are not in the ranges $[0, b]$ and $[-a, b]$, respectively. This can be a concern when running [SimpleBitUnpack](#page-40-0) and [BitUnpack](#page-40-1) on inputs that may come from an untrusted source.

Algorithm 18 [SimpleBitUnpack](#page-40-0) (v, b)

Reverses the procedure [SimpleBitPack](#page-39-1)*.*

Input: $b \in \mathbb{N}$ and a byte string v of length $32 \cdot$ bitlen b. **Output**: A polynomial $w \in R$ with coefficients in $[0, 2^c - 1]$, where $c =$ bitlen b. When $b + 1$ is a power of 2, the coefficients are in [0, b].

```
1: c ← bitlen b2: zBytesToBits(v)3: for  from 0 to 255 do
4: BitsToInteger}((z[ic], z[ic + 1], \dots z[ic + c - 1]), c)5: end for
6: return
```
Algorithm 19 [BitUnpack](#page-40-1) (v, a, b)

Reverses the procedure [BitPack](#page-39-2)*.*

Input: $a, b \in \mathbb{N}$ and a byte string v of length $32 \cdot$ bitlen $(a + b)$. **Output**: A polynomial $w \in R$ with coefficients in $[b - 2^c + 1, b]$, where $c =$ bitlen $(a + b)$. When $a + b + 1$ is a power of 2, the coefficients are in $[-a, b]$.

1: c ← bitlen $(a + b)$ 2: z ← [BytesToBits](#page--1-9) (v) 3: **for from** 0 **to** 255 **do** 4: $w_i \leftarrow b - \text{BitsToInteger}((z[ic], z[ic+1], \dots z[ic+c-1]), c)$ $w_i \leftarrow b - \text{BitsToInteger}((z[ic], z[ic+1], \dots z[ic+c-1]), c)$ $w_i \leftarrow b - \text{BitsToInteger}((z[ic], z[ic+1], \dots z[ic+c-1]), c)$ 5: **end for** 6: **return**

Algorithms [20](#page-41-0) and [21](#page-41-1) carry out byte-string-to-polynomial conversions for polynomials with sparse binary coefficients. In particular, the signing and verification algorithms (Sections [6.2](#page-32-0) and [6.3\)](#page-34-0) make use of a "hint," which is a vector of polynomials $\mathbf{h}\in R_2^k$ such that the total number of coefficients in $\mathbf{h}[0],\mathbf{h}[1],$ \dots , $\mathbf{h}[k{-}1]$ that are equal to 1 is no more than ω . This constraint enables encoding and decoding procedures that are more efficient (although more complex) than [BitPack](#page-39-2) and [BitUnpack](#page-40-1).

[HintBitPack](#page-41-0) (h) outputs a byte string y of length $\omega + k$. The last k bytes of y contain information about how many nonzero coefficients are present in each of the polynomials $\mathbf{h}[0], \mathbf{h}[1], \ldots, \mathbf{h}[k-1]$, and the first ω bytes of y contain information about exactly where those nonzero terms occur. [HintBitUnpack](#page-41-1) reverses the procedure performed by $HintBitPack$ and recovers the vector h .

Algorithm 20 [HintBitPack](#page-41-0)(h)

Encodes a polynomial vector with binary coefficients into a byte string.

Input: A polynomial vector $\mathbf{h} \in R_2^k$ such that the polynomials $\mathbf{h}[0], \mathbf{h}[1], ..., \mathbf{h}[k-1]$ have collectively at most ω nonzero coefficients.

Output: A byte string y of length $\omega + k$ that encodes h as described above.

```
1: y \in \mathbb{B}^{\omega+k} \leftarrow 0^{\omega+k}2: Index \leftarrow 0 \triangleright Index for writing the first \omega bytes of y3: for i from 0 to k-1 do \longrightarrow4: for  from 0 to 255 do
 5: if \mathbf{h}[i]_i \neq 0 then
 6: y[Index] \leftarrow j \triangleright store the locations of the nonzero coefficients in h[i]7: \mathsf{Index} \leftarrow \mathsf{Index} + 18: end if
 9: end for
10: y[\omega+i] \leftarrow Index \triangleright after processing h[i], store the value of Index
11: end for
12: return
```
Algorithm 21 [HintBitUnpack](#page-41-1) (y)

Reverses the procedure [HintBitPack](#page-41-0)*.*

Input: A byte string y of length $\omega + k$ that encodes h as described above.

Output: A polynomial vector $\mathbf{h} \in R_2^k$ or \bot .

```
1: \mathbf{h} \in R_2^k \leftarrow 0^k2: Index \leftarrow 0 > 1ndex for reading the first \omega bytes of y3: for i from 0 to k-1 do \longrightarrow4: if y[\omega + i] < Index or y[\omega + i] > \omega then return \perp \triangleright malformed input
 5: end if
 6: First ← Index
 7: while Index \langle y | \omega + i | do \qquad \qquad \triangleright y | \omega + i | says how far one can advance Index
 8: if Index > First then
 9: if y[Index -1] \ge y[Index] then return \perp \qquad \qquad \qquad malformed input
10: end if
11: end if
12: \mathbf{h}[i]_{y[\text{index}]} \leftarrow 1 \triangleright y[\text{Index}] says which coefficient in \mathbf{h}[i] should be 1
13: Index \leftarrow Index + 1
14: end while
15: end for
16: for i from Index to \omega - 1 do \omega b read any leftover bytes in the first \omega bytes of y17: if y[i] \neq 0 then return \perp because the contract of the contract
18: end if
19: end for
20: return
```
7.2 Encodings of ML-DSA Keys and Signatures

Algorithms [22–](#page-42-1)[27](#page-44-1) translate keys and signatures for ML-DSA into byte strings. These procedures take certain sequences of algebraic objects, encode them consecutively into byte strings, and perform the respective decoding procedures.

First, [pkEncode](#page-42-1) and [pkDecode](#page-42-2) translate ML-DSA public keys into byte strings and vice versa. When verifying a signature, [pkDecode](#page-42-2) might be run on an input that comes from an untrusted source. Thus, care is required when using [SimpleBitUnpack](#page-40-0). As used here, SimpleBitUnpack always returns values in the correct range.

Algorithm 22 [pkEncode](#page-42-1)(ρ, t_1)

Encodes a public key for ML-DSA into a byte string. **Input**: $\rho \in \mathbb{B}^{32}$, $\mathbf{t}_1 \in R^k$ with coefficients in [0, $2^{\text{bitten}} (q-1)-d-1$]. **Output**: Public key $pk \in \mathbb{B}^{32+32k(\text{bitlen } (q-1)-d))}$. 1: $pk \leftarrow \rho$ 2: **for** i **from** 0 **to** $k-1$ **do** 3: $pk \leftarrow pk$ || [SimpleBitPack](#page-39-1) $(\mathbf{t}_1[i], 2^{\text{bitlen}}(q-1)-d-1)$ 4: **end for** 5: **return**

Algorithm 23 $pkDecode(pk)$ $pkDecode(pk)$

Reverses the procedure [pkEncode](#page-42-1)*.*

Input: Public key $pk \in \mathbb{B}^{32+32k(\text{bitlen } (q-1)-d)}$. **Output**: $\rho \in \mathbb{B}^{32}$, $\mathbf{t}_1 \in R^k$ with coefficients in [0, 2^{bitlen (q-1)-d} - 1]. $\texttt{1: } (\rho, z_0, \ldots, z_{k-1}) \in \mathbb{B}^{32} \times \left(\mathbb{B}^{32 \left(\text{bitlen}\ (q-1) - d\right)} \right)^k \leftarrow pk$ 2: **for** i **from** 0 **to** $k-1$ **do** 3: $\mathbf{t}_1[i] \leftarrow$ [SimpleBitUnpack](#page-40-0)(z_i , 2^{bitlen (q-1)-d} − 1) \triangleright This is always in the correct range 4: **end for** 5: **return** (ρ, t_1)

Next, [skEncode](#page-43-0) and [skDecode](#page-43-1) translate ML-DSA secret keys into byte strings and vice versa. Note that there exist malformed inputs that can cause [skDecode](#page-43-1) to return values that are not in the correct range. Hence, [skDecode](#page-43-1) should only be run on inputs that come from trusted sources.

Algorithm 24 [skEncode](#page-43-0)(ρ , K , tr , \mathbf{s}_1 , \mathbf{s}_2 , \mathbf{t}_0)

Encodes a secret key for ML-DSA into a byte string.

Input: $\rho \in \mathbb{B}^{32}$, $K \in \mathbb{B}^{32}$, $tr \in \mathbb{B}^{64}$, $\mathbf{s}_1 \in R^{\ell}$ with coefficients in $[-\eta, \eta]$, $\mathbf{s}_2 \in R^k$ with coefficients in $[-\eta, \eta]$, $\mathbf{t}_0 \in R^k$ with coefficients in $[-2^{d-1} + 1, 2^{d-1}]$. **Output**: Private key $s k \in \mathbb{B}^{32+32+64+32 \cdot ((k+\ell) \cdot \text{bitlen} (2\eta)+dk)}$.

```
1: sk \leftarrow \rho ||K|| tr2: for i from 0 to \ell - 1 do
3: BitPack}(s_1[i], \eta, \eta)4: end for
5: for i from 0 to k-1 do
6: BitPack} (\mathbf{s}_2[i], \eta, \eta)7: end for
8: for i from 0 to k-1 do
9: BitPack} (\mathbf{t}_0[i], 2^{d-1} - 1, 2^{d-1})10: end for
11: return
```
Algorithm 25 [skDecode](#page-43-1)(sk)

Reverses the procedure [skEncode](#page-43-0)*.* **Input**: Private key $sk \in \mathbb{B}^{32+32+64+32 \cdot ((\ell+k) \cdot \text{bitlen}(2\eta) + dk)}$. **Output**: $\rho \in \mathbb{B}^{32}$, $K \in \mathbb{B}^{32}$, $tr \in \mathbb{B}^{64}$, $\mathbf{s}_1 \in R^{\ell}$, $\mathbf{s}_2 \in R^k$, $\mathbf{t}_0 \in R^k$ with coefficients in $[-2^{d-1}+1, 2^{d-1}]$. $\mathfrak{1}:~(\rho,K,tr,y_0,\ldots,y_{\ell-1},z_0,\ldots,z_{k-1},w_0,\ldots,w_{k-1})\in\mathbb{B}^{32}\times\mathbb{B}^{32}\times\mathbb{B}^{64}\times\left(\mathbb{B}^{32\text{-bitlen}\,(2\eta)}\right)^\ell\times$ $\left(\mathbb{B}^{32\cdot \text{bitlen}\,(2\eta)}\right)^k \times \left(\mathbb{B}^{32d}\right)^k \leftarrow sk$ 2: **for** i **from** 0 **to** $\ell - 1$ **do** 3: $\mathbf{s}_1[i] \leftarrow \text{BitUnpack}(u_i, n, \eta)$ $\mathbf{s}_1[i] \leftarrow \text{BitUnpack}(u_i, n, \eta)$ $\mathbf{s}_1[i] \leftarrow \text{BitUnpack}(u_i, n, \eta)$ \triangleright this may lie outside $[-n, n]$ if input is malformed 4: **end for** 5: **for** i **from** 0 **to** $k-1$ **do** 6: $\mathbf{s}_2[i] \leftarrow \text{BitUnpack}(z_i, \eta, \eta)$ $\mathbf{s}_2[i] \leftarrow \text{BitUnpack}(z_i, \eta, \eta)$ $\mathbf{s}_2[i] \leftarrow \text{BitUnpack}(z_i, \eta, \eta)$ \Rightarrow this may lie outside $[-\eta, \eta]$ if input is malformed 7: **end for** 8: **for** i **from** 0 **to** $k-1$ **do** 9: $\mathbf{t}_0[i] \leftarrow \text{BitUnpack}(w_i, 2^{d-1} - 1, 2^{d-1})$ $\mathbf{t}_0[i] \leftarrow \text{BitUnpack}(w_i, 2^{d-1} - 1, 2^{d-1})$ $\mathbf{t}_0[i] \leftarrow \text{BitUnpack}(w_i, 2^{d-1} - 1, 2^{d-1})$ \triangleright this is always in the correct range 10: **end for** 11: **return** $(\rho, K, tr, \mathbf{s}_1, \mathbf{s}_2, \mathbf{t}_0)$

Next, [sigEncode](#page-44-0) and [sigDecode](#page-44-1) translate ML-DSA signatures into byte strings and vice versa. When verifying a signature, [sigDecode](#page-44-1) might take input that comes from an untrusted source. Thus, care is required when using [BitUnpack](#page-40-1). As used here, BitUnpack always returns values in the correct range.

Algorithm 26 [sigEncode](#page-44-0)(\tilde{c} , z, h)

Encodes a signature into a byte string.

Input: $\tilde{c} \in \mathbb{B}^{\lambda/4}$, $\mathbf{z} \in R^{\ell}$ with coefficients in $[-\gamma_1 + 1, \gamma_1]$, $\mathbf{h} \in R_2^k$. **Output:** Signature $\sigma \in \mathbb{B}^{\lambda/4+\ell \cdot 32 \cdot (1+\text{bitlen}(\gamma_1-1))+\omega+k}$. 1: $\sigma \leftarrow \tilde{c}$ 2: **for** i **from** 0 **to** $\ell - 1$ **do** 3: $σ ← σ || \text{BitPack } (\mathbf{z}[i], \gamma_1 - 1, \gamma_1)$ $σ ← σ || \text{BitPack } (\mathbf{z}[i], \gamma_1 - 1, \gamma_1)$ $σ ← σ || \text{BitPack } (\mathbf{z}[i], \gamma_1 - 1, \gamma_1)$ 4: **end for** 5: $\sigma \leftarrow \sigma$ || [HintBitPack](#page-41-0) (h) 6: **return** σ

Algorithm 27 [sigDecode](#page-44-1)(σ)

Reverses the procedure [sigEncode](#page-44-0)*.*

Input: Signature $\sigma \in \mathbb{B}^{\lambda/4+\ell \cdot 32 \cdot (1+\text{bitten}(\gamma_1-1))+\omega+k}$. **Output**: $\tilde{c} \in \mathbb{B}^{\lambda/4}, \mathbf{z} \in R^{\ell}$ with coefficients in $[-\gamma_1 + 1, \gamma_1], \mathbf{h} \in R_2^k$, or \perp . $\mathfrak{a}\colon\thinspace (\tilde{c}, x_0,\ldots,x_{\ell-1}, y)\in\mathbb{B}^{\lambda/4}\times\left(\mathbb{B}^{32\cdot(1+\mathsf{bitlen\,}(\gamma_1-1))}\right)^\ell\times\mathbb{B}^{\omega+k}\leftarrow \sigma$ 2: **for** i **from** 0 **to** $\ell - 1$ **do** 3: $\mathbf{z}[i] \leftarrow \text{BitUnpack}(x_i, \gamma_1 - 1, \gamma_1) \quad \triangleright$ $\mathbf{z}[i] \leftarrow \text{BitUnpack}(x_i, \gamma_1 - 1, \gamma_1) \quad \triangleright$ $\mathbf{z}[i] \leftarrow \text{BitUnpack}(x_i, \gamma_1 - 1, \gamma_1) \quad \triangleright$ this is in the correct range, as γ_1 is a power of 2 4: **end for** 5: $\mathbf{h} \leftarrow \mathsf{HintBitUnpack}(y)$ $\mathbf{h} \leftarrow \mathsf{HintBitUnpack}(y)$ $\mathbf{h} \leftarrow \mathsf{HintBitUnpack}(y)$ 6: **return** $(\tilde{c}, \mathbf{z}, \mathbf{h})$

[w1Encode](#page-44-2) is a specific subroutine used in [ML-DSA](#page-27-2). Sign. The procedure $w1$ Encode encodes a polynomial vector w_1 into a string of bytes so that it can be processed by the function [H.](#page-22-1)

Algorithm 28 [w1Encode](#page-44-2)(w_1)

Encodes a polynomial vector w_1 *into a byte string.*

Input: $w_1 \in R^k$ whose polynomial coordinates have coefficients in $[0, (q-1)/(2\gamma_2) - 1]$. **Output:** A byte string representation $\tilde{\mathbf{w}}_1 \in \mathbb{B}^{32k \cdot \text{bitten}((q-1)/(2\gamma_2)-1)}$.

1: $\tilde{\mathbf{w}}_1 \leftarrow ()$ 2: **for** i **from** 0 **to** $k-1$ **do** 3: $\tilde{\mathbf{w}}_1 \leftarrow \tilde{\mathbf{w}}_1$ || [SimpleBitPack](#page-39-1) $(\mathbf{w}_1[i],(q-1)/(2\gamma_2)-1)$ 4: **end for** 5: **return** $\tilde{\mathbf{w}}_1$

7.3 Pseudorandom Sampling

This section specifies various algorithms for generating algebraic objects pseudorandomly from a seed ρ , where ρ is a byte string whose length varies depending on the algorithm. The first procedure to be defined is [SampleInBall](#page-45-1). As in Section [2.3,](#page-14-1) B_{τ} denotes the set of all polynomials $c \in R$ such that

- Each coefficient of c is either -1 , 0, or 1, and
- Exactly τ of the coefficients of c are nonzero.

[SampleInBall](#page-45-1) pseudorandomly generates an element of B_{τ} using the XOF of a seed ρ . The procedure is based on the Fisher-Yates shuffle. [H](#page-22-1) is applied to ρ , and the first 8 bytes of the output are used to choose the signs of the nonzero entries of c .^{[12](#page-45-2)} Subsequent bytes are used to choose the positions of those nonzero entries.

Algorithm 29 [SampleInBall](#page-45-1)(ρ)

```
Samples a polynomial c \in R with coefficients from \{-1,0,1\} and Hamming weight \tau \leq 64.
Input: A seed \rho \in \mathbb{B}^{\lambda/4}Output: A polynomial c in R.
 1: c \leftarrow 02: H.Init}()H.Absorb(ctx, \rho)
 4: H.Squeeze}(\text{ctx}, 8)5: h \leftarrowBytesToBits(s) \triangleright h is a bit string of length 64
 6: for i from 256 - \tau to 255 do
 7: (ctx, j) \leftarrow H.SquareZc(ctx, 1)8: while j > i do \triangleright ejection sampling in \{0, ..., i\}9: H.Squeeze}(\text{ctx}, 1)10: end while but a set of the set of \triangleright j is a pseudorandom byte that is \leq i11: c_i \leftarrow c_j12: c_j \leftarrow (-1)^{h[i+\tau-256]}13: end for
14: return
```
Algorithms [30](#page-46-0)[–34](#page-47-2) are the pseudorandom procedures [RejNTTPoly](#page-46-0), [RejBoundedPoly](#page-46-1), [ExpandA](#page-47-0), [ExpandS](#page-47-1), and [ExpandMask](#page-47-2). Each generates elements of R or T_a under different input and output conditions. [RejNTTPoly](#page-46-0) and [ExpandA](#page-47-0) make use of the more efficient XOF [G,](#page-22-1) whereas the other three procedures use the XOF [H.](#page-22-1)

The procedure [ExpandMask](#page-47-2) (Algorithm [34\)](#page-47-2) generates a polynomial vector y in R^k that disguises the secret key in the [ML-DSA](#page-34-1). Sign internal procedure (Algorithm [7\)](#page-34-1). In addition to the seed ρ , [ExpandMask](#page-47-2) also accepts an integer input μ that is incorporated into the pseudorandom procedure that generates s.

¹²The parameter τ is always less than or equal to 64, and thus 8 bytes are sufficient to choose the signs for all τ nonzero entries of c.

Algorithm 30 [RejNTTPoly](#page-46-0) (ρ)

Samples a polynomial $\in T_a$ *.* **Input**: A seed $\rho \in \mathbb{B}^{34}$. **Output:** An element $\hat{a} \in T_a$. 1: $i \leftarrow 0$ 2: $\text{ctx} \leftarrow \text{G}.\text{Init}()$ $\text{ctx} \leftarrow \text{G}.\text{Init}()$ $\text{ctx} \leftarrow \text{G}.\text{Init}()$ 3: ctx ← G.[Absorb](#page-22-1)(ctx, ρ) 4: **while** $j < 256$ do 5: $(\text{ctx}, s) \leftarrow \text{G.Squeeze}(\text{ctx}, 3)$ $(\text{ctx}, s) \leftarrow \text{G.Squeeze}(\text{ctx}, 3)$ $(\text{ctx}, s) \leftarrow \text{G.Squeeze}(\text{ctx}, 3)$ 6: $\hat{a}[j] \leftarrow \text{CoeffFromThreeBytes}(s[0], s[1], s[2])$ $\hat{a}[j] \leftarrow \text{CoeffFromThreeBytes}(s[0], s[1], s[2])$ $\hat{a}[j] \leftarrow \text{CoeffFromThreeBytes}(s[0], s[1], s[2])$ 7: **if** $\hat{a}[j] \neq \perp$ **then** 8: $j \leftarrow j+1$ 9: **end if** 10: **end while** 11: **return** \hat{a}

Algorithm 31 [RejBoundedPoly](#page-46-1)(ρ)

Samples an element $a \in R$ with coefficients in $[-\eta, \eta]$ computed via rejection sampling from ρ . **Input**: A seed $\rho \in \mathbb{B}^{66}$. **Output**: A polynomial $a \in R$. 1: $j \leftarrow 0$ 2: $\text{ctx} \leftarrow \text{H.Init}()$ $\text{ctx} \leftarrow \text{H.Init}()$ $\text{ctx} \leftarrow \text{H.Init}()$ 3: ctx ← [H.Absorb](#page-22-1)(ctx, ρ) 4: **while** $j < 256$ do 5: $z \leftarrow H.Squareze(ctx, 1)$ 6: $z_0 \leftarrow \text{CoeffFromHalfByte}(z \text{ mod } 16)$ $z_0 \leftarrow \text{CoeffFromHalfByte}(z \text{ mod } 16)$ $z_0 \leftarrow \text{CoeffFromHalfByte}(z \text{ mod } 16)$ 7: $z_1 \leftarrow \text{CoeffFromHalfByte}(|z/16|)$ $z_1 \leftarrow \text{CoeffFromHalfByte}(|z/16|)$ $z_1 \leftarrow \text{CoeffFromHalfByte}(|z/16|)$ 8: **if** $z_0 \neq \perp$ **then** 9: $a_j \leftarrow z_0$ 10: $j \leftarrow j + 1$ 11: **end if** 12: **if** $z_1 \neq \perp$ **and** $j < 256$ **then** 13: $a_j \leftarrow z_1$ 14: $j \leftarrow j+1$ 15: **end if** 16: **end while** 17: **return**

Algorithm 32 Expand $A(\rho)$

Samples $a k \times l$ *matrix* \hat{A} *of elements of* T_a *.* **Input**: A seed $\rho \in \mathbb{B}^{32}$. **Output**: Matrix $\hat{\mathbf{A}} \in (T_q)^{k \times \ell}$. 1: **for** r **from** 0 **to** $k-1$ **do** 2: **for** s **from** 0 **to** $\ell - 1$ **do** 3: $\rho' \leftarrow \rho ||\text{IntegerToBytes}(s, 1)||\text{IntegerToBytes}(r, 1)$ $\rho' \leftarrow \rho ||\text{IntegerToBytes}(s, 1)||\text{IntegerToBytes}(r, 1)$ $\rho' \leftarrow \rho ||\text{IntegerToBytes}(s, 1)||\text{IntegerToBytes}(r, 1)$ 4: $\hat{\mathbf{A}}[r, s] \leftarrow \text{RejNTTPoly}(\rho')$ $\hat{\mathbf{A}}[r, s] \leftarrow \text{RejNTTPoly}(\rho')$ $\hat{\mathbf{A}}[r, s] \leftarrow \text{RejNTTPoly}(\rho')$ \triangleright seed ρ' depends on s and r 5: **end for** 6: **end for** 7: $return A$

Algorithm 34 [ExpandMask](#page-47-2) (ρ, μ)

Samples a vector $y \in R^{\ell}$ *such that each polynomial* $y[r]$ *has coefficients between* $-\gamma_1 + 1$ *and* γ_1 . **Input**: A seed $\rho \in \mathbb{B}^{64}$ and a nonnegative integer μ . **Output**: Vector $y \in R^{\ell}$. 1: $c \leftarrow 1 + \text{bitlen } (\gamma_1 - 1)$ $\triangleright \gamma_1$ is always a power of 2 2: **for** r **from** 0 **to** $\ell - 1$ **do** 3: $\rho' \leftarrow \rho ||$ [IntegerToBytes](#page--1-7) $(\mu + r, 2)$ 4: $v \leftarrow H(\rho', 32c)$ $v \leftarrow H(\rho', 32c)$ $v \leftarrow H(\rho', 32c)$ \triangleright seed depends on $\mu + r$ 5: $\mathbf{y}[r] \leftarrow \text{BitUnpack}(v, \gamma_1 - 1, \gamma_1)$ $\mathbf{y}[r] \leftarrow \text{BitUnpack}(v, \gamma_1 - 1, \gamma_1)$ $\mathbf{y}[r] \leftarrow \text{BitUnpack}(v, \gamma_1 - 1, \gamma_1)$ 6: **end for** 7: **return**

7.4 High-Order and Low-Order Bits and Hints

This specification uses the auxiliary functions [Power2Round](#page-49-0), [Decompose](#page-49-1), [HighBits](#page-49-2), [LowBits](#page-50-0), [MakeHint](#page-50-1), and [UseHint](#page-50-2) and explicitly defines these functions, where $r \in \mathbb{Z}_q$, $r_1, r_0 \in \mathbb{Z}$, and h is a Boolean (or equivalently an element of \mathbb{Z}_2). However, this specification also uses these functions where $\mathbf{r},\mathbf{z}\in R^k_q$, ${\bf r}_1, {\bf r}_0\in R^k$, and ${\bf h}\in R_2^k.$ In this case, the functions are applied coefficientwise to the polynomials in the vectors. In particular:

• For $\mathbf{r}\in R^k_q$, define $(\mathbf{r}_1,\mathbf{r}_0)\in (R^k)^2=$ [Power2Round](#page-49-0) (\mathbf{r}) so that:

 $((\mathbf{r}_1[i])_j, (\mathbf{r}_0[i])_j) = \text{Power2Round}((\mathbf{r}[i])_j).$ $((\mathbf{r}_1[i])_j, (\mathbf{r}_0[i])_j) = \text{Power2Round}((\mathbf{r}[i])_j).$ $((\mathbf{r}_1[i])_j, (\mathbf{r}_0[i])_j) = \text{Power2Round}((\mathbf{r}[i])_j).$

• For $\mathbf{r}\in R^k_q$, define $(\mathbf{r}_1,\mathbf{r}_0)\in (R^k)^2 = \mathsf{Decompose}(\mathbf{r})$ $(\mathbf{r}_1,\mathbf{r}_0)\in (R^k)^2 = \mathsf{Decompose}(\mathbf{r})$ $(\mathbf{r}_1,\mathbf{r}_0)\in (R^k)^2 = \mathsf{Decompose}(\mathbf{r})$ so that:

$$
((\mathbf{r}_1[i])_j,(\mathbf{r}_0[i])_j)=\mathsf{Decompose}((\mathbf{r}[i])_j).
$$

• For $\mathbf{r} \in R_q^k$, define \mathbf{r}_1 = [HighBits](#page-49-2) (\mathbf{r}) so that:

$$
(\mathbf{r}_1[i])_j = \mathsf{HighBits}((\mathbf{r}[i])_j).
$$

• For $\mathbf{r} \in R_q^k$, define \mathbf{r}_0 = [LowBits](#page-50-0) (\mathbf{r}) so that:

$$
(\mathbf{r}_0[i])_j = \text{LowBits}((\mathbf{r}[i])_j).
$$

• For $\mathbf{z}, \mathbf{r} \in R_q^k$, define $\mathbf{h} \in R_2^k = \mathsf{MakeHint}(\mathbf{z}, \mathbf{r})$ $\mathbf{h} \in R_2^k = \mathsf{MakeHint}(\mathbf{z}, \mathbf{r})$ $\mathbf{h} \in R_2^k = \mathsf{MakeHint}(\mathbf{z}, \mathbf{r})$ so that:

 $(\mathbf{h}[i])_j = \mathsf{MakeHint}((\mathbf{z}[i])_j, (\mathbf{r}[i])_j).$ $(\mathbf{h}[i])_j = \mathsf{MakeHint}((\mathbf{z}[i])_j, (\mathbf{r}[i])_j).$ $(\mathbf{h}[i])_j = \mathsf{MakeHint}((\mathbf{z}[i])_j, (\mathbf{r}[i])_j).$

• For $\mathbf{h}\in R_2^k$ and $\mathbf{r}\in R_q^k$, define $\mathbf{r}_1\in R^k=$ [UseHint](#page-50-2) (\mathbf{h},\mathbf{r}) so that:

 $\mathbf{r}_1[i]_i = \text{UseHint}(\mathbf{h}[i])_i, (\mathbf{r}[i])_i).$ $\mathbf{r}_1[i]_i = \text{UseHint}(\mathbf{h}[i])_i, (\mathbf{r}[i])_i).$ $\mathbf{r}_1[i]_i = \text{UseHint}(\mathbf{h}[i])_i, (\mathbf{r}[i])_i).$

These algorithms are used to support the key compression optimization of ML-DSA. They involve dropping the d low-order bits of each coefficient of the polynomial vector t from the public key using the function [Power2Round](#page-49-0). However, in order to make this optimization work, additional information called a "hint" needs to be provided in the signature to allow the verifier to reconstruct enough of the information in the dropped public-key bits to verify the signature. Hints are created during signing and used during verification by the functions [MakeHint](#page-50-1) and [UseHint](#page-50-2), respectively. In the verification of a valid signature, the hint allows the verifier to recover $\mathbf{w}_1 \in R^k$, which represents $\mathbf{w} \in R^k_q$ rounded to a nearby multiple of $\alpha = 2\gamma_2$. The signer directly obtains w_1 using the function [HighBits](#page-49-2), and the part rounded off (i.e., r_0) is obtained by [LowBits](#page-50-0). r_0 is used by the signer in the rejection sampling procedure.

[Power2Round](#page-49-0) decomposes an input $r \in \mathbb{Z}_q$ into integers that represent the high- and low-order bits of r mod q in the straightforward bitwise way, r mod $q = r_1 \cdot 2^d + r_0$, where $r_0 = (r$ mod $q)$ mod $^{\pm}2^d$ and $r_1 = (r \mod q - r_0)/2^d$.

However, for the purpose of computations related to hints, this method of decomposing r has the undesirable property that when r is close to $q-1$ or 0, a small rounding error in r can cause r_1 to change by more than 1, even accounting for wrap-around. In contrast to other unequal pairs of values of $r_1 \cdot 2^d$ and $r'_1 \cdot 2^d$, the distance (mod q) between $\lfloor q/2^d \rfloor \cdot 2^d$ and 0 may be very small.

To avoid this problem, this specification defines [Decompose](#page-49-1), which is similar to [Power2Round](#page-49-0) except:

- *r* is generally decomposed as *r* mod $q = r_1 \cdot \alpha + r_0$, where $\alpha = 2\gamma_2$ is a divisor of $q 1$.
- If the straightforward rounding procedure would return $(r_1 = (q-1)/\alpha, r_0 \in [-(\alpha/2)+1, \alpha/2])$, [Decompose](#page-49-1) instead returns $(r_1 = 0, r_0 - 1)$.

The functions [HighBits](#page-49-2) and [LowBits](#page-50-0) — which only return r_1 and r_0 , respectively — and [MakeHint](#page-50-1) and [UseHint](#page-50-2) use [Decompose](#page-49-1). For additional discussion of the mathematical properties of these functions that are relevant to the correctness and security of ML-DSA, see Section 2.4 in $[6]$.

Algorithm 35 [Power2Round](#page-49-0) (r)

Decomposes r into (r_1, r_0) *such that* $r \equiv r_1 2^d + r_0$ *mod q.* **Input**: $r \in \mathbb{Z}_a$. **Output**: Integers (r_1, r_0) . 1: $r^+ \leftarrow r \mod q$ 2: $r_0 \leftarrow r^+ \mod 2^d$ 3: **return** $((r^+ - r_0)/2^d, r_0)$

Algorithm 36 [Decompose](#page-49-1) (r)

```
Decomposes r into (r_1, r_0) such that r \equiv r_1(2\gamma_2) + r_0 mod q.
Input: r \in \mathbb{Z}_q.
Output: Integers (r_1, r_0).
 1: r^+ \leftarrow r \mod q2: r_0 \leftarrow r^+ \mod \stackrel{\pm}{2}(\frac{2}{2})3: if r^+ - r_0 = q - 1 then
 4: r_1 \leftarrow 05: r_0 \leftarrow r_0 - 16: else r_1 \leftarrow (r^+ - r_0)/(2\gamma_2)7: end if
 8: return (r_1, r_0)
```
Algorithm 37 [HighBits](#page-49-2) (r)

Returns r_1 *from the output of [Decompose](#page-49-1)* (r) *.* **Input**: $r \in \mathbb{Z}_a$. **Output:** Integer r_1 . 1: $(r_1, r_0) \leftarrow$ [Decompose](#page-49-1) (r) 2: **return** r_1

Algorithm 38 [LowBits](#page-50-0) (r)

Returns r_0 *from the output of* [Decompose](#page-49-1) (r) *.* **Input**: $r \in \mathbb{Z}_q$. **Output**: Integer r_0 . 1: $(r_1, r_0) \leftarrow$ [Decompose](#page-49-1) (r) 2: **return** r_0

Algorithm 39 [MakeHint](#page-50-1) (z, r)

Computes hint bit indicating whether *adding* z to r *alters* the *high bits* of r .

Input: $z, r \in \mathbb{Z}_a$. **Output**: Boolean. 1: $r_1 \leftarrow$ [HighBits](#page-49-2)(r) 2: v_1 ← [HighBits](#page-49-2) $(r + z)$

3: **return** $[r_1 \neq v_1]$

Algorithm 40 [UseHint](#page-50-2) (h, r)

Returns the high bits of adjusted according to hint ℎ*.*

Input: Boolean $h, r \in \mathbb{Z}_q$. **Output**: $r_1 \in \mathbb{Z}$ with $0 \leq r_1 \leq \frac{q-1}{2\gamma_2}$. 1: $m \leftarrow (q-1)/(2\gamma_2)$ 2: $(r_1, r_0) \leftarrow \text{Decompose}(r)$ $(r_1, r_0) \leftarrow \text{Decompose}(r)$ $(r_1, r_0) \leftarrow \text{Decompose}(r)$ 3: **if** $h = 1$ and $r_0 > 0$ **return** $(r_1 + 1)$ mod m 4: **if** $h = 1$ and $r_0 \le 0$ **return** $(r_1 - 1)$ mod m 5: **return** r_1

 7.5 **NTT** and **NTT**^{-1}

The following algorithms implement the NTT and its inverse (NTT⁻¹), which is important for efficiency. There are other optimizations that are not included in this standard. In particular, mod q and mod $\pm q$ are expensive operations whose use can be minimized by using Montgomery Multiplication (see Appendix [A\)](#page--1-1).

An element of R_q is a polynomial in $\mathbb{Z}_q[X]/(X^{256}+1)$, and an element of T_q is a tuple in $\Pi^{255}_{j=0}\mathbb{Z}_q.$ The [NTT](#page-53-0) algorithm takes a polynomial $w \in R_q$ as input and returns $\hat{w} \in T_q$. NTT⁻¹ takes $\hat{w} \in T_q$ as input and returns w such that $\hat{w} = \text{NTT}(w)$ $\hat{w} = \text{NTT}(w)$ $\hat{w} = \text{NTT}(w)$.

This document always distinguishes between elements of R_q and elements of T_q . However, the natural data structure for both of these sets is as an integer array of size 256. This would allow the [NTT](#page-52-0) and NTT^{-1} NTT^{-1} algorithms to perform computation in place on an integer array passed by reference. That optimization is not included in this document.

In Section [2.5,](#page-17-0) $\zeta = 1753 \in \mathbb{Z}_q$, which is a 512th root of unity modulo q. On input $w \in R_q$, the algorithm outputs

$$
NTT(w) = (w(\zeta_0), w(\zeta_1), \dots, w(\zeta_{255})) \in T_q,
$$
\n(7.1)

where $\zeta_i = w(\zeta^{2\text{BitRev}_8(i)+1})$ mod q.

The values $\zeta^{\text{BitRev}_8(k)}$ mod q for $k = 1, ..., 255$ used in line 10 of Algorithms [41](#page-52-0) and [42](#page-53-0) are pre-computed into an array zetas $[1..255]$. The table of zetas is given in Appendix [B.](#page-60-0) If Montgomery Multiplication is used (see Appendix [A\)](#page--1-1), then the zetas array would typically be stored in Montgomery form.

[NTT](#page-53-0) and NTT⁻¹ use [BitRev](#page-53-1)₈, which reverses the order of bits in an 8-bit integer.

Algorithm 41 [NTT](#page-52-0) (w)

Computes the NTT. **Input**: Polynomial $w(X) = \sum_{j=0}^{255} w_j X^j \in R_q.$ **Output**: $\hat{w} = (\hat{w}[0], \dots, \hat{w}[255]) \in T_a$. 1: **for from** 0 **to** 255 **do** 2: $\hat{w}[j] \leftarrow w_i$ 3: **end for** 4: $m \leftarrow 0$ 5: $len \leftarrow 128$ 6: while $len \geq 1$ do 7: $start \leftarrow 0$ 8: **while** < 256 **do** 9: $m \leftarrow m + 1$ 10: $z \leftarrow$ zetas $[m]$ $> z \leftarrow \zeta^{\text{BitRev}_8(m)} \text{ mod } q$ $> z \leftarrow \zeta^{\text{BitRev}_8(m)} \text{ mod } q$ $> z \leftarrow \zeta^{\text{BitRev}_8(m)} \text{ mod } q$ 11: **for** j **from** $start$ **to** $start + len - 1$ **do** 12: $t \leftarrow (z \cdot \hat{w}[j + len]) \text{ mod } q$ 13: $\hat{w}[j + len] \leftarrow (\hat{w}[j] - t) \text{ mod } q$ 14: $\hat{w}[j] \leftarrow (\hat{w}[j] + t) \text{ mod } q$ 15: **end for** 16: $start \leftarrow start + 2 \cdot len$ 17: **end while** 18: $len \leftarrow |len/2|$ 19: **end while** 20: **return** \hat{w}

Algorithm 42 [NTT](#page-53-0)^{-1} (\hat{w})

Computes the inverse of the NTT. **Input**: $\hat{w} = (\hat{w}[0], \dots, \hat{w}[255]) \in T_q$. **Output**: Polynomial $w(X) = \sum_{j=0}^{255} w_j X^j \in R_q.$ 1: **for from** 0 **to** 255 **do** 2: $w_i \leftarrow \hat{w}[j]$ 3: **end for** 4: $m \leftarrow 256$ 5: $len \leftarrow 1$ 6: **while** < 256 **do** 7: $start \leftarrow 0$ 8: **while** < 256 **do** 9: $m \leftarrow m-1$ 10: $z \leftarrow -zeta^{BitRev_8(m)} \mod q$ $z \leftarrow -zeta^{BitRev_8(m)} \mod q$ $z \leftarrow -zeta^{BitRev_8(m)} \mod q$ 11: **for** j **from** $start$ **to** $start + len - 1$ **do** 12: $t \leftarrow w_i$ 13: $w_j \leftarrow (t + w_{j+len}) \text{ mod } q$ 14: $w_{j+len} \leftarrow (t - w_{j+len}) \text{ mod } q$ 15: $w_{j+len} \leftarrow (z \cdot w_{j+len}) \text{ mod } q$ 16: **end for** 17: $start \leftarrow start + 2 \cdot len$ 18: **end while** 19: $len \leftarrow 2 \cdot len$ 20: **end while** 21: $f \leftarrow 8347681$ $\triangleright f = 256^{-1} \mod q$ 22: **for from** 0 **to** 255 **do** 23: $w_i \leftarrow (f \cdot w_i) \mod q$ 24: **end for** 25: **return**

Algorithm 43 [BitRev](#page-53-1)₈ (m)

Transforms a byte by reversing the order of bits in its 8-bit binary expansion.

Input: A byte $m \in [0, 255]$. **Output**: A byte $r \in [0, 255]$. 1: $b \leftarrow$ [IntegerToBits](#page--1-5)(m, 8) 2: $b_{\text{rev}} \in \{0, 1\}^8 \leftarrow (0, ..., 0)$ 3: **for from** 0 **to** 7 **do** 4: $b_{\text{rev}}[i] \leftarrow b [7 - i]$ 5: **end for** 6: $r \leftarrow \textsf{BitsToInteger}(b_{\textsf{rev}}, 8)$ $r \leftarrow \textsf{BitsToInteger}(b_{\textsf{rev}}, 8)$ $r \leftarrow \textsf{BitsToInteger}(b_{\textsf{rev}}, 8)$ 7: **return** r

7.6 Arithmetic Under NTT

The [NTT](#page-52-0) converts elements of the ring R_q (where addition and multiplication are denoted by $+$ and \cdot , respectively) into elements of the ring T_q (where addition and multiplication are denoted by $+$ and ∘, respectively). This section gives explicit algorithms for linear algebra over the ring T_a .

The ring T_q is defined to be the direct product ring $\Pi_{i=0}^{255}\mathbb{Z}_q.$ Thus, an element $\hat a\in T_q$ is an array of length 256, and its elements are denoted by $\hat{a}[0], \hat{a}[1], \ldots, \hat{a}[255] \in \mathbb{Z}_a$.

Algorithm 44 [AddNTT](#page-54-1) (\hat{a}, \hat{b})

Computes the sum $\hat{a} + \hat{b}$ *of two elements* $\hat{a}, \hat{b} \in T_a$. **Input**: $\hat{a}, \hat{b} \in T_q$. **Output**: $\hat{c} \in T_a$. 1: **for from** 0 **to** 255 **do** 2: $\hat{c}[i] \leftarrow \hat{a}[i] + \hat{b}[i]$ 3: **end for** 4: **return** \hat{c}

```
AlgorithmMultiplyNTT(\hat{a}, \hat{b})
```
Computes the product $\hat{a} \circ \hat{b}$ *of two elements* $\hat{a}, \hat{b} \in T_a$.

Input: $\hat{a}, \hat{b} \in T_a$. **Output**: $\hat{c} \in T_a$. 1: **for from** 0 **to** 255 **do** 2: $\hat{c}[i] \leftarrow \hat{a}[i] \cdot b[i]$ 3: **end for** 4: **return** \hat{c}

Algorithm 46 [AddVectorNTT](#page-54-3) $(\hat{\mathbf{v}}, \hat{\mathbf{w}})$

Computes the sum $\hat{\mathbf{v}} + \hat{\mathbf{w}}$ *of two vectors* $\hat{\mathbf{v}}$, $\hat{\mathbf{w}}$ *over* T_a .

```
Input: \ell \in \mathbb{N}, \hat{\mathbf{v}} \in T_q^{\ell}, \hat{\mathbf{w}} \in T_q^{\ell}.Output: \hat{\mathbf{u}} \in T_q^{\ell}.
  1: for i from 0 to \ell - 1 do
  2: AddNTT}(\hat{\mathbf{v}}[i], \hat{\mathbf{w}}[i])3: end for
  4: return ̂
```
Algorithm 47 [ScalarVectorNTT](#page-55-0) (\hat{c}, \hat{v})

Computes the product $\hat{c} \circ \hat{v}$ *of a scalar* \hat{c} *and a vector* \hat{v} *over* T_a *.* **Input**: $\hat{c} \in T_q$, $\ell \in \mathbb{N}$, $\hat{\mathbf{v}} \in T_q^{\ell}$. **Output**: $\hat{\mathbf{w}} \in T_a^{\ell}$. 1: **for** i **from** 0 **to** $\ell - 1$ **do** 2: $\hat{\mathbf{w}}[i] \leftarrow \text{Multiply} \text{NTT}(\hat{c}, \hat{\mathbf{v}}[i])$ 3: **end for** 4: **return** ̂

Algorithm 48 [MatrixVectorNTT](#page-55-1) $(\hat{\mathbf{M}}, \hat{\mathbf{v}})$

Computes the product $\hat{M} \circ \hat{v}$ *of a matrix* \hat{M} *and a vector* \hat{v} *over* T_q *.*

Input: $k, \ell \in \mathbb{N}$, $\hat{\mathbf{M}} \in T_q^{k \times \ell}$, $\hat{\mathbf{v}} \in T_q^{\ell}$. **Output**: $\hat{\mathbf{w}} \in T_q^k$. 1: $\hat{\mathbf{w}} \leftarrow 0^k$ 2: **for** i **from** 0 **to** $k-1$ **do** 3: **for** j from 0 to $\ell - 1$ do 4: $\hat{\mathbf{w}}[i] \leftarrow \text{AddNTT}(\hat{\mathbf{w}}[i], \text{MultiplyNTT}(\hat{\mathbf{M}}[i, j], \hat{\mathbf{v}}[j]))$ $\hat{\mathbf{w}}[i] \leftarrow \text{AddNTT}(\hat{\mathbf{w}}[i], \text{MultiplyNTT}(\hat{\mathbf{M}}[i, j], \hat{\mathbf{v}}[j]))$ 5: **end for** 6: **end for** 7: **return** ̂

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Appendix A — Montgomery Multiplication

This document uses modular multiplications of the form $a \cdot b$ modulo q . This is an expensive operation that is often sped up in practice through the use of Montgomery Multiplication.

If a is an integer modulo q, then its *Montgomery form with multiplier* 2^{32} is $r \equiv a \cdot 2^{32}$ mod q. ^{[13](#page--1-14)}

Suppose that two integers u and v modulo q are in Montgomery form. Their product modulo q is $c = u \cdot v \cdot 2^{-32}$, which is also in Montgomery form. If the integer product of u and v does not overflow a 64-bit signed integer, then one can compute c by first performing the integer multiplication $u \cdot v$ and then "reducing" the product by multiplying by 2^{-32} modulo q . This last operation can be done efficiently as follows.

The MontgomeryReduce function takes an integer a with absolute value at most $2^{31}q$ as input. It returns an integer r such that $r = a \cdot 2^{-32}$ mod q. The output is in Montgomery form with multiplier 2^{32} mod q. An implementation would typically input a 64-bit input and return a 32-bit output. The "modulo 2^{32} " operation simply extracts the 32 least significant bits of a 64-bit value. The value $(a - t \cdot q)$ on line 3 is an integer divisible by 2^{32} . Therefore, the division consists of simply taking the most significant 32 bits of a 64-bit value. Extracting the four low- or high-order bytes is often done using typecasting.

Algorithm 49 [MontgomeryReduce](#page--1-11) (a)

Computes $a \cdot 2^{-32}$ mod q . **Input**: Integer a with $-2^{31}q \le a \le 2^{31}q$. **Output**: $r \equiv a \cdot 2^{-32} \text{ mod } q$. 1: QINV \leftarrow 58728449 > \geq the inverse of q modulo 2^{32} 2: t ← ((a mod 2^{32}) ⋅ QINV) mod 2^{32} 3: $r \leftarrow (a - t \cdot q)/2^{32}$ 4: return

With this algorithm, the modular product of a and b is $c =$ MontgomeryReduce $(a \cdot b)$, where a, b , and c are in Montgomery form. The return value of the algorithm is not necessarily less than q in absolute value, but it is less than $2q$ in absolute value. This is not a concern in practice since the objective of Montgomery Multiplication is to efficiently work with modular values that fit in a 32-bit register. If necessary, the result can be normalized to an integer in $(-q, q)$ using a comparison and an integer addition.

Converting an integer modulo q to Montgomery form by multiplying by 2^{32} modulo q is an expensive operation. When a sequence of modular operations is to be performed, the operands are converted once to Montgomery form. The operations are then performed, and the factor 2^{32} is extracted from the final result.

¹³This section does not distinguish between different versions of the "mod" operator. There are three such versions of " $x = a$ modulo q'' : i) $x \in [0, q - 1]$; ii) $x \in [-q/2], |q/2|]$; iii) $x \in [-q + 1, q - 1]$. The last version corresponds to the $\%$ " operator in most programming languages.

Appendix B — Zetas Array

The values $\zeta^{\text{BitRev}_8(k)}$ mod q for $k = 1, ..., 255$ used in the NTT Algorithms [41](#page-52-0) and [42](#page-53-0) may be pre-computed and stored in an array zetas $[1..255]$. This table of zetas is given below.

zetas[0..255] = {

Appendix C — Loop Bounds

There are four algorithms in this standard with loops that iterate an indeterminate number of times, though the expected number of iterations is a small constant in each case. Three of the four algorithms involve sampling from the output of an XOF, where the amount of output required from the XOF is proportional to the number of iterations that are performed.

Implementations **should not** bound the number of iterations in these loops or the amount of output that is extracted from the XOFs when executing these functions.^{[14](#page-61-2)} If an implementation bounds the number of iterations or the number of bytes that may be extracted from the XOF, it **shall not** use a limit lower than those presented in Table [3.](#page-61-1) The limits yield a probability of approximately 2^{-256} (or less) of being reached in a correct implementation of this standard. The probability is calculated under standard assumptions about the output distributions of XOFs and hash functions.

Table 3. While loop and XOF output limits for a 2−256 **or less probability of failure**

Implementations may limit the number of iterations of a while loop or the number of bytes drawn from the XOF to not exceed the maximum values in Table [3.](#page-61-1) If this option is used and the maximum number of iterations or XOF output bytes is exceeded, the algorithm shall destroy all intermediate results. If a return value or exception is produced, it shall be the same for any execution in which the maximum number of iterations or output bytes is exceeded.

There is essentially no performance penalty for using a larger than necessary limit, as the limit will only be reached on a faulty execution of the loop. Because of this, limits were chosen that lower-bound the probability of reaching them to 2^{-256} .

ML-DSA.Sign_internal

Table [1](#page-24-1) contains the expected repetitions in the rejection sampling loop of [ML-DSA](#page-34-1). Sign internal. These are 4.25, 5.1, and 3.85 for Categories 2, 3, and 5, respectively. Therefore, the probability that the number of repetitions exceeds *n* is less than or equal to $(\frac{5.1-1}{5.1})^n$ for all categories. Solving $(\frac{5.1-1}{5.1})^n \leq 2^{-256}$ yields $n = 814$.

RejBoundedPoly

Let X be the number of coefficients generated in n iterations of the while loop of RejBoundedPoly^{[15](#page-61-3)}. Then X is $Binomial(2n, \theta)$, where θ is either $\frac{9}{16}$ or $\frac{15}{16}$, depending on the parameter η . For $\theta = \frac{9}{16}$, the probability that fewer than 256 coefficients are generated in 481 iterations of the main loop in [RejBoundedPoly](#page-46-1) is less than 2^{-256} . Each iteration consumes one byte of output from [H.](#page-22-1)

¹⁴[RejBoundedPoly](#page-46-1), [RejNTTPoly](#page-46-0), and [SampleInBall](#page-45-1) use the incremental APIs described in Section [3.7](#page-22-1) in order to extract the amount of output needed from the XOFs, given that the amount needed is not known in advance.

 15 Note that 0, 1, or 2 coefficients are generated in each iteration.

RejNTTPoly

The number of valid coefficients generated in n calls to G.[Squeeze](#page-22-1) in [RejNTTPoly](#page-46-0) is $Binomial(n, 2^{-23}q)$. It follows that after 298 calls, the probability of failure is less than 2^{-256} . Each iteration consumes three bytes of output from [G.](#page-22-1)

SampleInBall

Step 9 in [SampleInBall](#page-45-1) is executed every time a pseudorandom byte is greater than a value i in the range [256 $-\tau$, 255]. The parameter τ is 39, 49, and 60 for categories 2, 3, and 5, respectively. Therefore, the probability that this step is executed more than n times in a single iteration of the for loop is less than or equal to $(\frac{59}{256})^n \le (\frac{\tau}{256})^n$. Solving $(\frac{59}{256})^n \le 2^{-256}$ yields a bound of $n=121$ for the while loop on step 8 of [SampleInBall](#page-45-1). Each iteration consumes one byte of output from [H.](#page-22-1)

Each call to Sample In Ball extracts eight bytes from [H](#page-22-1) and then performs τ iterations of the for loop, each of which extracts an indeterminate amount of data from H . The probability that more than n bytes of output will be required from [H](#page-22-1) during an execution of [SampleInBall](#page-45-1) for a given value of τ is

$$
P(n,\tau) = \left\{ \begin{array}{cc} 1 & \text{if } n \leq 8 \\ 0 & \text{if } \tau = 1 \text{ and } n > 8 \\ \left(\frac{257-\tau}{256}\right)P(n-1,\tau-1)+\left(\frac{\tau-1}{256}\right)P(n-1,\tau) & \text{if } \tau > 1 \text{ and } n > 8 \end{array} \right. .
$$

 $P(n, 60)$ is less than 2^{-256} when n is 221 or greater. Implementations may limit the number of bytes extracted from [H](#page-22-1) to $n \geq 221$. Such implementations must stop the execution of [SampleInBall](#page-45-1), return a constant that represents an error and no other output, and destroy all intermediate results after n bytes of output have been consumed.

Appendix D — Differences from the CRYSTALS-DILITHIUM Submission

ML-DSA is derived from Version 3.1 of CRYSTALS-DILITHIUM [\[6\]](#page-56-6). Version 3.1 differs slightly from the most recent version that appears on the NIST website (i.e., Version 3 CRYSTALS-DILITHIUM [\[5\]](#page-56-5)). Appendices [D.1,](#page-63-1) [D.2,](#page-63-2) and [D.3](#page-63-3) document the differences between Versions 3 and 3.1, the differences between Version 3.1 and the initial public draft of the ML-DSA, and the differences between the initial public draft and the ML-DSA standard as published in this document, respectively.

D.1 Differences Between Version 3.1 and the Round 3 Version of CRYSTALS-DILITHIUM

The lengths of the variables ρ' (private random seed) and μ (message representative) in the signing algorithm were increased from 384 to 512 bits. The increase in the length of μ corrects a security flaw that appeared in the third-round submission, where a collision attack against SHAKE256 with a 384-bit output would make it so that parameters targeting NIST security strength category 5 could only meet category 4 [\[32\]](#page-58-5).

Additionally, the length of the variable tr (the hash of the public key) was reduced from 384 to 256 bits. In key generation, the variable ς was relabeled as ρ' and increased in size from 256 bits to 512 bits.

D.2 Differences Between Version 3.1 of CRYSTALS-DILITHIUM and FIPS 204 Initial Public Draft

In order to ensure the properties noted in [\[14\]](#page-57-0), ML-DSA increases the length of tr to 512 bits and increases the length of \tilde{c} to 384 and 512 bits for the parameter sets ML-DSA-65 and ML-DSA-87, respectively. In draft ML-DSA, only the first 256 bits of \tilde{c} are used in the generation of c .

In Version 3.1 of the CRYSTALS-DILITHIUM submission, the default version of the signing algorithm is deterministic with ρ' being generated pseudorandomly from the signer's private key and the message, and an optional version of the signing algorithm has ρ' sampled instead as a 512-bit random string. In ML-DSA, ρ' is generated by a "hedged" procedure in which ρ' is pseudorandomly derived from the signer's private key, the message, and a 256-bit string *rnd*, which **should** be generated by an Approved RBG by default. The ML-DSA standard also allows for an optional deterministic version in which rnd is a 256-bit constant string.

The draft ML-DSA standard also included pseudocode that unintentionally omitted a check for malformed input while unpacking the hint [\[33\]](#page-58-6). Failure to perform this check results in a signature scheme that is not strongly existentially unforgeable [\[34\]](#page-58-7).

D.3 Changes From FIPS 204 Initial Public Draft

In the final version of the ML-DSA standard, the omitted malformed input check was restored to the hint unpacking algorithm (Algorithm [21\)](#page-41-1). Additionally, in the final version of ML-DSA, all of the bits of \tilde{c} are used in the generation of c (Algorithm [29\)](#page-45-1), and [ExpandMask](#page-47-2) (Algorithm [34\)](#page-47-2) is modified to take output bits from the beginning of the output of [H.](#page-22-1)

Based on comments that were submitted on the draft version, more details were provided for the pre-hash version HashML-DSA in Section [5.4.](#page-27-1) These modifications include domain separation for the cases in which the message is signed directly and cases in which a digest of the message is signed. The changes were made by explicitly defining external functions for both versions of the signing and verification functions

that call an internal function corresponding to the signing or verification functions from the draft FIPS. Domain separation is included in the input to the internal function (see Algorithms [2,](#page-27-2) [3,](#page-27-3) [4,](#page--1-3) [5,](#page--1-4) [7,](#page-34-1) and [8\)](#page-36-0). To simplify APIs and for testing purposes, this document also introduced a similar external/internal split for key generation (see Algorithms [1](#page-26-3) and 6), but this is a purely editorial change, as the external key generation algorithm is functionally equivalent to the key-generation algorithm from the draft FIPS.

Finally, to offer misuse resistance against the possibility that keys for different parameter sets might be expanded from the same seed $[35]$, domain separation was added to line [1](#page-32-1) of Algorithm [6.](#page-32-1)