

The Consolidation of the Beam Interlocks System

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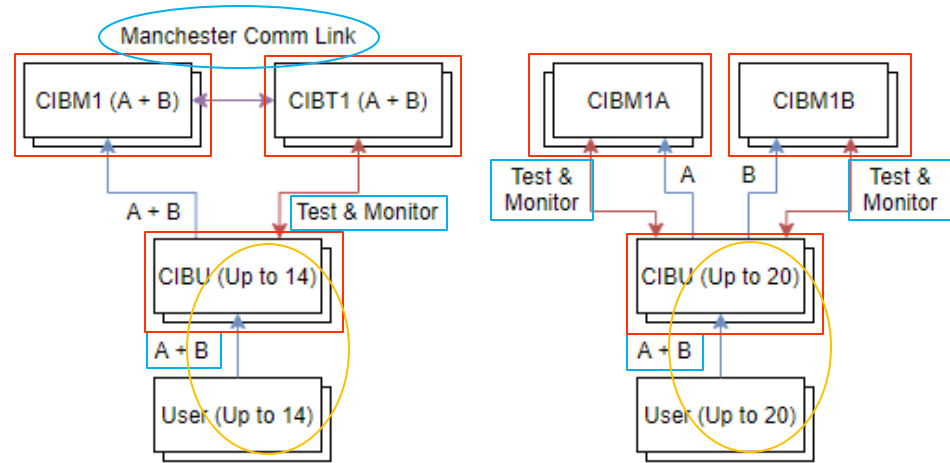
BIS2 – Motivation for Change

BIS1:

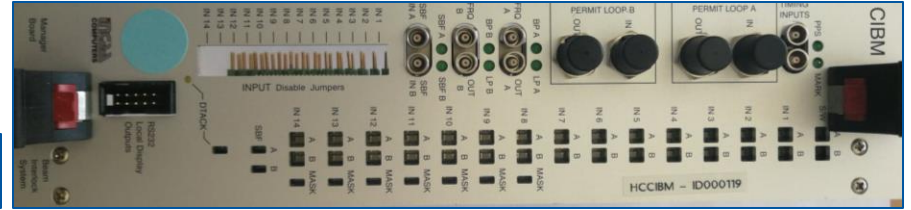
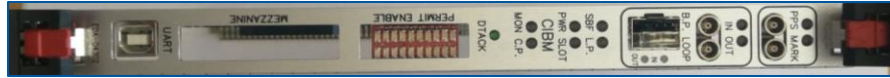
- 2x CIBMs per crate, each interlocking A & B
- 2x CIBTs per crate, handling Test + Monitor
- Can interlock up to 14 users

BIS2:

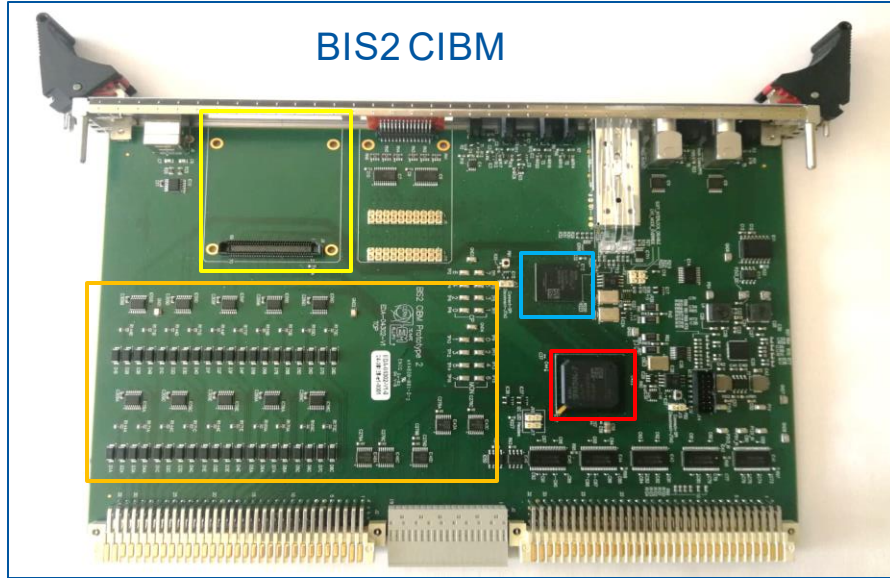
- 4x CIBMs per crate, each interlocking A or B
- All CIBMs now handle Test + Monitor
- Can interlock up to 20 users



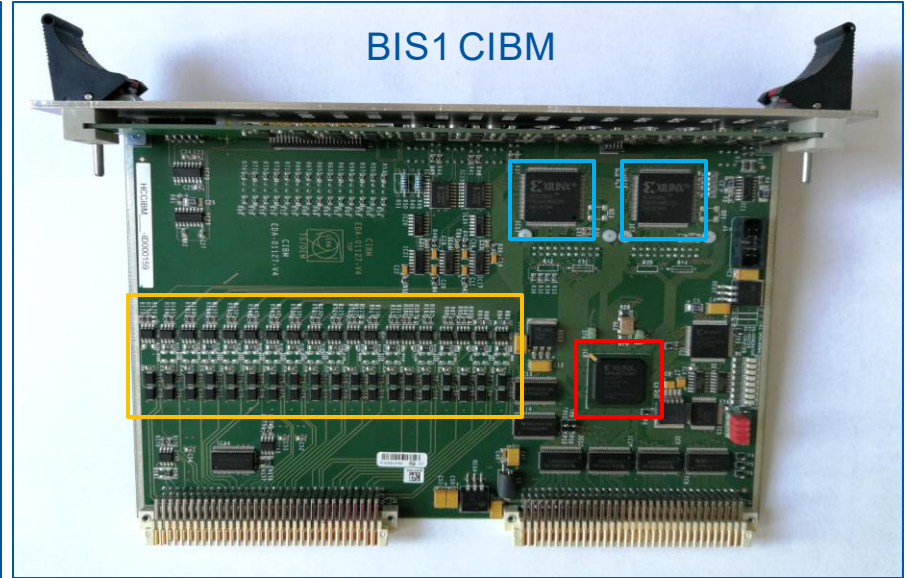
CIBM Architecture



BIS2 CIBM



BIS1 CIBM



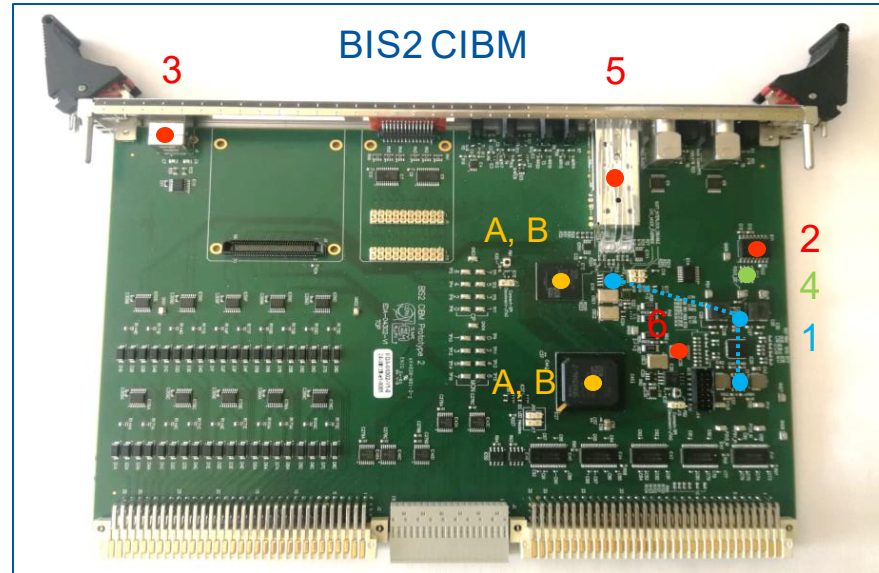
New Peripherals

I2C:

1. Temperature Sensors
2. RTC (Real Time Clock)
3. UART
4. EEPROM
5. SFP
6. ADC

FPGA - XADC:

- A. Temperature Sensors
- B. Power Supplies



Future



4 BIS2 CIBMs under test

BIS2 Crate

Space for new and exciting BIS2 systems

Thanks for Watching

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