

2002-039

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VXIbus-BASED SIGNAL GENERATOR FOR RESONANT POWER SUPPLY SYSTEM OF THE 3 GeV RCS

March 2002

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(DJapan Atomic Energy Research Institute, 2002 編集兼発行 日本原子力研究所

JAERI-Tech 2002-039

VXlbus-based Signal Generator for Resonant Power Supply System of the 3 GeV RCS

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> > (Received February 1, 2002)

The 3 GeV Proton RCS of the JAERI-KEK Joint Project is a 25 Hz separate-function rapid cycling synchrotron under design. Bending magnets (BM) and quadrupole magnets (QM) are excited separately. The 3 GeV RCS requests above IO families of magnets excited independently, far beyond 3 families in practical RCS's. Difficulty of field tracking between BM and QM is significantly increased.

Magnet strings are grouped into resonant networks and excited resonantly with power supplies driven by a waveform pattern, typically a DC-biased sinusoidal signal. To achieve a close tracking between many families, the driving signal of each power supply should be adjusted in phase and amplitude flexibly and dynamically.

This report proposes a signal generator based on VXlbus. The VXIbus, an extension of VMEbus (VME extensions for Instrument), provides an open architecture with shared process bus and timing. The VXfbus-based signal generator facilitates the timing synchronization and is easy to extend to many channels needed by the 3 GeV RCS. Experimental results of **the** signal generator are reported.

Keywords: Joint Project, 3GeV Synchrotron, Resonant Power Supplies, Signal Generator, Tracking

On leave from Hitachi, Ltd.

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VXIbus を用いた 3GeV RCS 共振電源信号発生器

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(2002年2月1日 受理)

JAERI-KEK 大強度陽子加速器統合計画の 3GeV シンクロトロンの設計は、スケジュールどおり に進行中である。この 3GeV 陽子加速器は, 機能分離型のラピッド・サイクリング・シンクロト ロン(RCS)であり,偏向電磁石(BM)および四極電磁石(QM)は,DC バイアスされた 25Hz 正弦波 のパターン電流で励磁される。これらを直接励磁した場合には膨大な無効電力が発生してしま **うため、共振ネットワークを構築して励磁する。**

電磁石は、12のファミリーに分けられ、それぞれ独立に励磁される。このファミリー数は、こ れまでの加速器での実績である3ファミリーを遥かに超えている。このため, B/Q(BM, QM)間 の磁場トラッキングをとることに困難が予想される。磁場トラッキングは、電磁石電源の通電 パターン設定用の信号源に大きく依存するため、ダイナミックかつフレキシブルな信号発生器 の開発が必要である。

このレポートでは、VXIbus を用いた信号発生器を提案する。VXIbus (VME eXtensions for Instrument)は、VMEbusの拡張であり、プロセス・バスおよびタイミングを共有するオープン・ アーキテクチャを提供する。VXIbusを用いた信号発生器は,信号同期および多チャンネルを実 現するために有利である。このため、信号発生器の所要性能を検討し、VXIbusを用いて信号発 生器を構築した。さらに,実験を行い,基本オペレーションなどに関して所期の性能を確認し た。

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1. **Introduction**

The 3 GeV proton synchrotron 3 GeV RCS) of the JAERI-KEK Joint Project (JKJ Project) for Highintensity Proton Accelerators [1] is a 25 Hz separate function rapid cycling synchrotron (RCS) that will be constructed at the JAERI Tokai site on schedule (Fig. 1-1).

Fig. 1-1 Facility complex of the JAERI-KEK Joint Project for High-intensity Proton Accelerators

In a separate function RCS, magnetic excitation is carried out with bending magnets (BM) and quadrupole magnets (QM: QF or QD) in separate families. The three magnet families, BM, QF and QD, are excited either in one magnet string (e.g. ISIS 500 MeV Proton Synchrotron [2]) or in independent magnet strings (e.g. BESSY-II Electron Booster [3]). But for the 3 GeV RCS of the JKJ Project, above 10 magnet families are requested to excite independently 41, far beyond 3 families in practical RCS's constructed in the world. Difficulty of B/Q field tracking between BM and QM is significantly increased. B/Q tracking is an essential requirement for stabilizing the betatron tunes in a synchrotron by tracking the focusing (defocusing) magnetic field closely to the bending magnetic field. Since each magnet string is grouped into a resonant network and excited resonantly by a power supply typically with a DC-biased sinusoidal pattern as shown in Fig. 1-2. Signal generator for the driving pattern of the power supply plays a crucial role in performing the field tracking between many magnet families.

Fig. 1-2(a) One family of magnet string in resonant network and its power supply

Fig. 1-2(b) Pattern of the power supply for bending magnets

To achieve close tracking, the driving signal of each power supply should be adjusted flexibly and dynamically in phase and amplitude. Waveform distortion, resulted from magnetic saturation, also contributes to the tracking errors, but the subject is not covered in this report. According to the proposed timing control of the 3 GeV RCS, the magnetic excitation is synchronized and triggered by a master trigger generated from the master clock of the accelerator complex [5]. Therefore the signal generator is obliged to have the following features.

- External trigger
- Dynamic phase adjustment (phase advance or lag)
- Dynamic amplitude adjustment
- Waveform adjustment without discontinuity
- Many channels
- etc.

This report introduces a VXlbus-based control system as illustrated in Fig. 13, and a signal generator is proposed for the resonant power supplies of the 3 GeV RCS.

2 3 GeV RCS Power Supply System for Lattice Magnets

The 3 GeV RCS accelerates a high-intensity proton beam from 400 MeV to 3 GeV at a repetition rate of 25 Hz. With designed beam power of I MW it will be used to produce pulsed spallation neutrons and muons and works as an injector as well for a 50 GeV synchrotron.

The magnetic field cycle repeats in the form of DC-biased sinusoidal pattern at 25 Hz as shown in Fig. 1-2(b). To achieve a high intensity with low beam losses, betatron tunes, beam collimation, physical aperture and injection schemes are carefully designed and the present lattice version has a variety of features such as high transition gamma, long dispersion-free straight sections, etc. 4].

Bending magnet string and about IO families of quadrupole magnets (QM's) are excited separately at a repetition rate of 25 Hz. The magnet string of each family is grouped into meshes and excited resonantly to avoid large amount of reactive power. Power supplies for above 10 families of main lattice magnets should be designed and be available on schedule for the construction of the accelerator complex. Based on the past R&D's $[6,7]$, we are carrying on the design of the power supply system $[8,9,10,11]$.

2.1 Tracking of Magnetic Field of Bending and Quadrupole Magnets

The magnetic field of each family of the QM's should closely track the BM field. The essential tracking requirement for synchrotron is described as follows [12].

$$
\Delta v = \frac{1}{4\pi} \oint \Delta k \beta(s) ds = \frac{1}{4\pi} \oint \frac{\Delta k}{k} \cdot k \beta(s) ds \approx \frac{\Delta k}{k} \cdot \xi_{\text{name}} \tag{2-1}
$$

but $k = \frac{8}{3}$ (2-2) $k = \frac{g}{B\rho}$

where,

 Δv : betatron tune shift,

 k : quadrupole strength,

 $\beta(s)$: betatron amplitude function,

4nature: natural chromaticity,

g: field gradient of a quadrupole magnet,

B: magnetic field density of a bending magnet, and

 ρ : bending radius of a bending magnet.

From above expressions, quadrupole strength k is required to keep constant during the operation cycle for injection, acceleration and extraction. The tune shift Δv , resulted from the quadrupole strength deviation is inevitable in practice due to deviation in phase and amplitude or due to waveform distortion. We estimate the deviation resulted from phase and amplitude in field gradient of quadrupoles as described below.

BM field density (B) and QM field gradient (g) are given by:

$$
g = g_{dc} - g_{ac} \cdot \cos(\omega_0 t) = g_{dc} \cdot (1 - \alpha \cdot \cos(\omega_0 t)) \tag{2-3a}
$$

$$
B = B_{ac} - B_{ac} \cdot \cos(\omega_0 t) = B_{dc} \cdot (1 - \alpha \cdot \cos(\omega_0 t)) \tag{2-3b}
$$

We define

$$
k_0 = \frac{g_{dc}}{B_{ac}\rho} = \frac{g_{ac}}{B_{ac}\rho}, \alpha = \frac{B_{ac}}{B_{dc}} = \frac{g_{ac}}{g_{dc}}
$$

Assuming that QM field gradient (g) has phase error $\Delta\theta$ with respect to BM and amplitude error of Δg , we can derive the quadrupole strength deviation as follows.

$$
\left(\frac{\Delta k}{k_0}\right)_{\Delta\theta} \cong \frac{\alpha \cdot \sin(\omega_0 t)}{1 - \alpha \cdot \cos(\omega_0 t)} \cdot \Delta\theta \le \frac{\alpha}{\sqrt{1 - \alpha^2}} \cdot \Delta\theta \tag{2-4a}
$$

$$
\left(\frac{\Delta k}{k_0}\right)_{\Delta g} = \frac{1}{1 - \alpha \cdot \cos(\omega_0 t)} \cdot \frac{\Delta g_{dc}}{g_{dc}} - \frac{\alpha \cdot \cos(\omega_0 t)}{1 - \alpha \cdot \cos(\omega_0 t)} \cdot \frac{\Delta g_{ac}}{g_{ac}}
$$
\n
$$
\left(\frac{\Delta k}{k_0}\right)_{\Delta g_{ac}} \le \frac{1}{1 - \alpha} \cdot \frac{\Delta g_{dc}}{g_{dc}}, \left(\frac{\Delta k}{k_0}\right)_{\Delta g_{ac}} \le \frac{\alpha}{1 - \alpha} \cdot \frac{\Delta g_{ac}}{g_{ac}}
$$
\n
$$
(2-4b)
$$

Statistically, the deviation tolerance can be expressed as:

$$
\left(\frac{\Delta k}{k_0}\right)_{rms} = \sqrt{\frac{\alpha^2}{1-\alpha^2}\Delta\theta^2 + \frac{1}{(1-\alpha)^2}\cdot\left(\frac{\Delta g_{dc}}{g_{dc}}\right)^2 + \frac{\alpha^2}{(1-\alpha)^2}\cdot\left(\frac{\Delta g_{ac}}{g_{ac}}\right)^2}
$$
(2-5)

Given $\zeta_{\textit{nature}}$ =8.9 and α =0.61 in present design, the error tolerance can be estimated as shown in Fig. 2-1.

Supposing $\left(\frac{46d_c}{c}\right)_{cm} = \left(\frac{46d_c}{c}\right)_{cm} = 10^{-4}$, $\Delta\theta_{cm} \le 1.37$ mrad is required for $\Delta V \le 0.01$. *9d, ga,*

$$
-5- \nonumber\\
$$

Fig. 2-1 Estimation of error tolerance for tune shift $\Delta \nu = 0.01$, 0.02 and 0.05

2.2 Resonant Network Analyses

In an RCS, magnets are commonly configured as a resonant circuit named "White circuit", and powered by an AC power source [13]. The resonant current is generally DC-biased to achieve a full use of the sinusoidal waveform for beam injection and acceleration in one magnetic cycle. This general configuration of RCS avoids the generation of large amount of reactive power in magnet strings. Magnets are usually grouped into meshes to work at moderate resonant voltage. Each mesh is connected in series so that a uniform waveform in each magnet is achieved.

Fig. 22 Configuration of power supply system in parallel resonance (PR) or in series resonance (SR)

Figure 2-2 shows a one-mesh magnet/power supply configuration in RCS. An AC power source and a resonant circuit can be configured in parallel resonance (PR), or in a configuration variation with the power supply inserted in the resonant circuit in series (SR).

Fundamentally, the resonant network is driven by a current source (Is) in PR configuration or by a voltage source (Vs) in SR configuration. With a simple AC model of the resonant circuit, circuit response is analyzed as follows.

The transfer function of the resonant circuit is

$$
\frac{I_m}{I_s} = \frac{1}{n} \cdot \frac{\omega_1^2}{s^2 + (\frac{\omega_0}{Q})s + {\omega_0}^2}
$$
 for PR, (2-6a)

$$
\frac{I_m}{V_s} = \frac{1}{L_m s} \cdot \frac{s^2 + (\frac{\omega_0}{Q})s + \omega_2^2}{s^2 + (\frac{\omega_0}{Q})s + \omega_0^2}
$$
 for SR, (2-6b)

where,
$$
\omega_0^2 = \frac{L_m + L_{ch}}{L_m L_{ch} C_m}
$$
 is the resonant frequency, $Q = \omega_0 C_m R_e = \frac{R_e \cdot (L_{ch} + L_m)}{\omega_0 L_{ch} L_m}$ is the quality

factor of the resonant circuit, $\omega_1^2 = \frac{1}{L_1C_2}$, $\omega_2^2 = \frac{1}{L_1C_2}$, R_e is the equivalent resistance for AC losses

and $1:n$ -turns ratio (primary/secondary) of the choke transformer in PR.

Phase of the transfer function is derived in eqs. (2-7) for PR and SR respectively:

$$
\phi(\frac{I_m}{I_s}) = -\arctan\left[\frac{\omega_0 \omega}{Q(\omega_0^2 - \omega^2)}\right]
$$
 for PR (2-7a)

$$
\phi\left(\frac{I_m}{V_s}\right) = \arctan\left[\frac{\omega_0 \omega}{Q(\omega_2^2 - \omega^2)}\right] - \arctan\left[\frac{\omega_0 \omega}{Q(\omega_0^2 - \omega^2)}\right] - \frac{\pi}{2}
$$
 for SR (2-7b)

or

or

Therefore,
\n
$$
\Delta \phi = \frac{\partial \phi}{\partial \omega_0} \Delta \omega_0 + \frac{\partial \phi}{\partial Q} \Delta Q = -\frac{Q \omega \cdot (({\omega_0}^2 - {\omega}^2) - 2{\omega_0}^2) \cdot \Delta \omega_0 - {\omega_0} {\omega} ({\omega_0}^2 - {\omega}^2) \cdot \Delta Q}{Q^2 ({\omega_0}^2 - {\omega}^2)^2 + ({\omega} {\omega_0})^2}
$$

$$
\Delta \phi(\omega_0) = \frac{2Q}{\omega_0} \cdot \Delta \omega_0
$$
 at $\omega = \omega_0$ for PR, (2-8a)

or, for SR, we have

$$
\Delta \phi = \frac{\partial \phi}{\partial \omega_0} \Delta \omega_0 + \frac{\partial \phi}{\partial Q} \Delta Q
$$

$$
=\frac{Q\omega\cdot(\omega_2^2-\omega^2)\cdot\Delta\omega_0-\omega_0\omega(\omega_2^2-\omega^2)\cdot\Delta Q}{Q^2(\omega_2^2-\omega^2)^2+(\omega\omega_0)^2}\n-\frac{Q\omega\cdot((\omega_0^2-\omega^2)-2\omega_0^2)\cdot\Delta\omega_0-\omega_0\omega(\omega_0^2-\omega^2)\cdot\Delta Q}{Q^2(\omega_0^2-\omega^2)^2+(\omega\omega_0)^2}
$$

At $\omega = \omega_0$, we get

$$
\Delta \phi(\omega_0) = \frac{Q \omega_0 \cdot (\omega_2^{2} - {\omega_0}^{2}) \cdot \Delta \omega_0 - {\omega_0}^{2} (\omega_2^{2} - {\omega_0}^{2}) \cdot \Delta Q}{Q^2 (\omega_2^{2} - {\omega_0}^{2})^{2} + {\omega_0}^{4}} + \frac{2Q}{\omega_0} \cdot \Delta \omega_0
$$

Supposing $L_{ch} = k \cdot L_m$, we get $\omega_0^2 = (1 + k) \cdot \omega_2^2$, and therefore

$$
\Delta \phi(\omega_0) = \left(-\frac{k(1+k)}{(kQ)^2 + (1+k)^2} + 2\right) \cdot \frac{Q}{\omega_0} \cdot \Delta \omega_0 - \frac{k(1+k)}{(kQ)^2 + (1+k)^2} \cdot \Delta Q \cong 2Q \cdot \left(\frac{\Delta \omega}{\omega_0}\right)
$$
\n(2-8b)

Response to a step signal helps us to understand the performances in real time.

In PR, we get

$$
I_m(s) = \frac{1}{n} \cdot \frac{\omega_1^2}{s^2 + (\frac{\omega_0}{Q})s + \omega_0^2} \cdot \frac{1}{s} = \frac{1}{n} \cdot \frac{-\frac{\omega_1^2}{\omega_0}(\frac{1}{\omega_0} \cdot s + \frac{1}{Q})}{s^2 + (\frac{\omega_0}{Q})s + \omega_0^2} + \frac{\omega_0^2}{s^2}
$$

and

$$
I_{m}(t) = \frac{1}{n} \cdot (\frac{\omega_{1}}{\omega_{0}})^{2} \cdot (1 - e^{-\frac{\omega_{0}}{2Q}t} \frac{2Q}{\sqrt{4Q^{2} - 1}} \cdot \cos(\sqrt{1 - \frac{1}{4Q^{2}}} \cdot \omega_{0}t - \varphi))
$$
(2-9a)

but $\varphi = \arctan(\theta)$ $\sqrt{4Q^2-1}$

Similarly, in SR we obtain

$$
I_m(s) = \frac{1}{L_m s} \cdot \frac{s^2 + (\frac{\omega_0}{Q})s + \omega_2^2}{s^2 + (\frac{\omega_0}{Q})s + \omega_0^2} \cdot \frac{1}{s} = \frac{1}{L_m} \cdot \frac{(1 - \frac{\omega_2^2}{\omega_0^2}) \cdot (-\frac{s}{\omega_0 Q} + (1 - \frac{1}{Q^2}))}{s^2 + (\frac{\omega_0}{Q})s + \omega_0^2} + \frac{(1 - \frac{\omega_2^2}{\omega_0^2}) \cdot \frac{s}{\omega_0 Q} + \frac{\omega_2^2}{\omega_0^2}}{s^2})
$$

and

$$
I_m(t) = \frac{1}{L_m} \cdot \left(\frac{(1 - \frac{\omega_2^2}{\omega_0^2})}{\omega_0 Q} \left(1 - e^{-\frac{\omega_0}{2Q}t} \cdot \sqrt{1 + \frac{(2Q^2 - 1)^2}{4Q^2 - 1}} \cdot \cos(\omega_0 \sqrt{1 - \frac{1}{4Q^2}t + \varphi}) + \frac{\omega_2^2}{\omega_0^2} \cdot t\right)\right)
$$
(2-9a)

but,
$$
\varphi = \arctan(\frac{2Q^2 - 1}{\sqrt{4Q^2 - 1}})
$$
.

(Notice: the terms of the equations related to the response to DC component is not in correct form due to the transfer function is based on AC equivalent circuit.).

From the expressions derived above, we can describe the circuit responses with network parameters, i.e. quality factor Q and resonant frequency ω_0 . The analyses help us to understand the performances of the resonant circuits, some of which can be concluded as below.

- Phase shift due to resonant frequency variation: $\Delta\phi(\omega_0) = 2Q \cdot (\frac{\Delta\omega_0}{\sigma^2})$ $\boldsymbol{\omega}_{0}$
- Time constant of amplitude variation for resonant current $\tau = \frac{2Q}{\pi}$ $\omega_{_0}$

$$
\bullet \quad \omega_0 = \sqrt{\frac{L_m + L_{ch}}{L_m C_m}}, Q = \omega_0 C_m R_e = \frac{R_e \cdot (L_{ch} + L_m)}{\omega_0 L_{ch} L_m}
$$

3. VXIbus-based Control System

3.1 Introduction to VXlbus

VXIbus VME extensions for Instrument) is a fast-growing platform for instrument systems 14]. Because of many limitations in VME specifications-particularly for analog conversion, the major instrument manufactures developed the specifications for VXIbus, and VXIbus was adopted as IEEE Standard II 55 in March 1993. VXI is now used in many applications ranging from test and measurement and ATE (Automatic Test Equipment), to data acquisition and analysis in both research and industrial automation.

VXI-an open architecture with shared process bus and timing (Fig.3-1) offers the numerous benefits below.

Fig.3-1 VXI: an open architecture with shared process bus and timing

- Open standard with extensive multivendor support from many of largest instrument suppliers, including National Instruments, Hewlett-Packard and Tektronix, etc.
- Modular approach for system configuration with off-the-shelf building blocks that meet specific application needs.
- Easy programming and integration with standardized VXIplug&play software because of standards for both hardware and software combined by the VXI Consortium and VXlplug&play System Alliance.
- A precision clock for timing and trigger lines for event handling-to facilitate high-performance systems.
- Increased system throughput reduces test time and increases capabilities.

Mechanical configuration with smaller size and higher density facilitates mobility or portability and access to **the** test and control devices.

To support the open multivendor architecture, members of VXIplug&play Systems Alliance [15] have implemented common standards in both hardware and software. All hardware that is compatible with the VXIbus Specifications is applicable to Xlplug&play frameworks. The alliance also achieves interoperability for system software including operating system, programming language, I/O drivers, instrument drivers and higher-level application software tools.

3.2 Access to VXIbus Device

VXIbus devices are configured and accessed by the following ways.

1). Hardware Registers

VXI modules have a specific set of registers for hardware configuration. The following is the description of VXI configuration registers.

- Upper 16kB of A16 space reserved for VXI configuration space
- 64B per device
- 8-bit logical address specifies base address for each device
- 256 devices per VXI system

Each VXI device has an 8-bit logical address, which is analogous to GPIB address of a GPIB device and can be manually set or automatically configured by the system at startup.

2). Register-based Devices

With VXI configuration registers-the common register set for all VXI devices, the system can identify each VXI device (its type, model/manufacturer, address space, and memory requirements). System and memory configuration can be performed automatically with the Resource Manager (RM) a centralized soft module for system initialization.

3). Message-based Communication and Word Serial Protocol

VXIbus specification defines message-based devices in addition to register-based devices. All messagebased VXIbus devices have communication register and configuration registers and can communicate at a minimum level in a standard way using VXI-specific Word Serial Protocol, which is very similar to IEEE-488 protocol.

4) Commander/Servant Hierarchies

VXI Commander/Servant Hierarchies is a communication protocol using conceptual layers of VXI devices to construct hierarchical system. The structure is like an inverted tree in which a Commander device has one or more associated lower-level Servant devices. Commanders communicate with Servants through communication registers of Message-based servants using the Word Serial Protocol or by devicespecific register manipulation for Register-based servants. Servants communicate with their Commander by responding to the Word Serial commands and queries, or by device-specific register status.

5). Interrupts and Asynchronous Events

Servants can communicate asynchronous status and events to their Commander through hardware interrupts or by writing specific messages (signals) directly to their Commander's hardware Signal Register.

3.3 Ways to Control a VXI System

Slot 0 controllers provide the communication path between other modules in the mainframe and its host computer. Slot 0 controllers can be divided into three categories:

1). GPlB-VXI interface module: The GPIB-VXI interface transparently translates the GPIB protocol to and from the VXI Word Serial protocol.

2) Embedded Controller: this is an embedded computer module offering direct connection to VXI backplane.

3). MXIbus (Multisystem eXtensions for Instrumentation bus): MXIbus links an external computer to control the VXI backplane. The external computer operates as though it is embedded directly inside the VXI mainframe.

3.4 VXlbus Interface Software

1). VISA: Virtual Instrument System Architecture

As a standard I/O Application Programming Interface (API) for instrumentation programmings defined by the VXIplug&play Systems Alliance, VISA can control VXI, GPIB, PXI or serial instruments for either message-based communication or register-based communication.

2). lVI: Interchangeable Virtual Instruments

Interchangeable Virtual Instruments defined more standardization to instrument divers and can do instrument simulation [16]. Interchangeability is achieved through generic class drivers such as oscilloscope, DMM, arbitrary waveform/furiction generator, switch and power supply, in which an instrument is controlled by using a set of function and attributions.

4. Signal Generator Scheme and Experiment

Signal generators to drive RCS magnet power supplies differ in accelerator facilities since requested tracking tolerance is not the same and each facility has different development story. We introduce the schemes of two existing RCS's in BESSY 11 (Berlin, Germany) and SRRC (Hsinchu, Taiwan) before going into the details of the scheme proposed for the 3 GeV RCS.

4.1 Signal Generator Scheme for BESSY 11 and SRRC

BESSY II and SRRC are both 3rd generation synchrotron radiation light sources in which RCS boosters are used to shorten beam filling time. Both are excited at 10 Hz from injection energy of 50 MeV to extraction energy of 1.5 GeV (BESSY II) and 1.9 GeV (SRRC) respectively and either drives three independent 10 Hz White circuits for dipoles and two quadrupole families. Since the absolute tracking tolerance may differ each other, BESSY 11 booster synchrotron adopted Dual Arbitrary Waveforni Generator ADS, a commercial signal generator by Robde&Schwarz and a circuitry unit is implemented for magnetic field regulation and phase synchronization. On the other hand, in recent upgrade SRRC developed a VME module for 10 Hz signal generator with control interface for phase and amplitude control. Table I is a brief comparison of the signal generators of BESSY 11 Booster, SRRC booster and the proposed 3 GeV RCS [3,4,17,18].

4.2. Signal Generator Scheme Proposed for the 3 GeV RCS

In the proposed control system shown in Fig. 1-3, a VXI module (Wavetek Model 1375) is used as the signal generator, It is triggered and synchronized by a 25 Hz master trigger signal obtained from a tentative timing system. The other blocks are functioned with VXI modules or with software implementation as follows.

1). Signal generator: Wavetek Model 1375 20 MS/s Arbitrary Waveform Generator, triggered and synchronized (externally) by master trigger signal.

2). Phase delay (or advance) block: the phase delay (or advance) is carried out by memory shift of the sinusoidal pattern instead of using converter circuit.

3). Time interval converter: time interval is measured and obtained directly using Hp El 333A (counter).

4). Software environment: LabVIEW [19].

Figure 4-1 illustrates the programming flow diagram and the control scheme of the system. Omitting the DC-bias, we create the sinusoidal pattern with the following memory data:

$$
(2^{12}-1)\cdot[1+\sin(2\pi\cdot\frac{n-\frac{N-M}{2}}{M})]/2, n=0,1,2,...,N-1
$$

where, N is the memory length, $M \ll N$ is the length for one repetition period, and the 12-bit vertical data ranges from minimum through maximum with the midpoint corresponding the zero of the sinusoidal pattern. The signal generator outputs one period of data with memory shift index p as the start point, namely:

$$
(2^{12}-1)\cdot[1+\sin(2\pi\cdot\frac{n-\frac{N-M}{2}}{M})]/2,n=p,p+1,p+2,...,p+M-1
$$

where $p=[0, N-M].$

The signal phase can be shifted by $\pm \frac{N-M}{\sqrt{N}} \cdot \frac{2\pi}{N}$ with step of $\frac{2\pi}{N}$ as shown in Fig.4-1(a). Signal 2 *M* M patterns M_0 , M_1 and M_2 illustrates typically the cases for in phase, phase advance and phase lag respectively.

Occurring upon the request from the external trigger, the VXI Events process the adjustments of the phase and amplitude of the waveform so that the adjustments can be carried out with correct timing in defined time interval. As shown in Fig.4-1(b), the scheme enables the events to be counted to KE and phase and amplitude adjustments are applied at Event No.KA and KB respectively.

4.3 Experiment

An experimental set of the VXI-based signal generator is built to confirm the scheme and performances. Figure 42 shows the external trigger, external clock and the output signal.

Limited by the memory size of 32 kB of the module, pattern memory is set to 25000 data points among which 20000 points are outputted for one repetition period and 5000 points left for memory shift. Accordingly, **the** external clock is set to 500 kHz synchronized by the external clock of 25 Hz. The phase of the output signal can be shifted for 1.26 radian (10 ms) with minimum step of 0.314 mrad (2 μ s).

Fig.4-1 (a) Memory data of sinusoidal pattern

Fig.4-1 (b) Flow diagram

Figure 4-3 is the LabVIEW front panel of the signal generator. Wavetek Model 1375 is indicated by instrument descriptor VXI::7. And this instrument can be extended to many channels. After trigger mode/source and sample clock source/sample mode are set, the signal generator is started up in sequence defined as in the figure.

Startup and shutdown of the signal generator are shown in Fig. 4-4. The rise and fall time can be set according to practical use. After the output reaches the set value, amplitude regulation and phase shift are functioned.

Fig.4-4 (a) Signal output at start-up (horizontal: I s/div)

Fig. 4-4(b) Signal output at shutdown (horizontal: 1 s/div)

Figure 45 demonstrates three cases of the phase shift, i.e. phase lag, in phase and phase advance, with respect to the external trigger signal. Since the phase shift is controlled by memory shift, the phase shift is operated precisely.

(c) phase advance of 20 μ s

Fig.4-5 Signal output phase with respect to external trigger signal (horizontal: 20 μ s/div)

A model power supply 61 was used to test the performance of the signal generator and the functions have been well demonstrated.

5. Conclusions

The VXIbus-based signal generator provides a way to drive the resonant power supplies for the main magnets of the 3 GeV RCS. Experiments have confirmed the operation. Flexible adjustments in both phase and amplitude of waveform facilitate the close B/Q tracking between many magnet families. For better adjustment accuracy, it is preferred to upgrade the DAC of the VXI module to I 6-bit and extend the memory points to over 1M. Further experiments are planed to fulfill the feedback control in amplitude regulation and phase synchronization.

Acknowledgements

We are grateful to Hideaki YOKOMIZO, director of Center for Neutron Science, Hiromitsu SUZUKI, subgroup leader of Accelerator Group and Prof. Yoshiro IRIE from KEK for supporting this research work.

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国際単位系 (SI) と換算表

表1 SI基本単位および補助単位

量	袮 名	記 号	名	称	記 号	倍数	接頭語	記
長 さ	ル - ~	m	時 分,	Н	min, h, d	10^{10}	ヶ ΙT. $^{\mathrm{+}}$	E
質 量	グ ラム \ast p	kg	分, 度、	杪	\bullet	10^{15}	ぺ Þ.	
時 間	杪	s	\cdot	ル ь.	ىلا ı.	10^{12}	チ ラ	
電 流	⇁ ペ -7 ン	A		ン		10 ⁹	$\ddot{}$ ガ	G
熱力学温度	ビ łτ. ン ル	K	電子ボルト		eV	10^{s}	ガ	N
質 量 物	ル ÷	mol	原子質量単位		u	10 ³	\ast \Box	k
度 光	÷ ぅ カ ン	cd				10 ²	ヶ \sim ъ.	
畢 角 面	ジ 与 ァ	rad	$1~{\rm eV}$ = 1.60218 \times 10 ⁻¹⁹ J $n = 1.66054 \times 10^{-27}$ kg		10 ¹	$\tilde{\tau}$ ヵ	d	
体 角 立	ステラジアン	sr			10^{-1}	$\tilde{\tau}$ シ		

A 3 lid 1 0) 9 VF -t- t SI W a **94 lo-'** *4* **[3)U**

$1 \text{ eV} = 1.60218 \times 10^{-19} \text{ J}$ $1 u = 1.66054 \times 10^{-27} kg$

 $1 \text{ Å} = 0.1 \text{ nm} = 10^{-10} \text{ m}$ *i* b= *100 fm²*= *10⁻² m²* こでは省略した。 1 Gal=1 cm/s² = 10⁻² m/s²
1 Ci = 3.7 × 10¹⁰ Bq 1 rad = 1 cGy = 10^{-2} Gy $-$ *IC* λ t TC λ . 1 rem = $1 \text{ cSv} = 10^{-2} \text{Sv}$

表 2 SI と併用される単位 **5 5 SI ※** 5 SI 接頭語

- <u>(注)</u>
- 1. 表 1 5 は「国際単位系」第 5 版,国際 度量衡局 1985年刊行による。ただし, 1 eV および1uの値は CODATA の1986年推奨 **値によった。**
2. 表4には海里,ノット,アール,ヘクタ
	-
- *9'* -79 **7, - ' 1) -** H Wb/A I bar=O I lvlla 10'Pa 3. baril. *JISTI1Af*0.)TT)`J,\$-Xb*JA* 合に限り表2のカテゴリーに分類されてい
- PP TCl[∞] 3, A IN FQ

1 R=2.58×10⁻⁴C/kg

F a ref F = 2011 a red in the state of the stat び「血圧の単位」mmHg を表2のカテゴリ

換 算 表

