

POWER SUPPLIES AND MONITORING EQUIPMENT FOR PULSED MAGNETS

1. Introduction

The general principle of powering and monitoring the pulsed beam transport magnets was described by D. Neet (NPA/Int. 62-9). The greater part of the information contained therein is still valid. However, at that time no operating experience was available. In the meantime modifications and additions were made, that proved to be reliable during the 8 neutrino runs that have been done up to now. These modifications will be described here in detail and a complete and up to date set of circuit diagrams is added. Only these drawings should be used by future users for reference.

The whole setup performed extremely satisfactorily during the neutrino runs. The position of the external beam on target needed readjustment only every few hours. This could easily be made by adjusting the current in the last bending magnet. Over a period of 5 days the current in one of the bending magnets was monitored, giving a difference between highest and lowest recorded value of 2 o/oo. The pulse to pulse accuracy was ± 0.5 o/oo.

2. Power supplies

The operating principle is that of a feedback control system, in which the difference between a reference input and some function of the controlled variable is used to supply an actuating error signal to the control elements and the controlled system. The amplified error signal is applied in a manner tending to reduce this difference to zero.

The way in which the error signal depends upon the controlled variable determines the behaviour of the system under steady state conditions. The solution of the differential equations of the control system gives the transfer function and its frequency response.

Assuming a sinusoidal input signal, the Laplace transform of the transfer function can then be written as :

$$F = \frac{X_{\text{out}}}{X_{\text{in}}} = \frac{b_0 + b_1 p + b_2 p^2 + \dots}{a_0 + a_1 p + a_2 p^2 + \dots} \quad \text{where } p \text{ is the Laplace operator}$$

The general types of control systems can easily be deduced from this equation

P - element : The output signal is proportional to the input signal

$$F = K = \frac{b_0}{a_0} \quad (\text{all other terms are zero})$$

I - element The output signal equals the integral with respect to time of the input signal

$$F = \frac{K_I}{p}, \text{ where } K_I = \frac{b_0}{a_1} \quad (\text{all other terms are zero})$$

D - element The output signal equals the derivative with respect to time of the input signal

$$F = K_D \cdot p, \text{ where } K_D = \frac{b_1}{a_0} \quad (\text{all other terms are zero})$$

A combination PD would be

$$F = K + K_D \cdot p = K (1 + T_D \cdot p), \text{ where } T_D = \frac{K_D}{K} \text{ is the differentiation time constant}$$

These are only ideal cases, in reality time constants appear that give rise to delays. They present themselves e.g. in the case of a P - element as

$$F = \frac{K}{1 + T_1 \cdot p}, \text{ in which } T_1, \dots \text{ is the time constant for which the inverse transform is } e^{-t/T_1}$$

A PD element as used in our case, then becomes

$$F = K \frac{1 + T_D \cdot p}{1 + T_1 \cdot p}$$

In the Giesenhausen supplies operation is now as follows (see PM 03). A voltage proportional to the instantaneous voltage on the capacitor bank is supplied to one of the inputs of the DC amplifier. In series with this voltage is connected part of the voltage drop in a resistor in the main charging current path, which voltage is therefore proportional to $C \frac{dV}{dt}$. A small portion of this voltage is again differentiated by $C l$. A closer analysis of this circuit will show that the total input voltage can be written as

$$v_X = \frac{b_0 + b_1 p + b_2 p^2}{a_0 + a_1 p} v_{cap} = \frac{K + T_{D1} \cdot p + T_{D2} \cdot p^2}{1 + T_1 p} \cdot v_{cap}$$

where K is the ratio of the voltage divider across the output terminals of the power supply and T are time constants created by R_1 , R_2 , C_1 , R and C . The other input of the amplifier is connected to a reference voltage.

As long as $/ v_X + v_{ref} / > 0$ the D.C. amplifier will show an output voltage that through the power amplifier opens the transductors that control the charging current.

Due to the arrangement of the differentiation of the input signal to the amplifier, the time constants that would otherwise delay this signal with respect to the instantaneous capacitor voltage are now properly counterbalanced, resulting in a system that is completely stable under all load conditions.

The potentiometer that controls the amount of differentiation and therefore the stability should of course be set according to the particular conditions of load and charging current. Diagrams are provided for this (PM 04). It is quite sensitive when a small load and low charging current are used, therefore the charging current should be closely observed in this case, to be certain that no oscillations or small secondary charging peaks occur.

As a rule the current should go down smoothly in about 100 msec at the end of the charging cycle. The shunt signal is available at the terminals on the door.

A second set of diagrams gives the transformer taps to be used for a certain range of output voltage. The bias current of the transductor should be changed simultaneously. To this end a connecting strip inside the supply provides terminals, marked X_1 , X_2 , X_3 , X_4 , which should be shorted by tags from X up to the number corresponding to the transformer tapping used.

Although relatively complicated, this principle ensures that the transductor always has the same operating point, thereby providing equal characteristics, for any output voltage.

In practice the output voltage is fixed for a certain application of the power supply, so that transformer tap and bias current are set once and for all.

All diagrams with "number of capacitors" as variable refer of course to the capacitance value with which they were measured i.e. 278 μF each. Whenever other capacitors are used they should of course be redrawn.

3. Interlocks

As it became evident that not firing or prefiring of the ignitrons did not result in overloading, but that on the other hand too frequent interruptions of the charging cycle led only to timelosses, the interlock was modified so that any ignitron failure did not switch off the power supply but only lights up an indication lamp.

4. Timing

In the former setup both kicker magnet and magnetic horn were triggered through delays of 10 msec from the T_0 pulse. As these delays had an accuracy of about 1.0/00, the jitter of the horn pulse with respect to the kicker magnet could amount to 20 μsec on a pulse length of 200 μsec . As this was unacceptable both pulses are now taken from the same point with only an additional 100 μsec delay for the bending magnet.

A simulator was added for testing and running in the absence of P.S. machine pulses.

5. Monitoring

A monitoring panel (PM 18) allows display on one oscilloscope of :

- a) Kicker magnet pulse and current in each beam transport magnet
- b) Kicker magnet pulse and current in magnetic horn
- c) Signal induced in the beam current transformer and signal from photodetector after the horn.

Comparison of these two signals gives a direct check on the efficiency of the horn and the position of the beam on the target.

6. Current measuring circuit

The relay that provides the switching action (PM 16) is powered from a higher voltage to minimize the intrinsic delay.

At the input the selector switches both inner conductor and screen of the incoming cable. This prevents closed loops and multiple earthing that give rise to parasites on the current waveform.

7. Acknowledgements

R.A. Brown and J.P. Zanasco took care of all the tests and modifications that were found to be necessary.

G. Pluym

/fv

Distribution : (open)

Scientific staff of N.P.A.

PS/4096

5. अप्रैल 1980 को विद्यालय में एक बड़ा समाजिक सभा आयोजित किया गया।

विद्यार्थी ने इसमें शामिल हुए।

6. अप्रैल 1980 को विद्यालय में एक बड़ा समाजिक सभा आयोजित किया गया।

विद्यार्थी ने इसमें शामिल हुए।

7. अप्रैल 1980 को विद्यालय में एक बड़ा समाजिक सभा आयोजित किया गया।

विद्यार्थी ने इसमें शामिल हुए।

8. अप्रैल 1980 को विद्यालय में एक बड़ा समाजिक सभा आयोजित किया गया।

विद्यार्थी ने इसमें शामिल हुए।

9. अप्रैल 1980 को विद्यालय में एक बड़ा समाजिक सभा आयोजित किया गया।

विद्यार्थी ने इसमें शामिल हुए।

10. अप्रैल 1980 को विद्यालय में एक बड़ा समाजिक सभा आयोजित किया गया।

विद्यार्थी ने इसमें शामिल हुए।

11. अप्रैल 1980 को विद्यालय में एक बड़ा समाजिक सभा आयोजित किया गया।

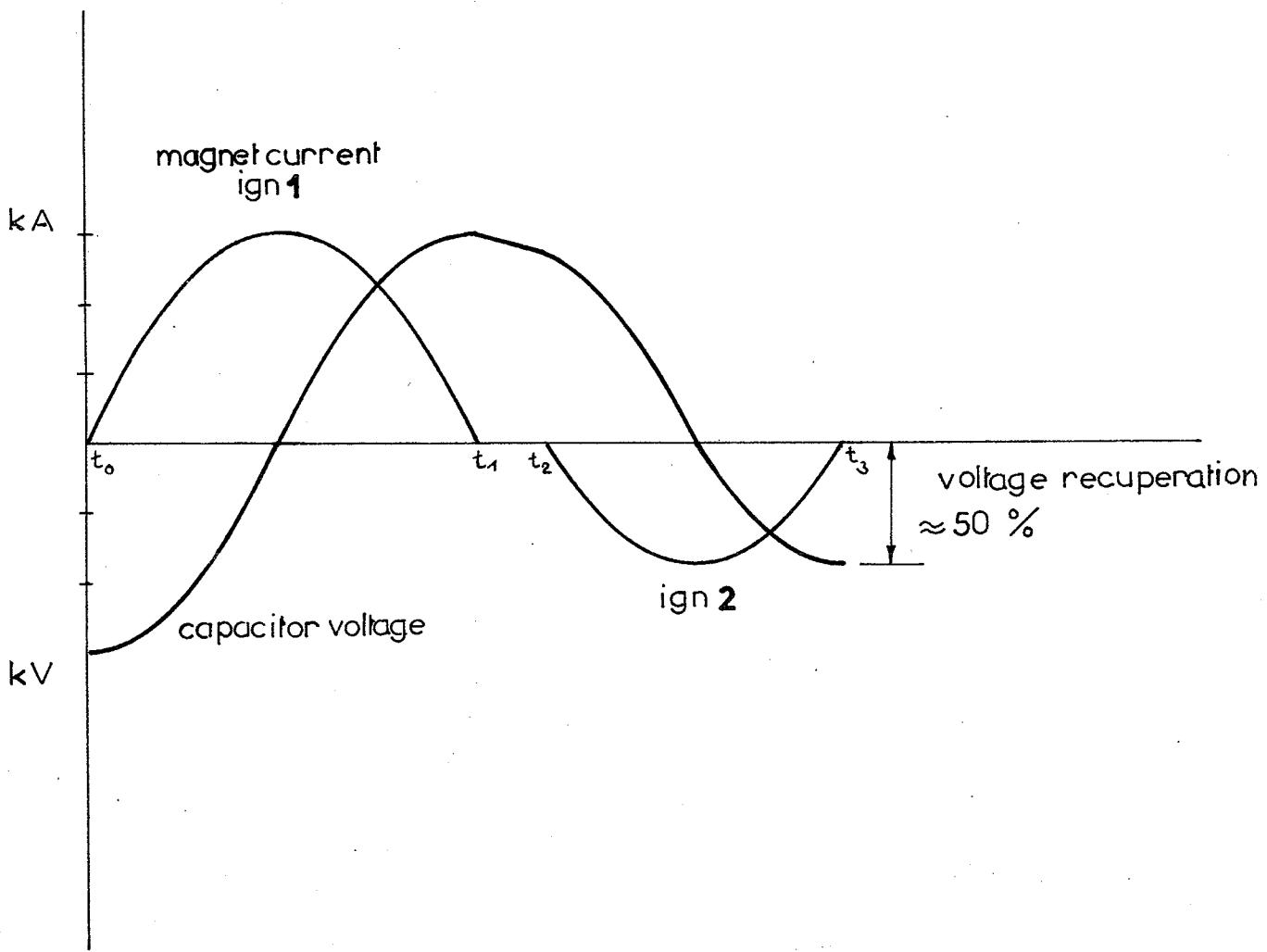
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12. अप्रैल 1980 को विद्यालय में एक बड़ा समाजिक सभा आयोजित किया गया।

विद्यार्थी ने

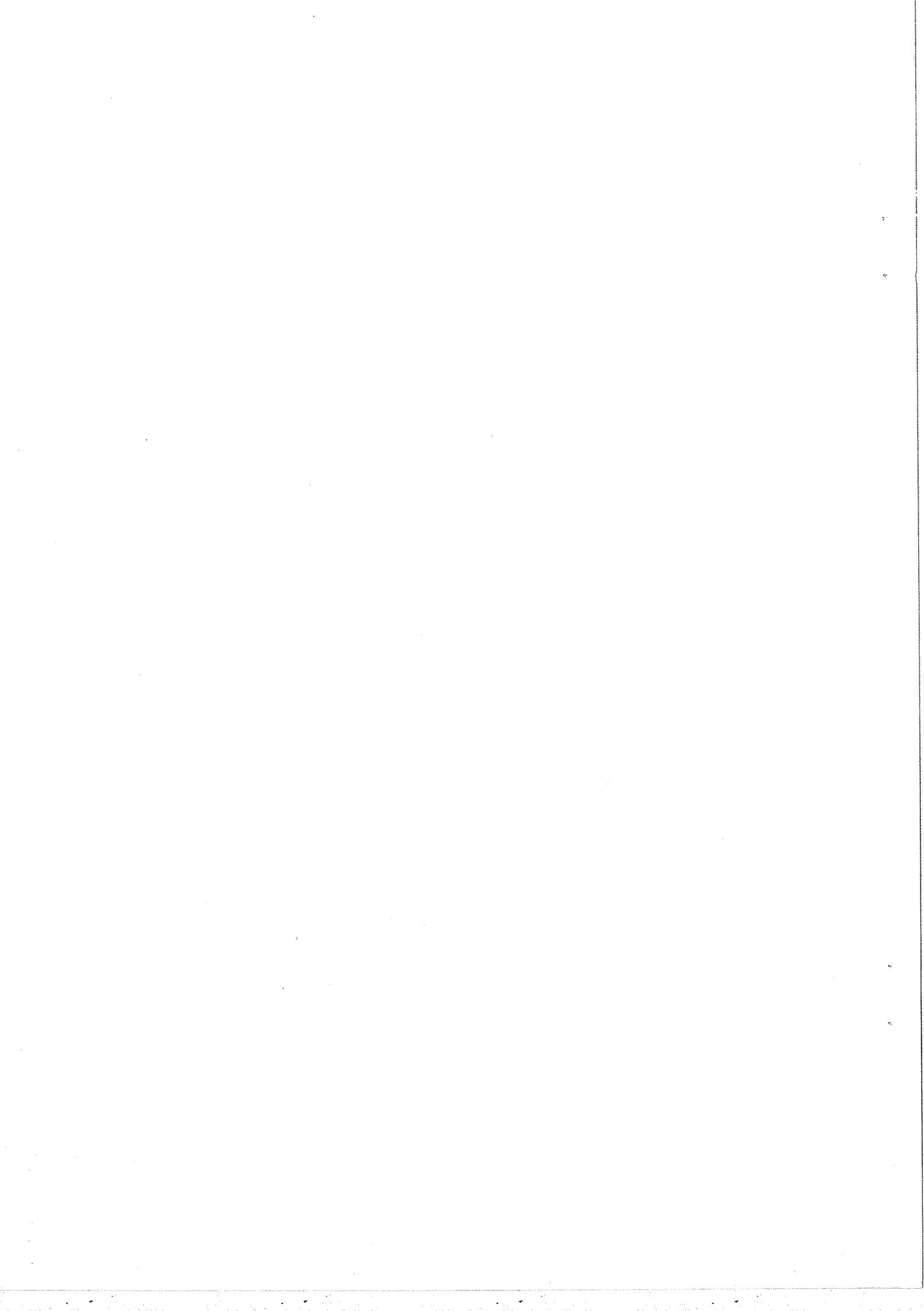
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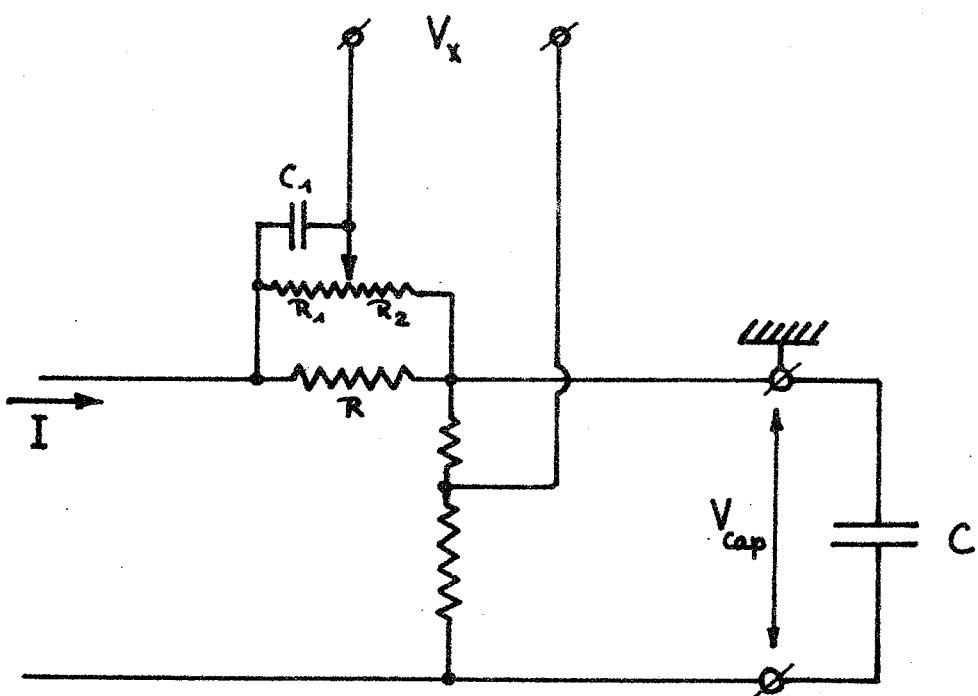
विद्यार्थी



WAVE FORMS

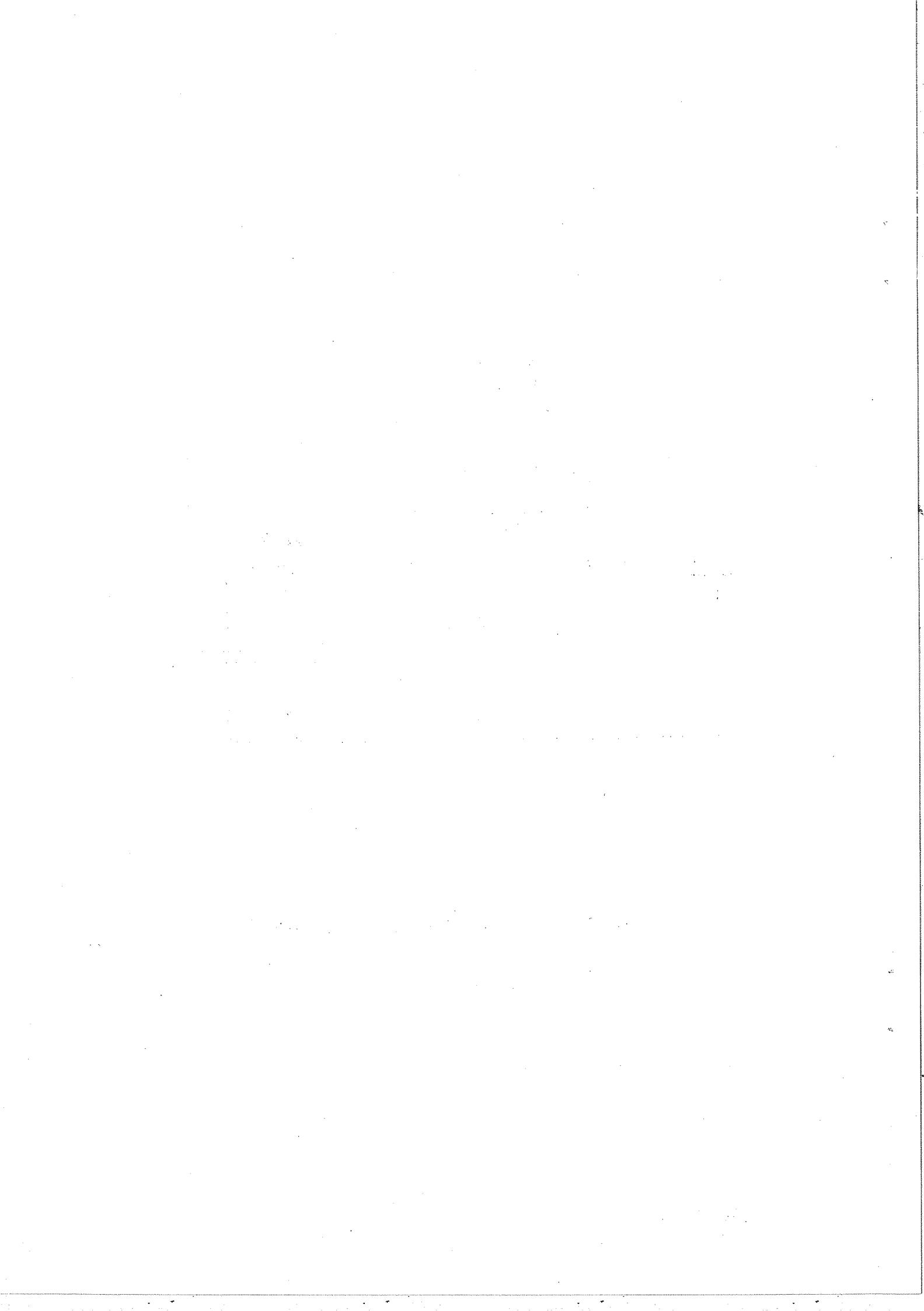
4-10-63
C.P.

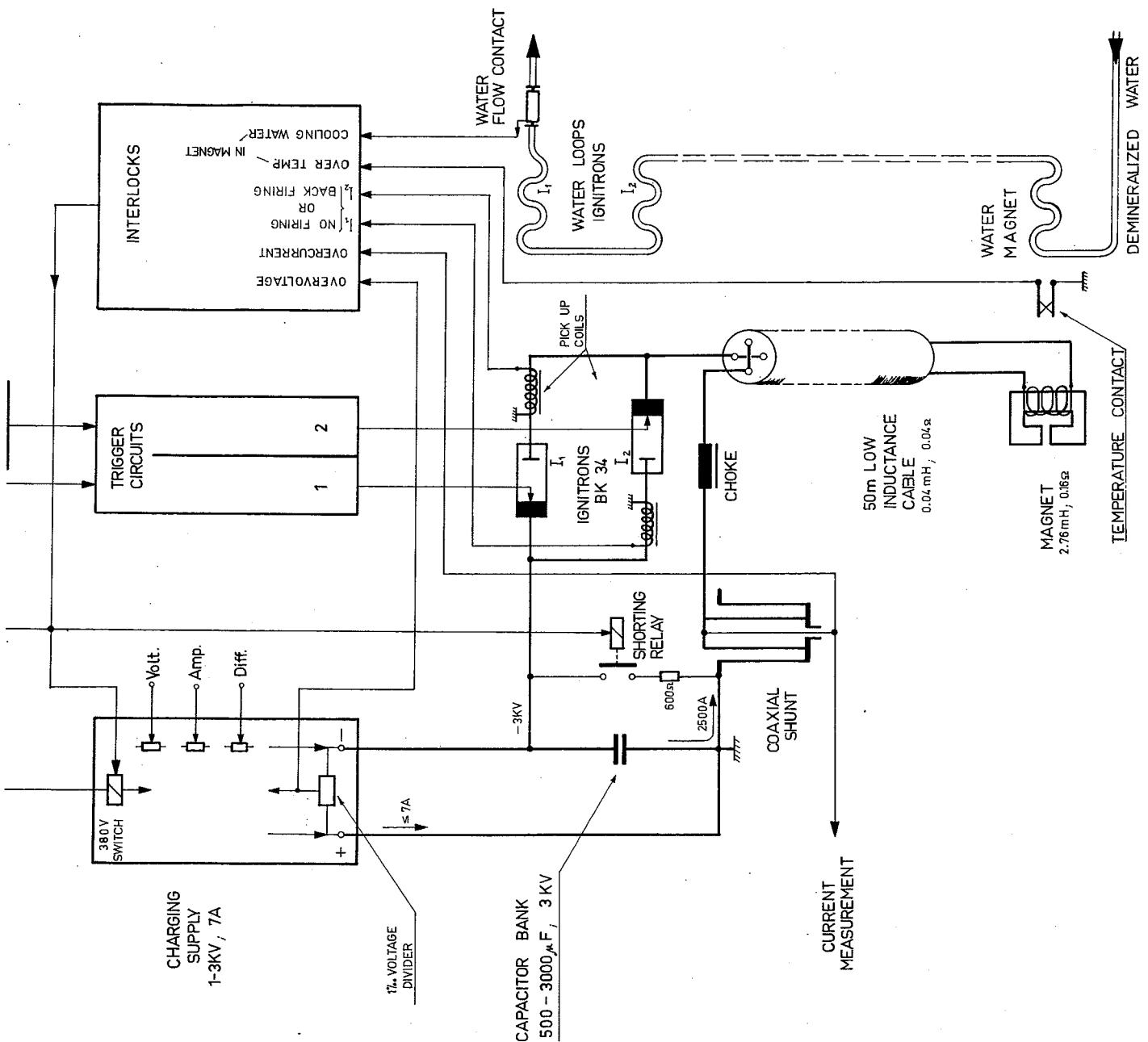


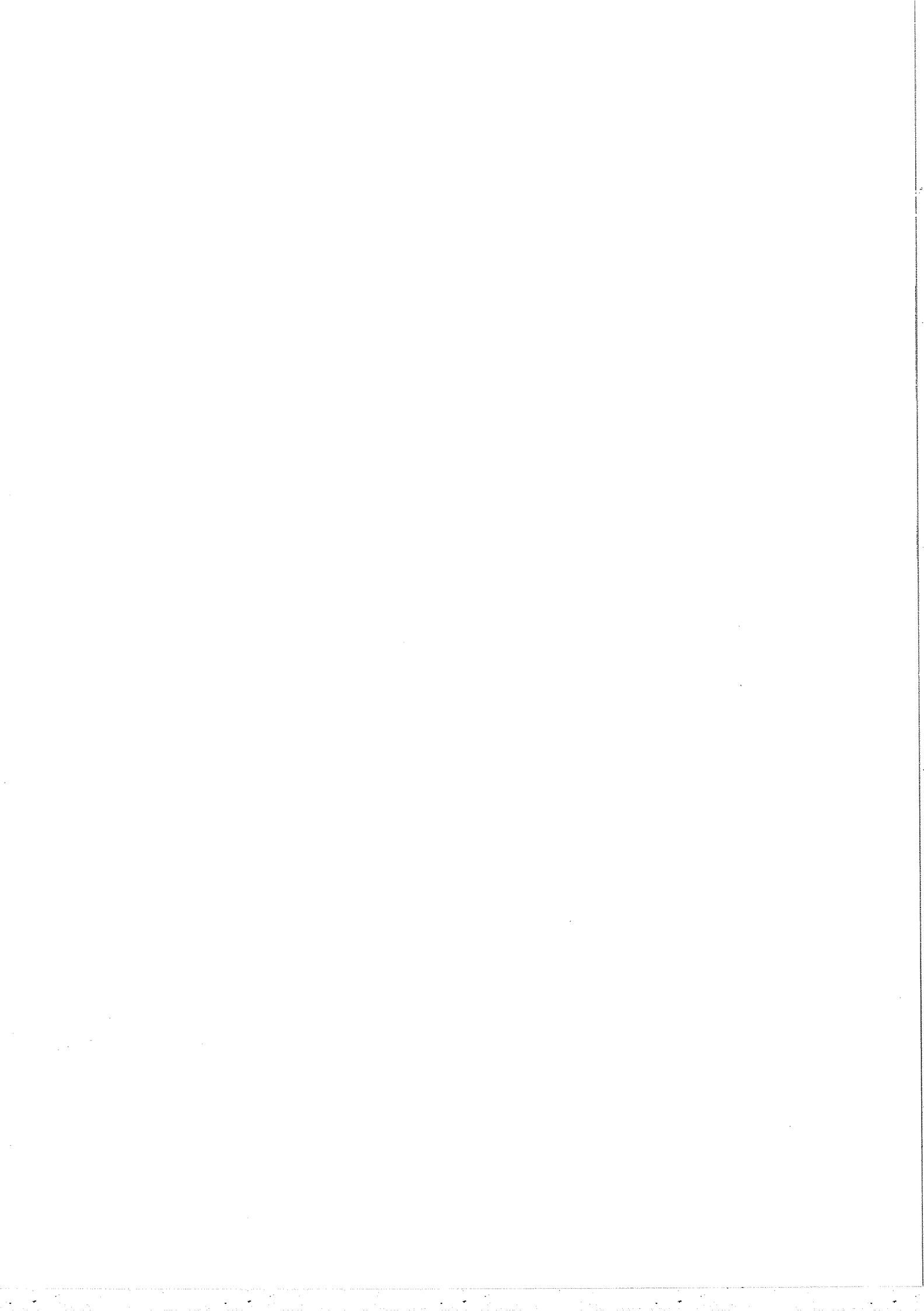


DIFFERENTIATION NETWORK

PS/4096.







SUBJECT
SUJETNEUTRINO BEAM
TIMING

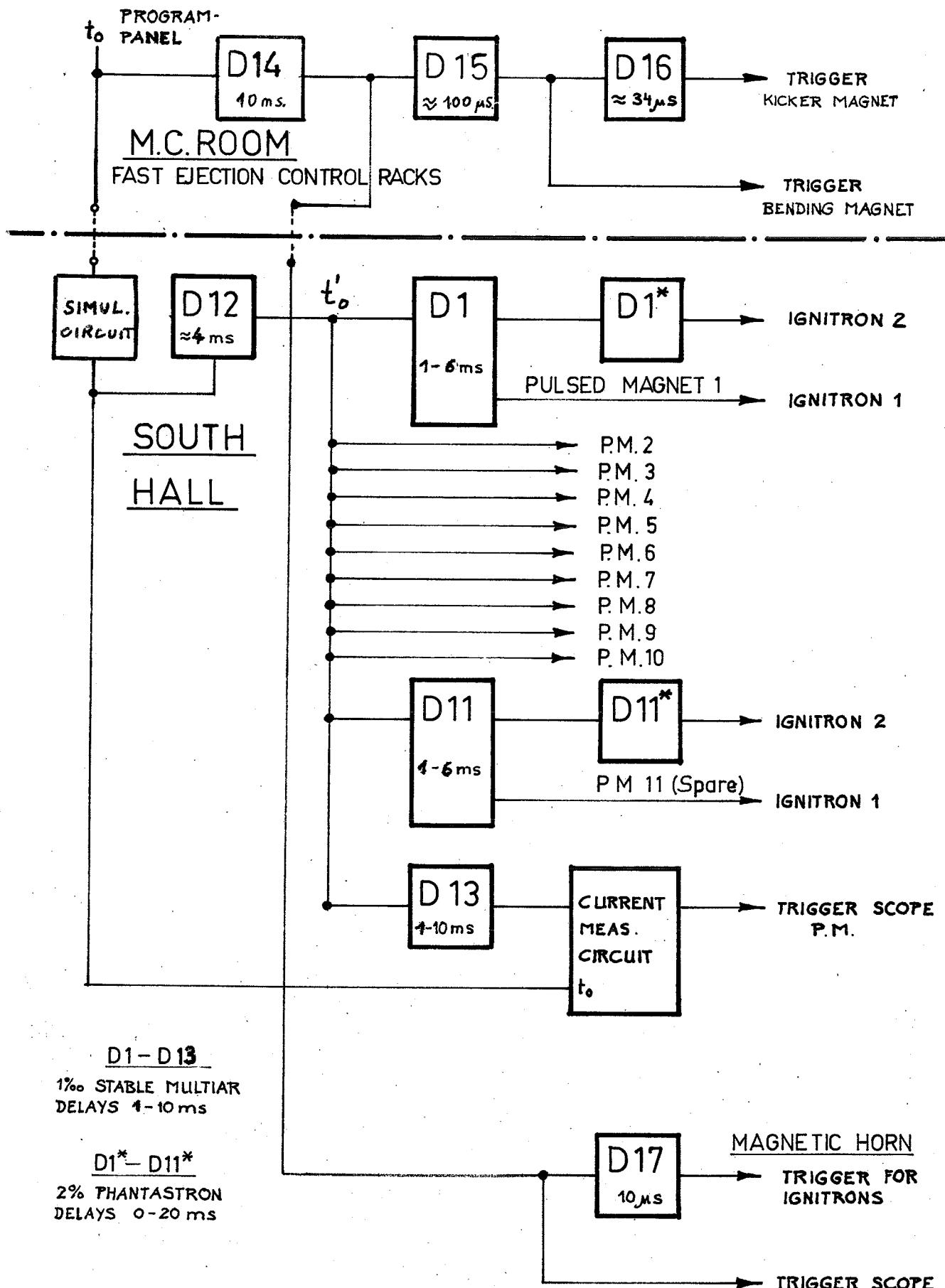
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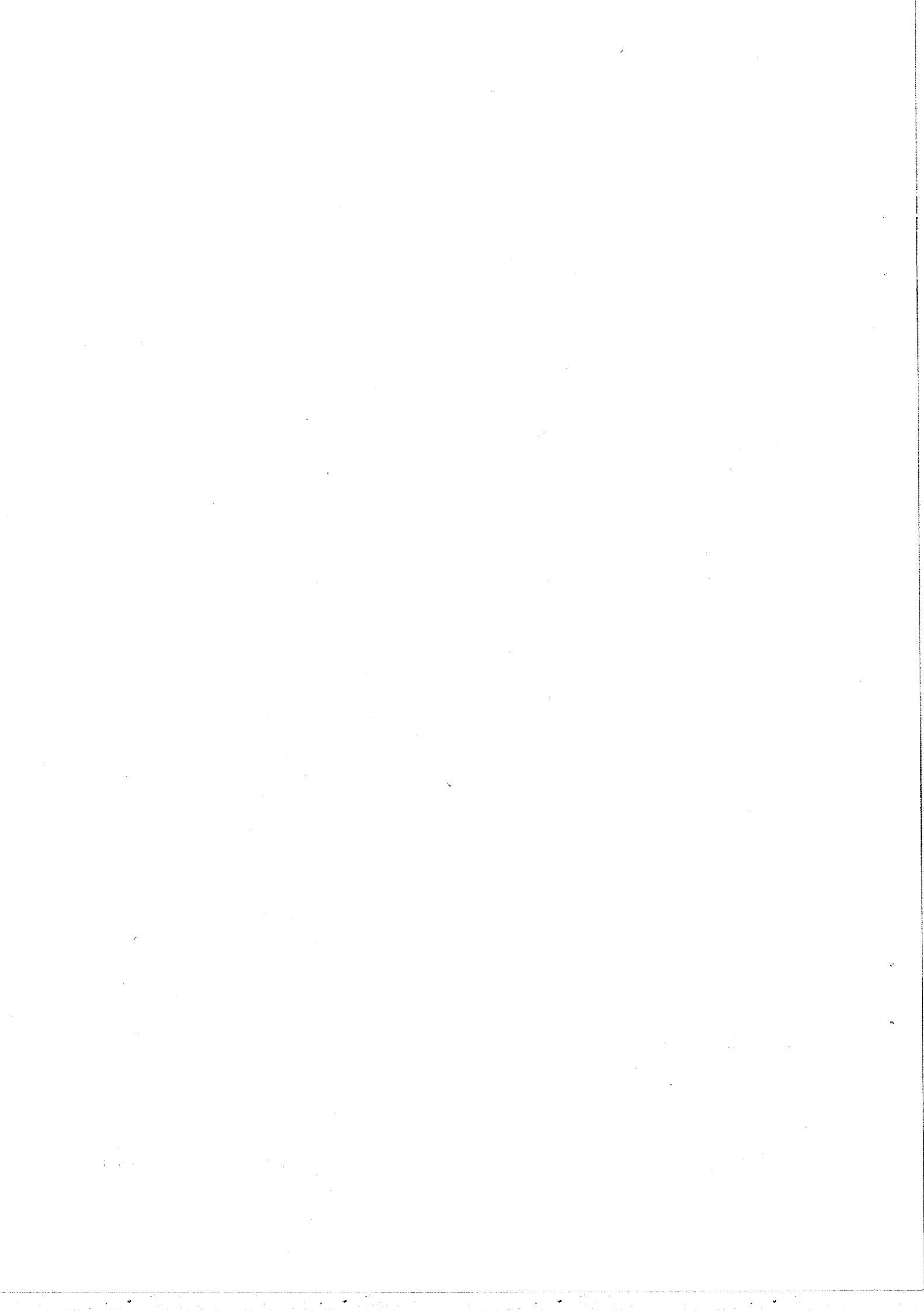
PLUYM.

DATE

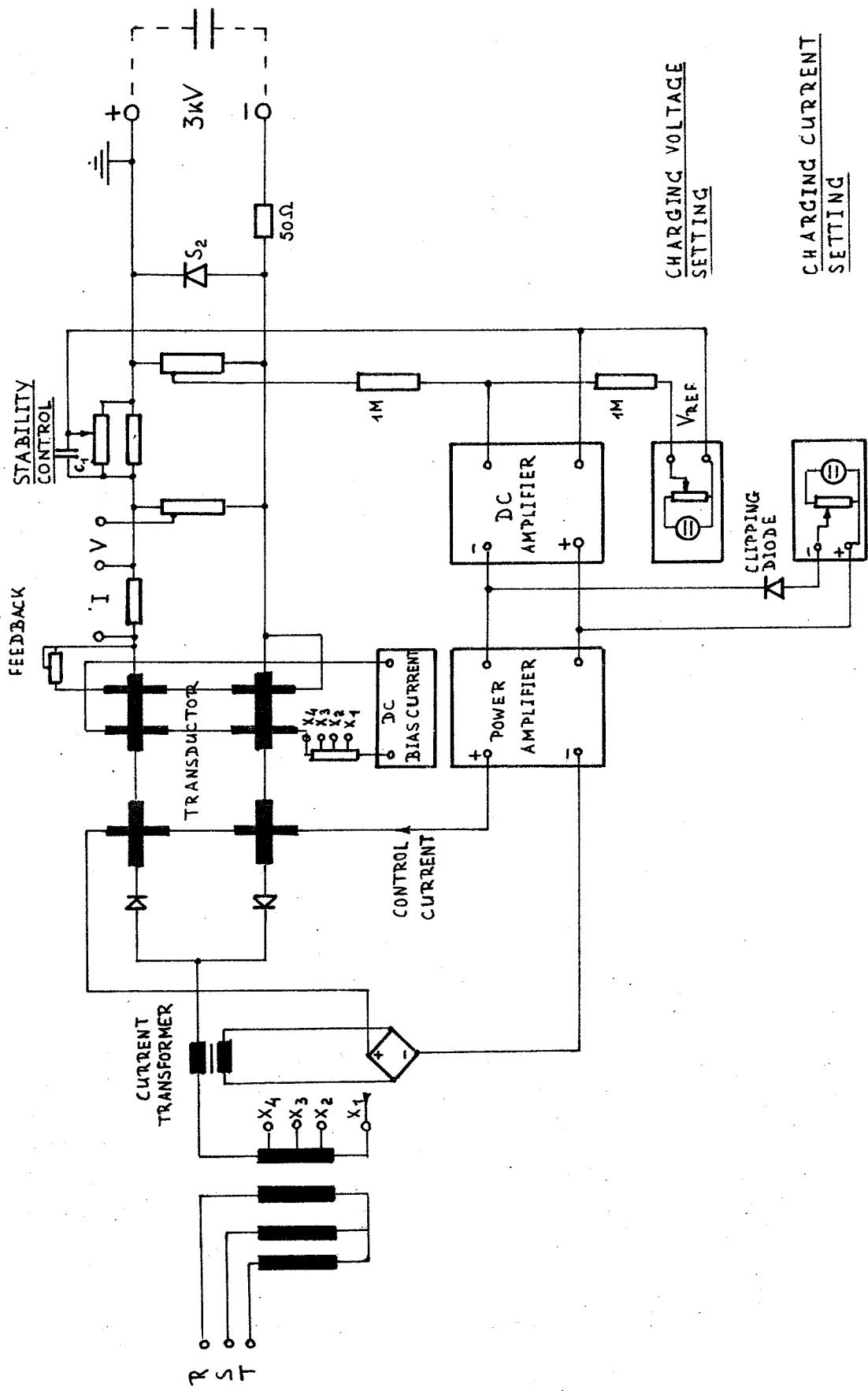
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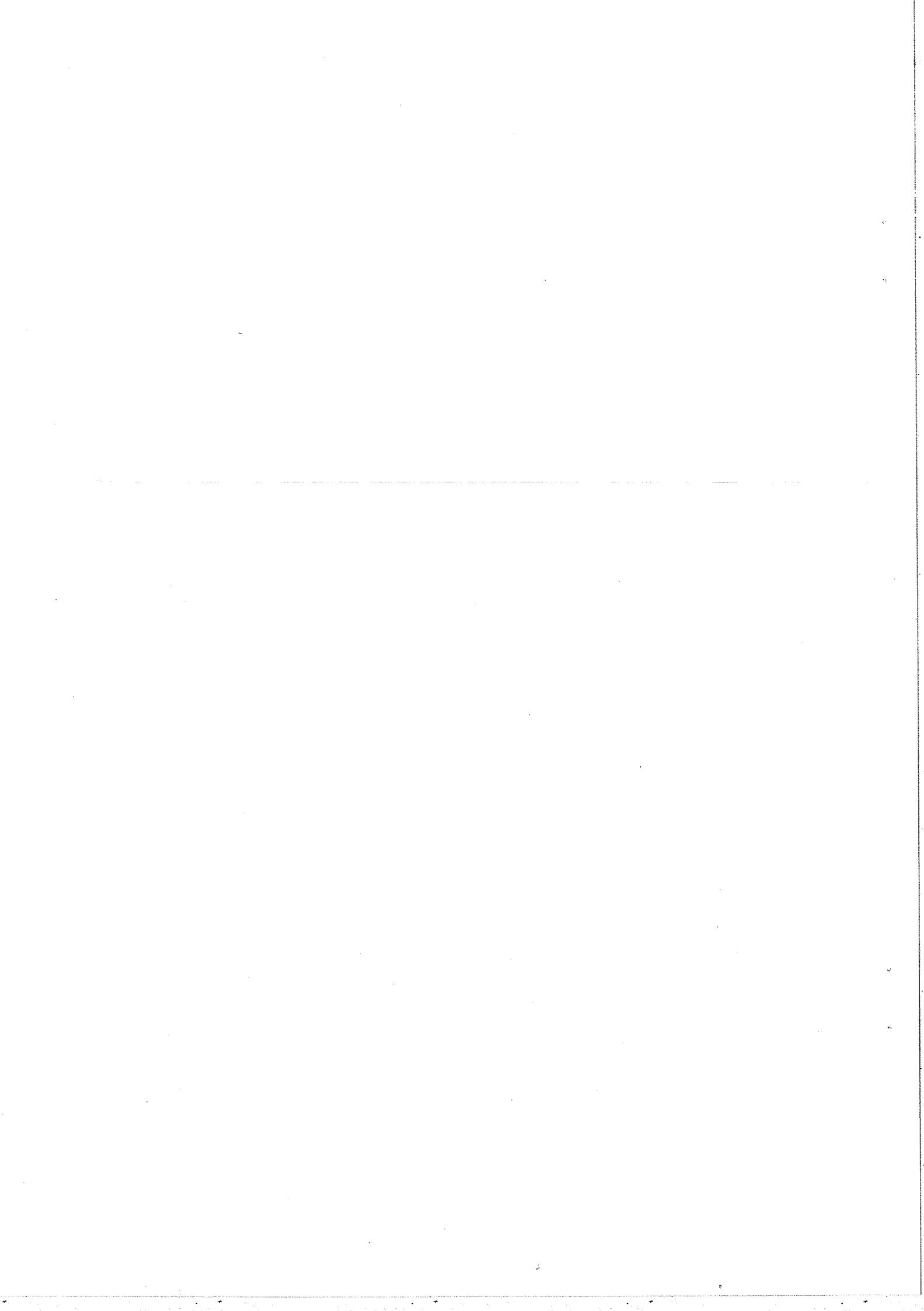
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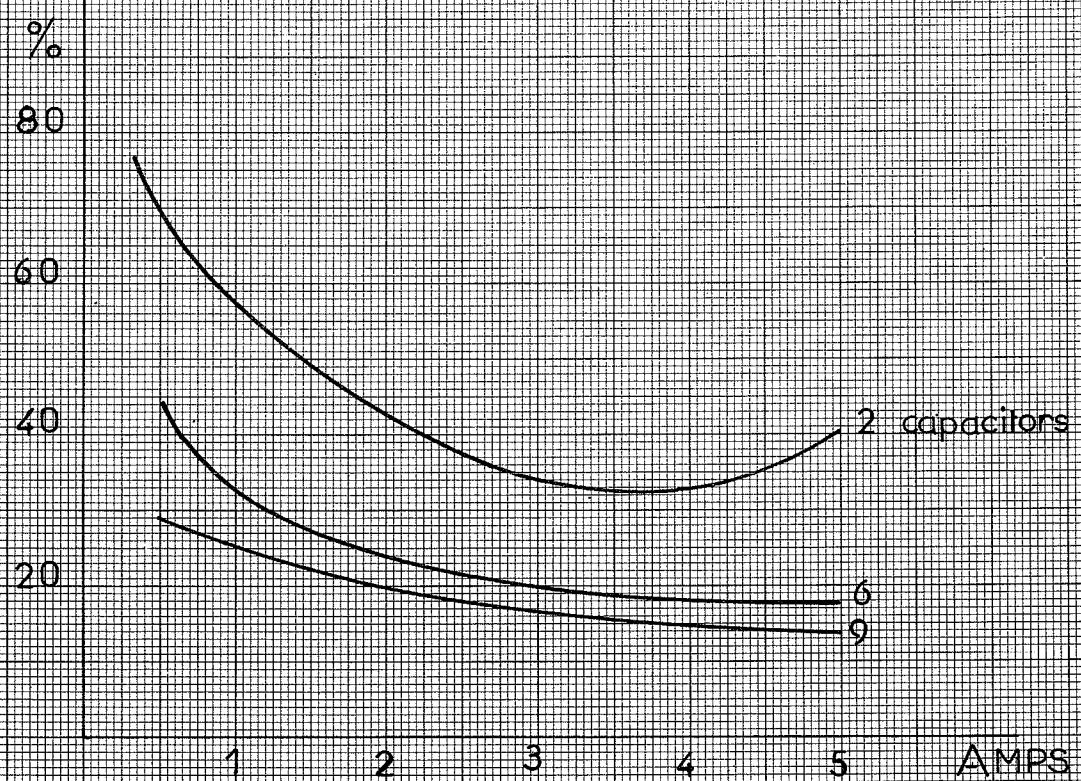
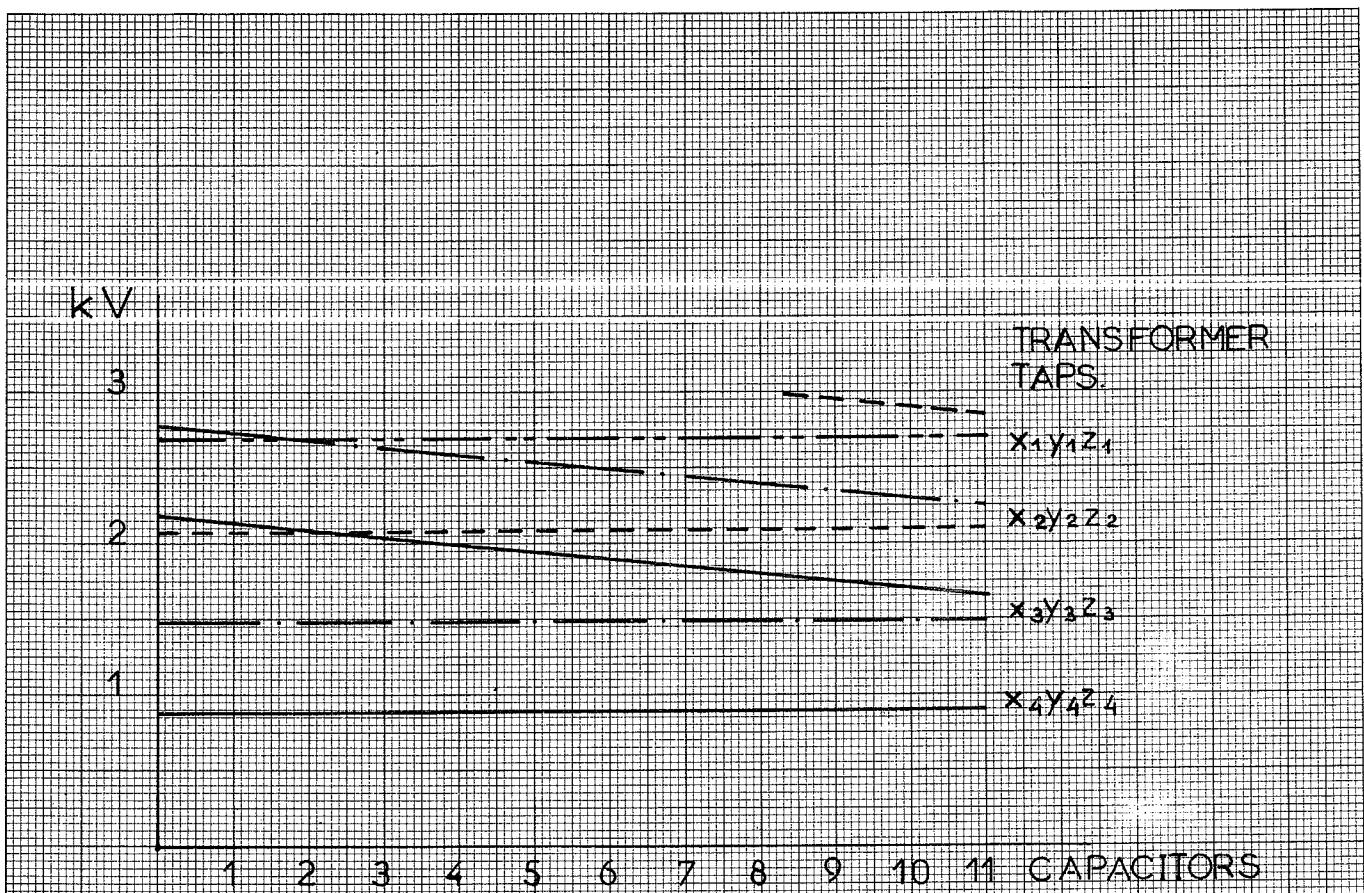




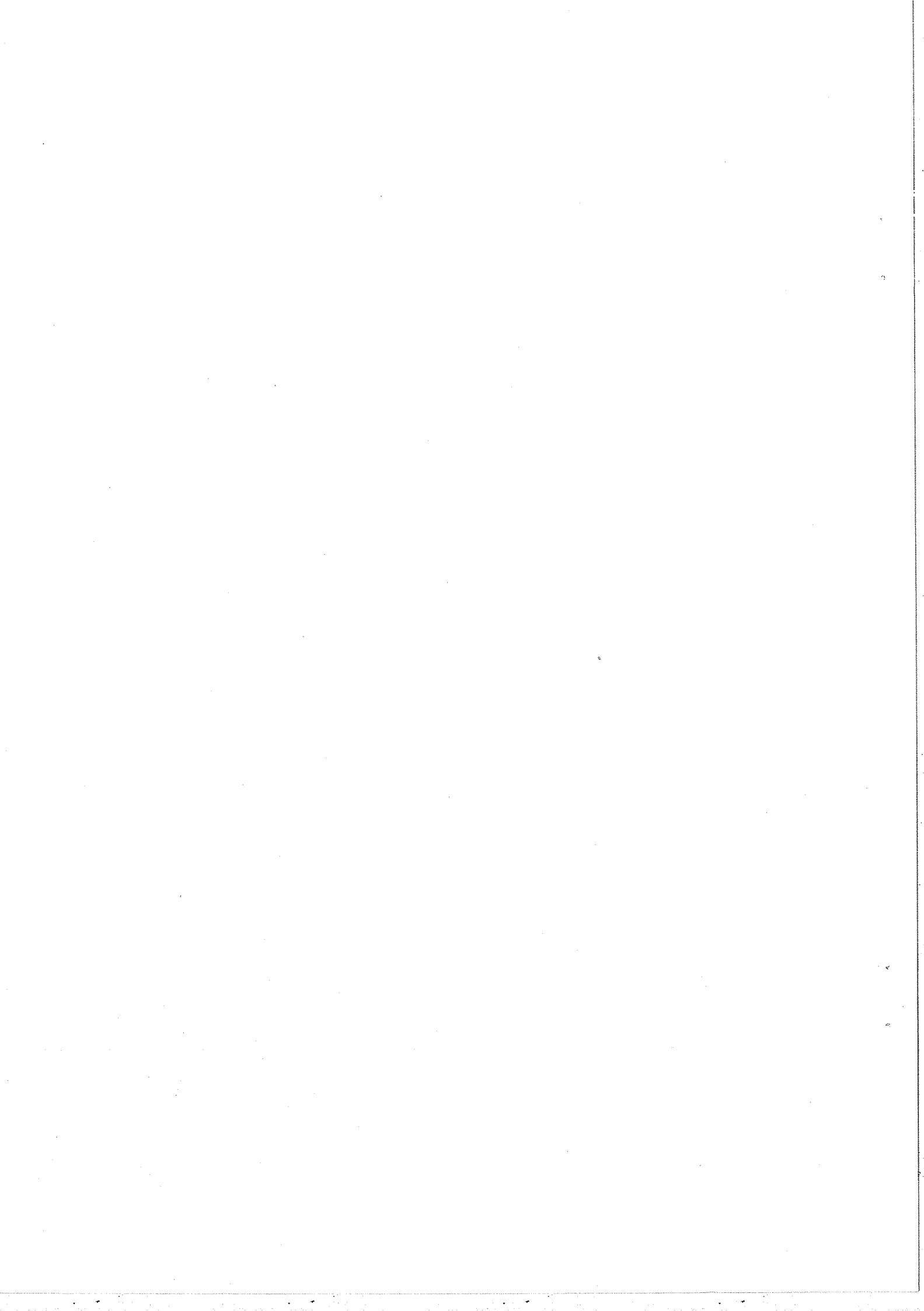
PM 03

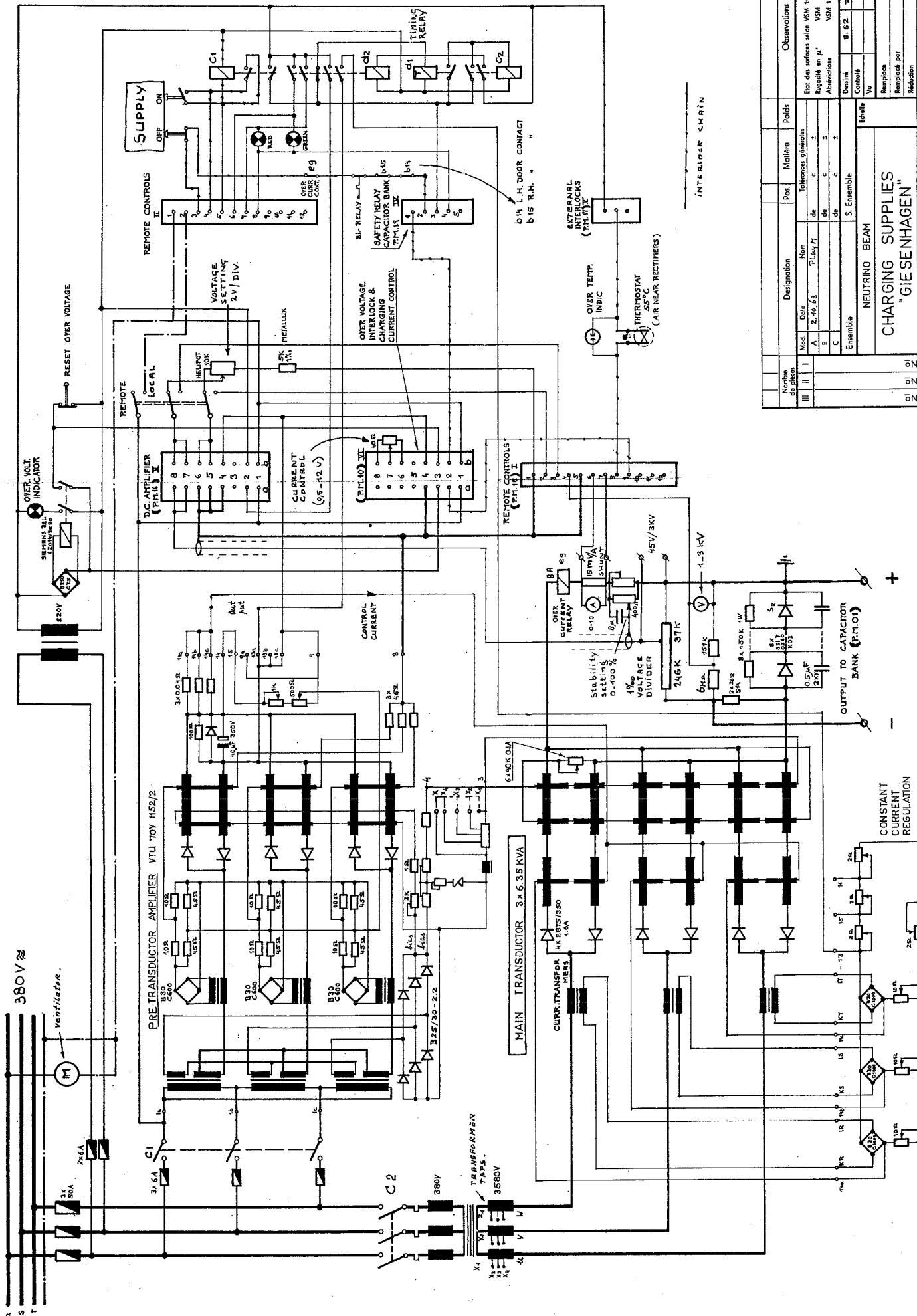




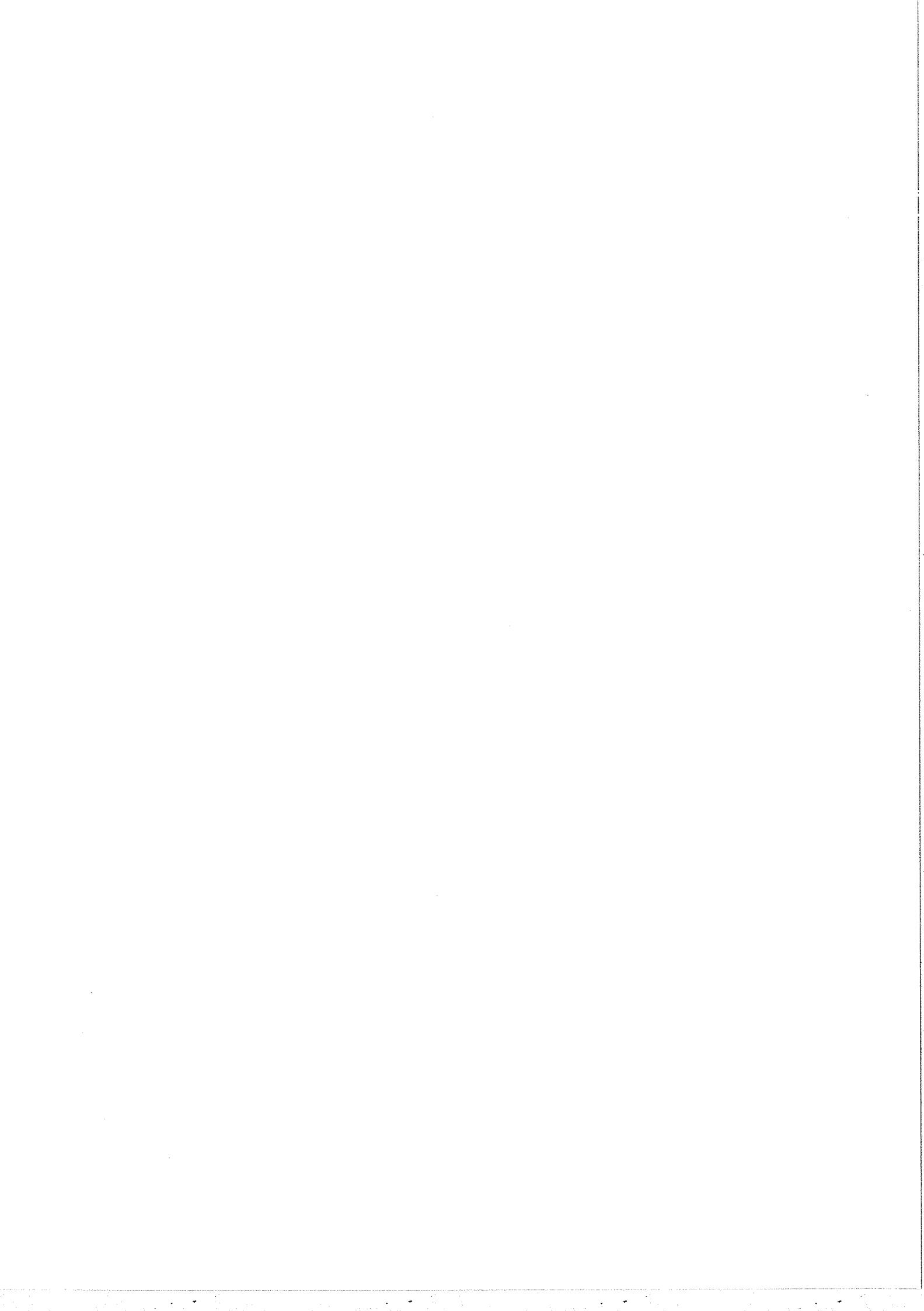


PM 04.





Dossier No		Designation		Poids		Observations
III	II	I	Mod.	Date	Nom	
		A	2-10/63	Plast M	de	Bat des surfaces VSM 10220
		B			de	Rapide en μ
		C			de	Abordables
					S. Ensemble	Densité 8.62



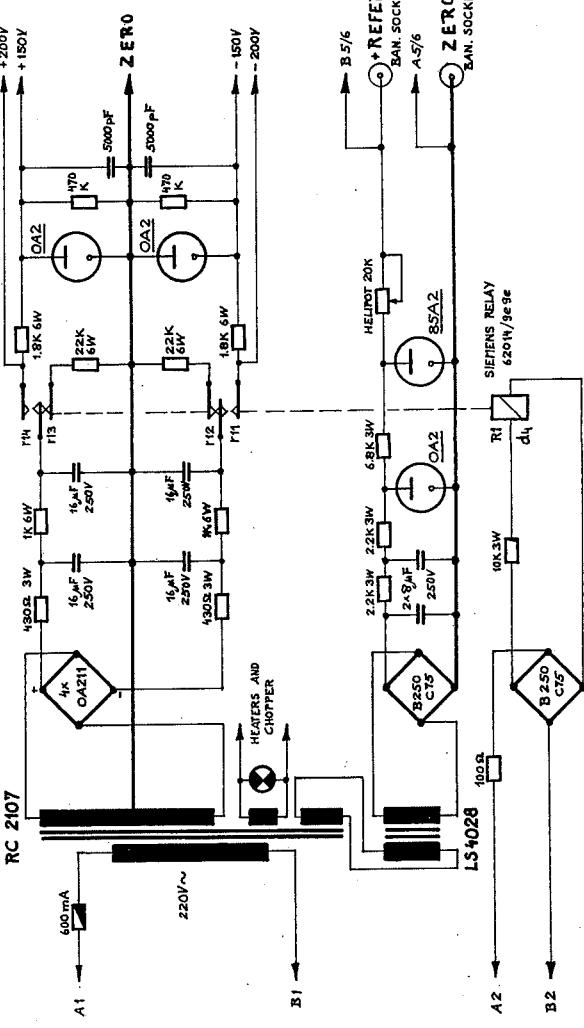
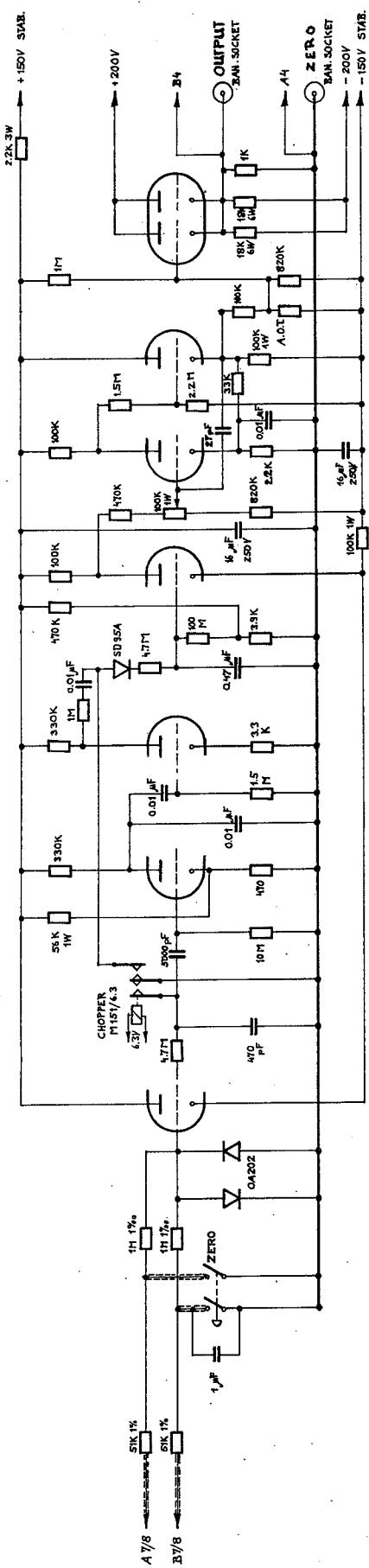
12 6057

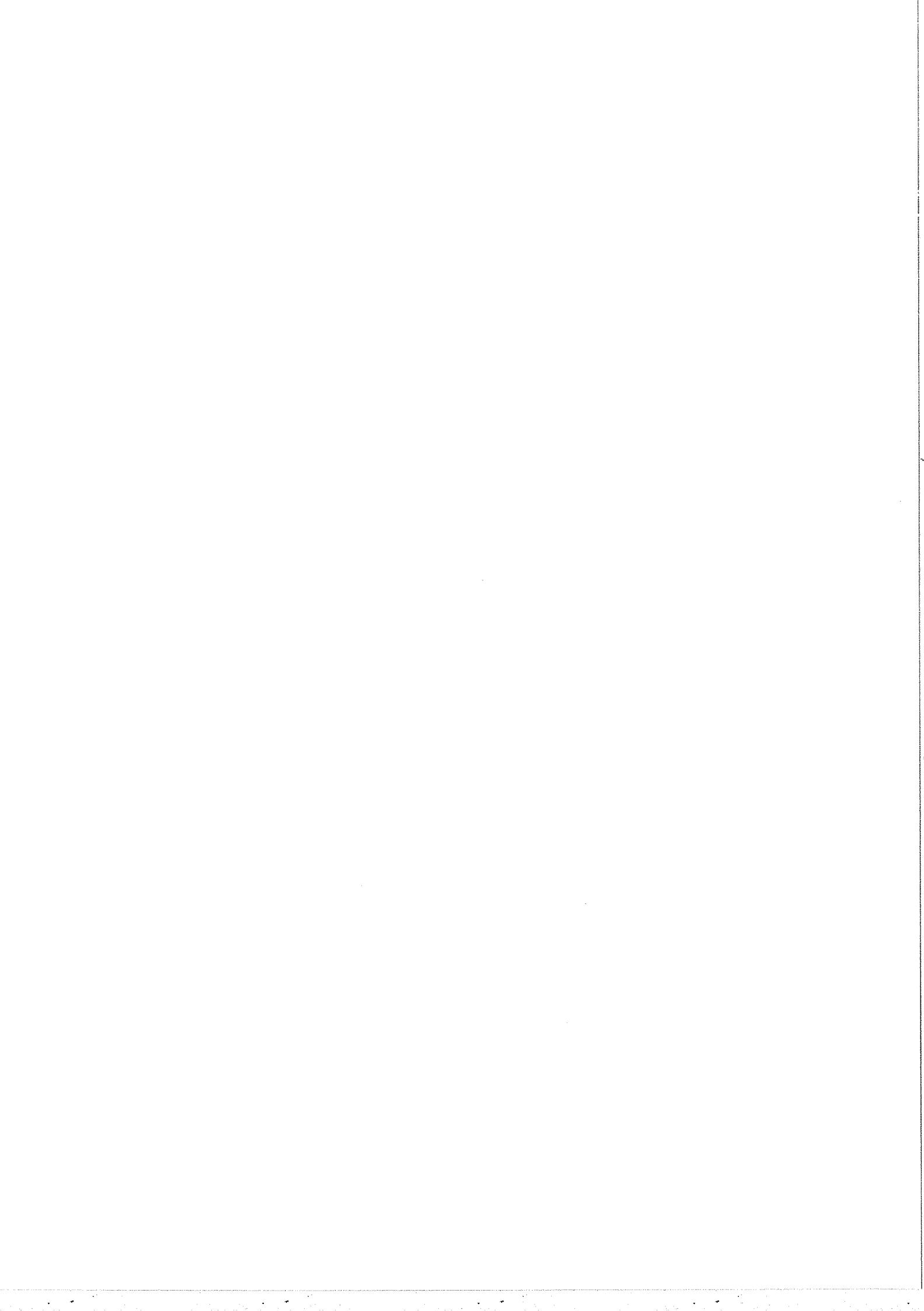
6057

2-6057

E88CC

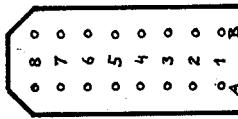
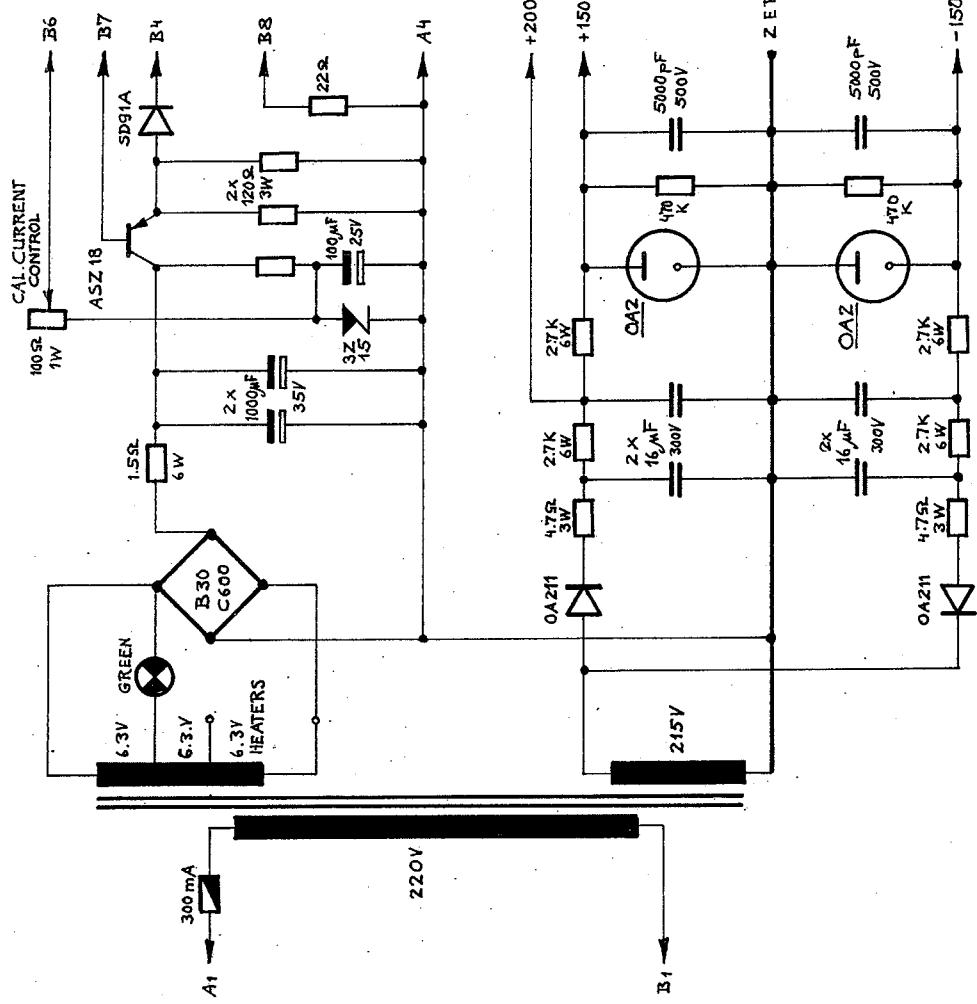
E 88CC





SUB CHASSIS I
POWER SUPPLIES

POWER SUPPLIES

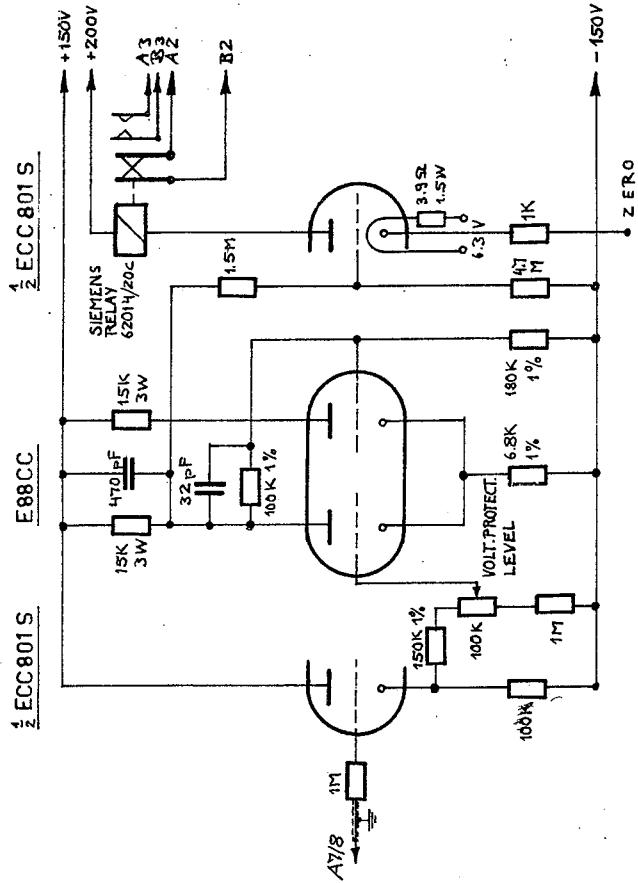


LOWER PLUG
FROM REAR
SEE P.M. 13

SEE P.M. 13

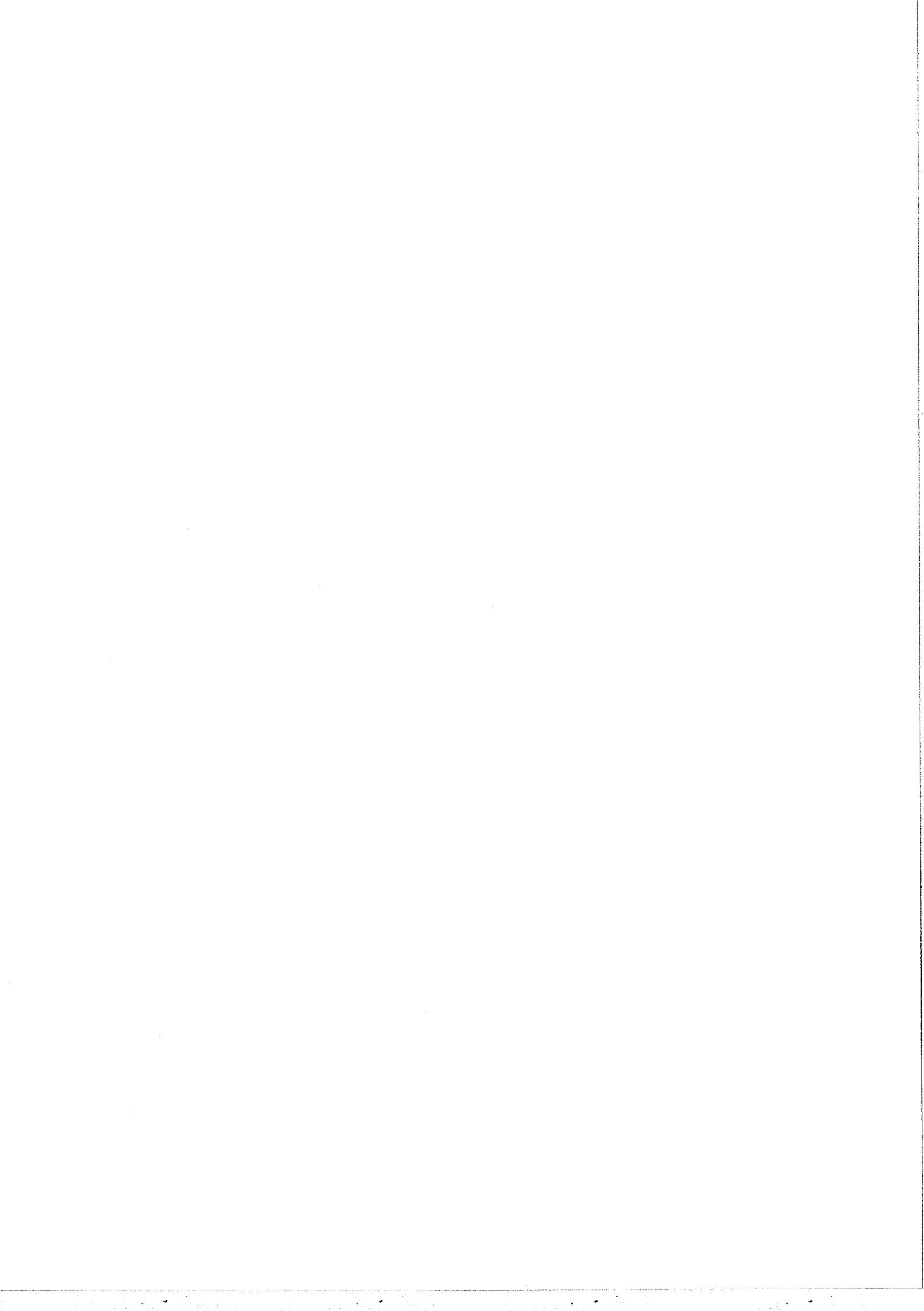
SUB CHASSIS II

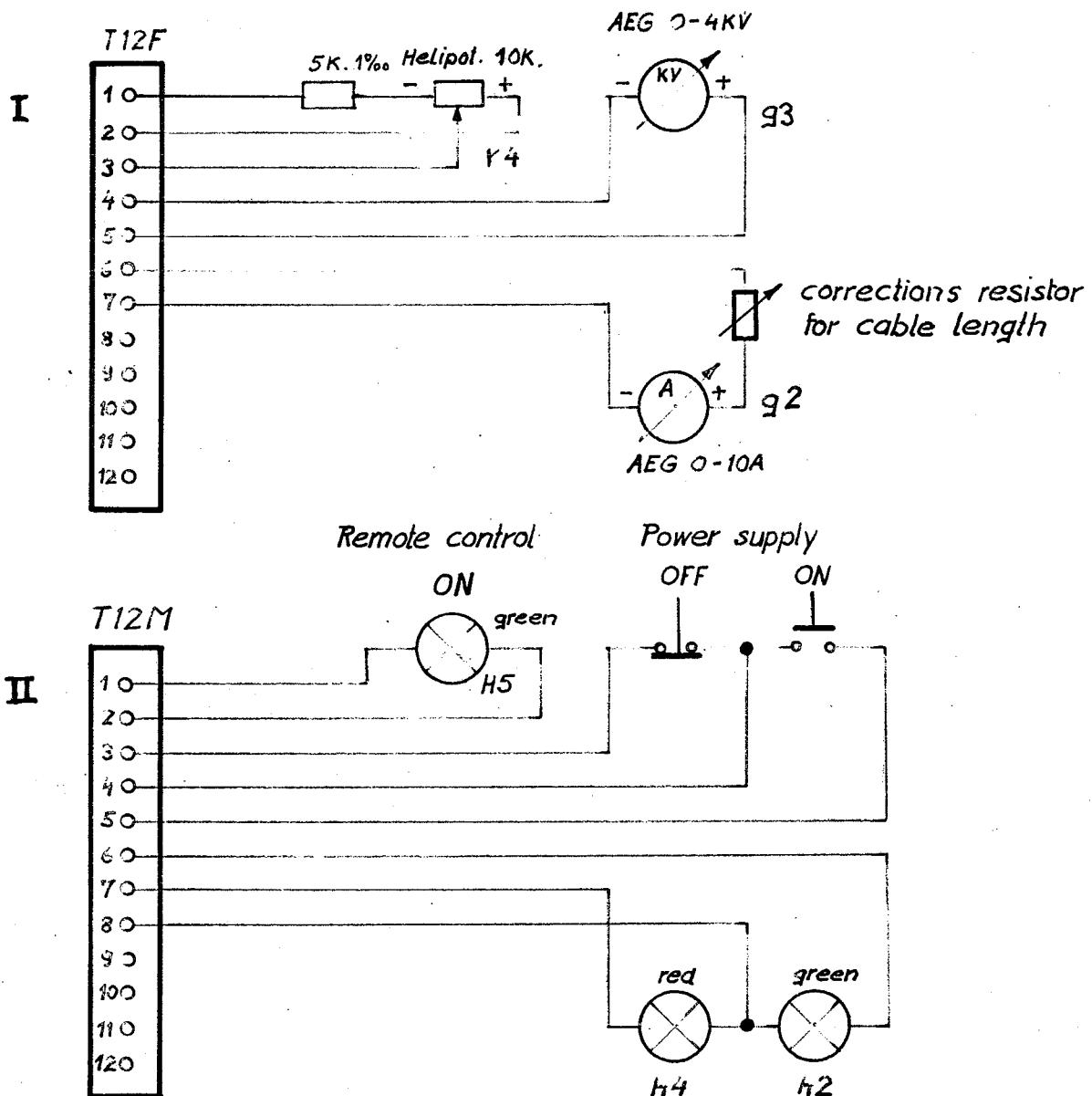
OVER VOLTAGE INTERLOCK



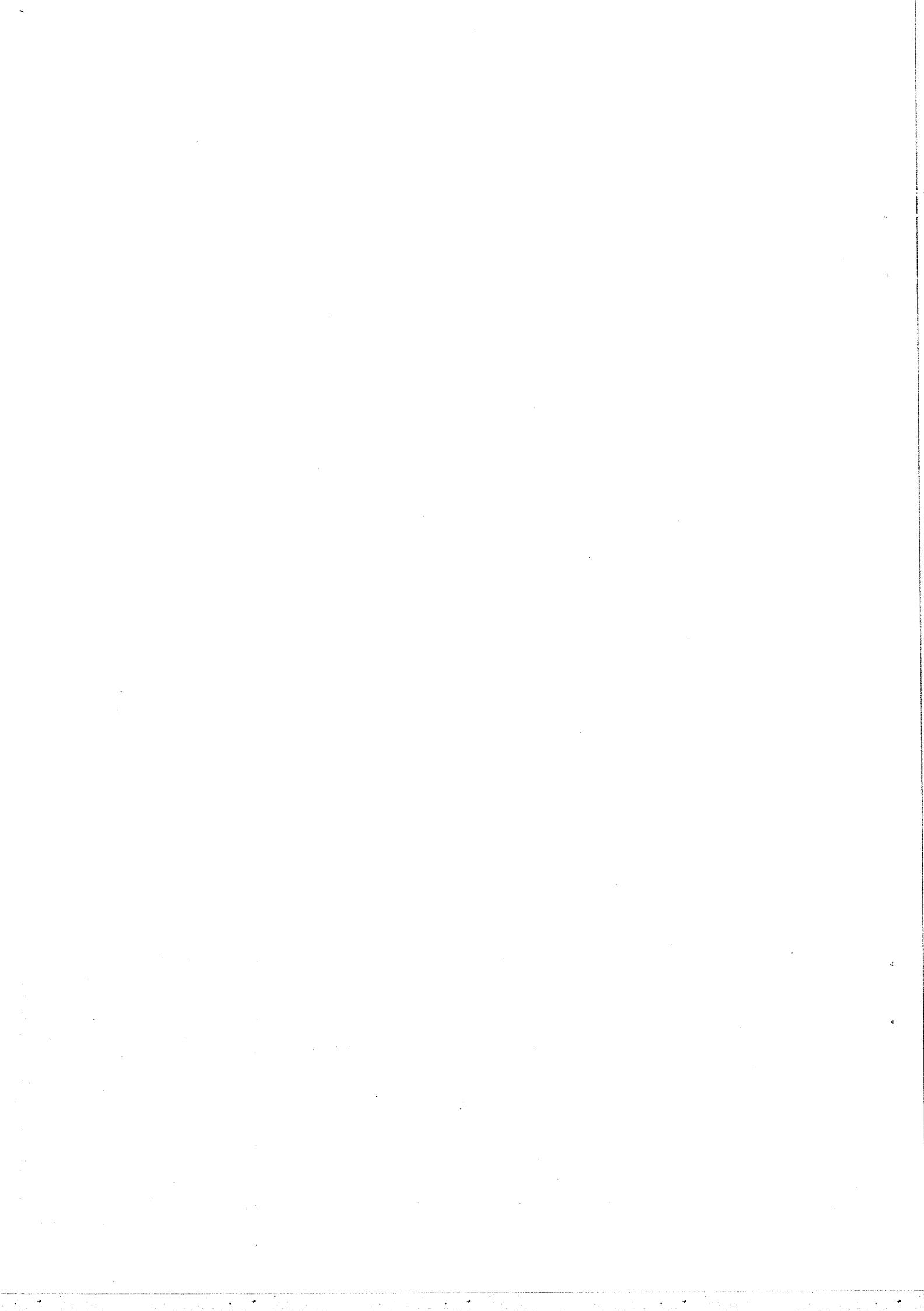
TO WORK IN CONJUNCTION WITH PM.15										TO WORK IN CONJUNCTION WITH PM.07				
Designation			Pos.		Matière	Poids	Observations							
Nombre de pièces	Mod.	Date	Nom	Tolérances générales			Estat des surfaces selon VSM 10320		Rugosité en μ'	VSM 10319	S. 2000	Réduction		
				A	4-16-63	PLAQUE N	de	\pm						
			B				de	\pm						
			C				de	\pm						
			Ensemble			S. Ensemble								
			NEUTRINO BEAM				Echelle							
			OVER VOLTAGE INTERLOCK & CHARGING CURRENT CONTROL				Vu							
Dossier N°	2						Remplace							
Dossier N°	2						Remplacé par							
Dossier N°	2						Réduction							

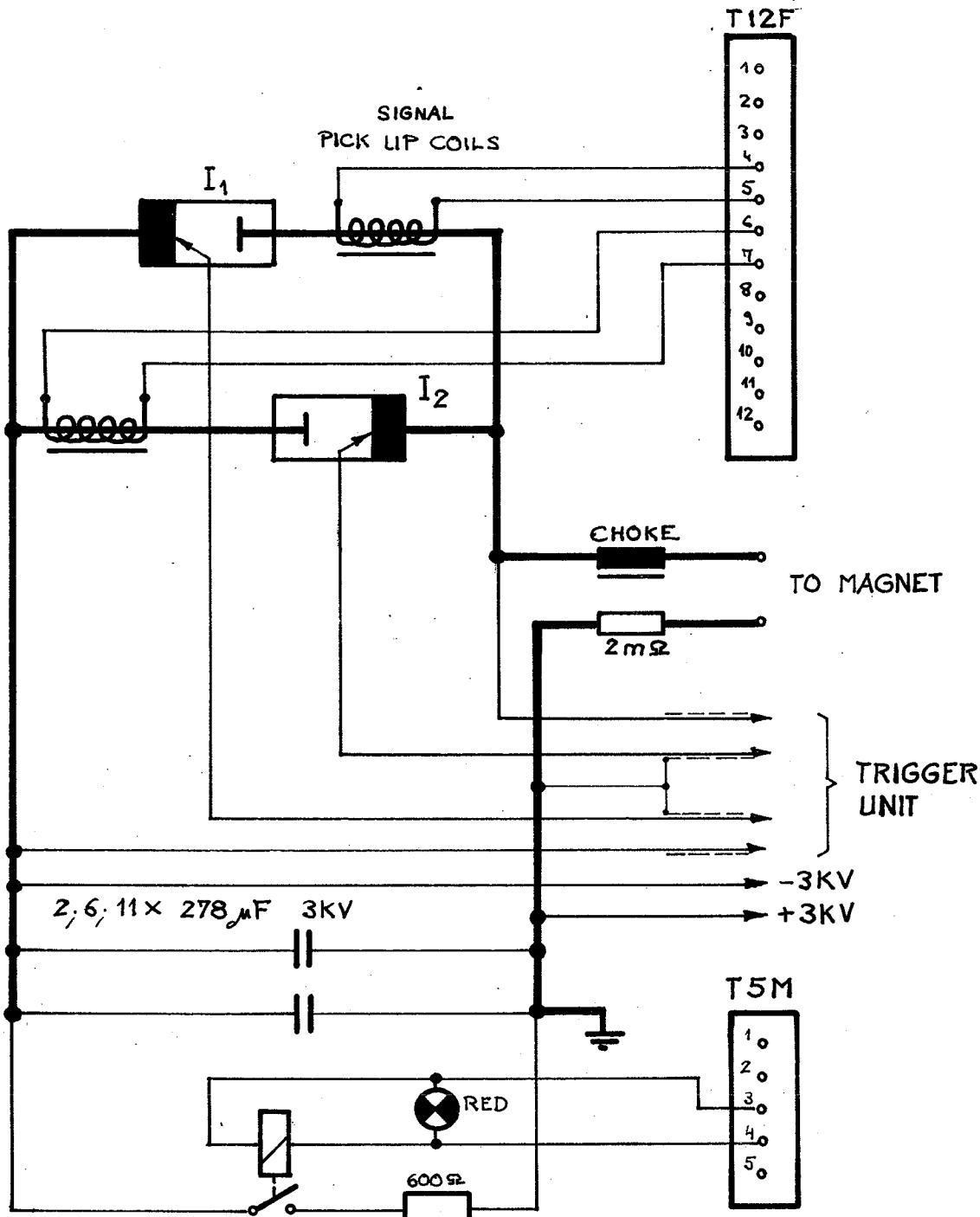
SIS/R/111



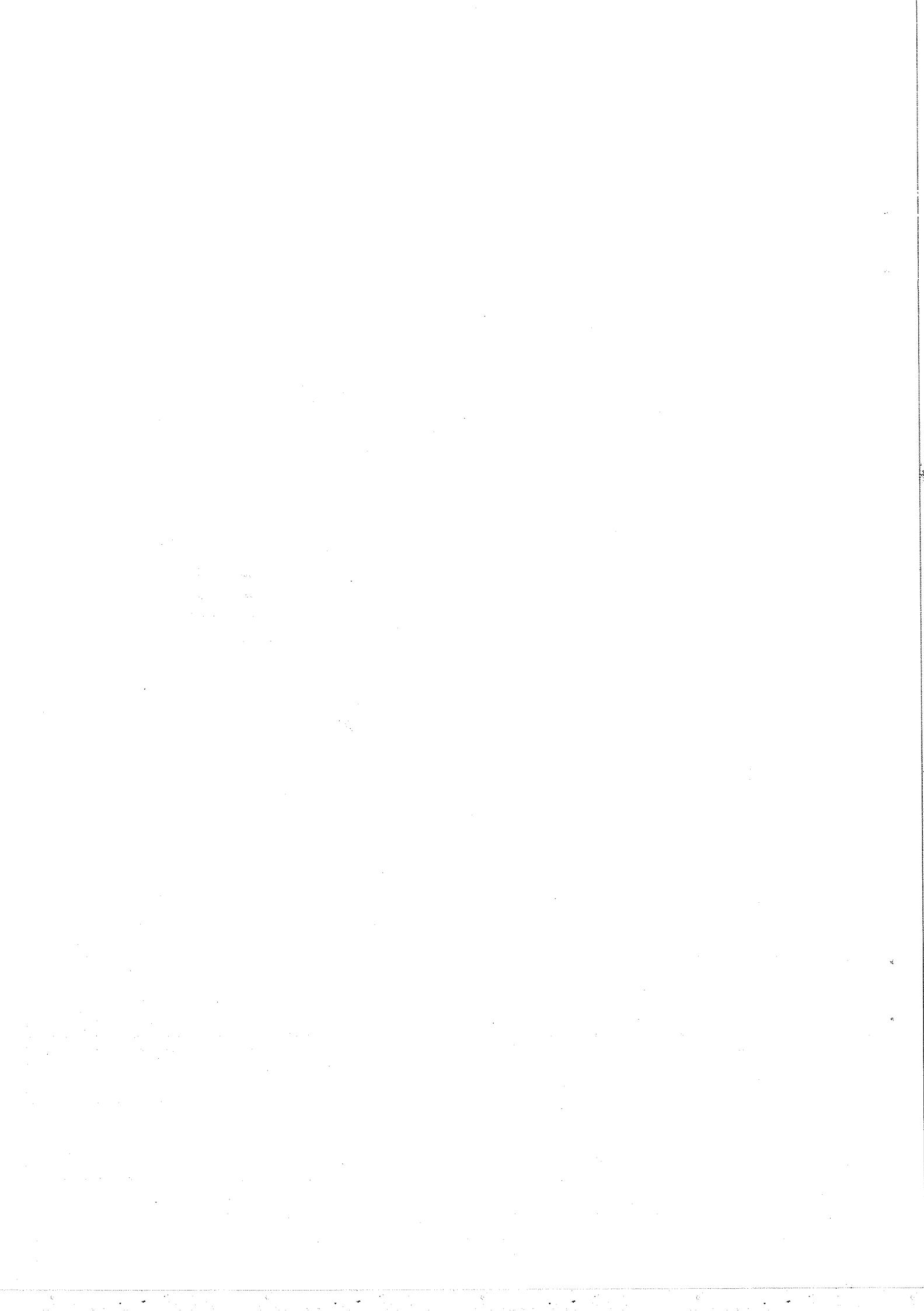


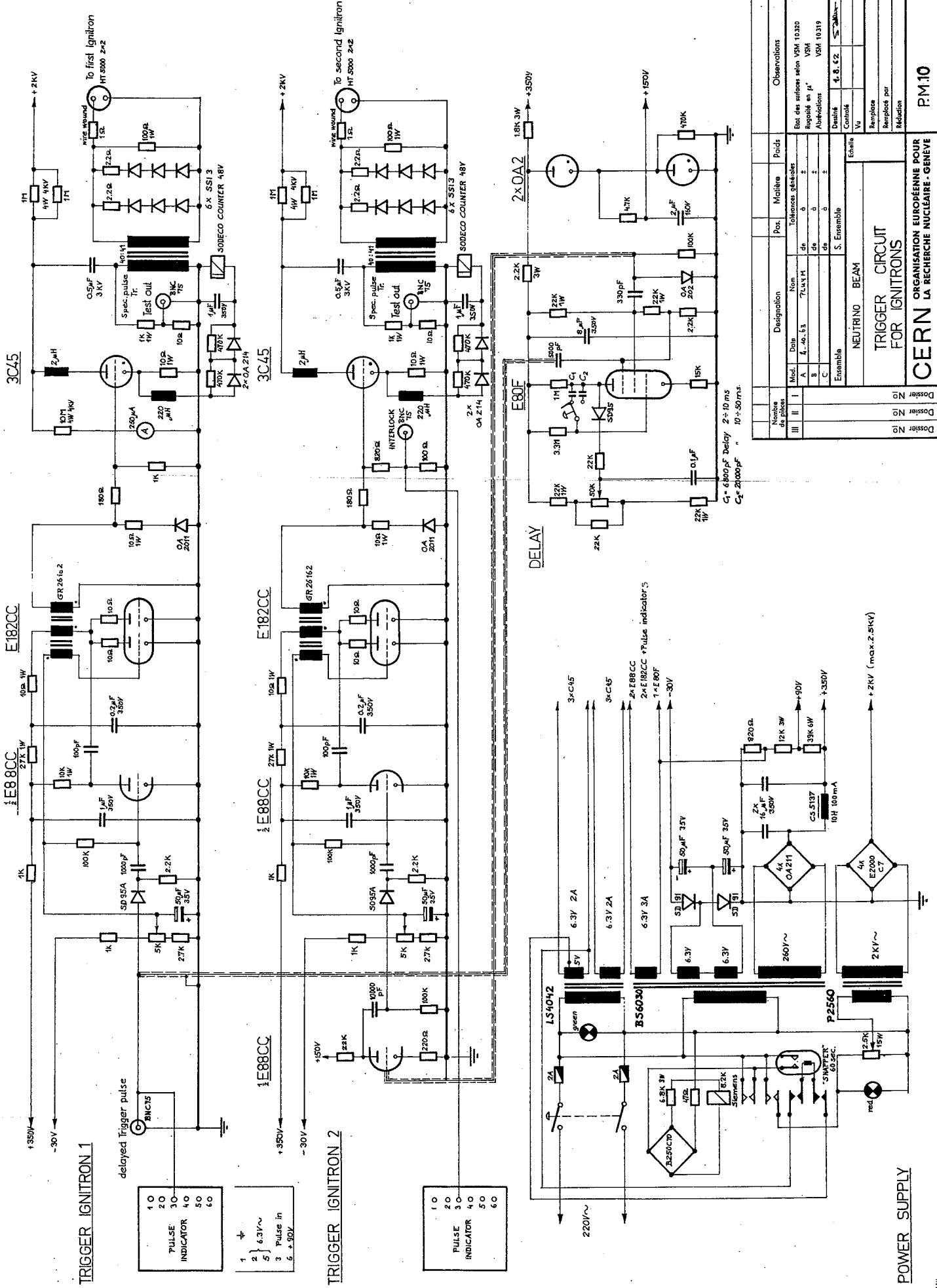
											Observations					
Nombre de pièces			Designation		Pos.	Matière	Poids									
III	II	I	Mod.	Date	Nom	Tolérances générales										
			A	4-10-63	PLUYM	de	à	±	Etat des surfaces selon VSM 10320							
			B			de	à	±	Rugosité en μ " VSM							
			C			de	à	±	Abréviations VSM 10319							
Ensemble			S. Ensemble						Dessiné	29-6-62.	Gi					
NEUTRINO BEAM									Contrôlé							
REMOTE CONTROL UNIT FOR P.M. POWER SUPPLY									Vu							
									Remplace							
									Remplacé par							
									Réduction							
Dossier N°	Dossier N°	Dossier N°	CERN ORGANISATION EUROPÉENNE POUR LA RECHERCHE NUCLÉAIRE - GENÈVE						P.M.08							

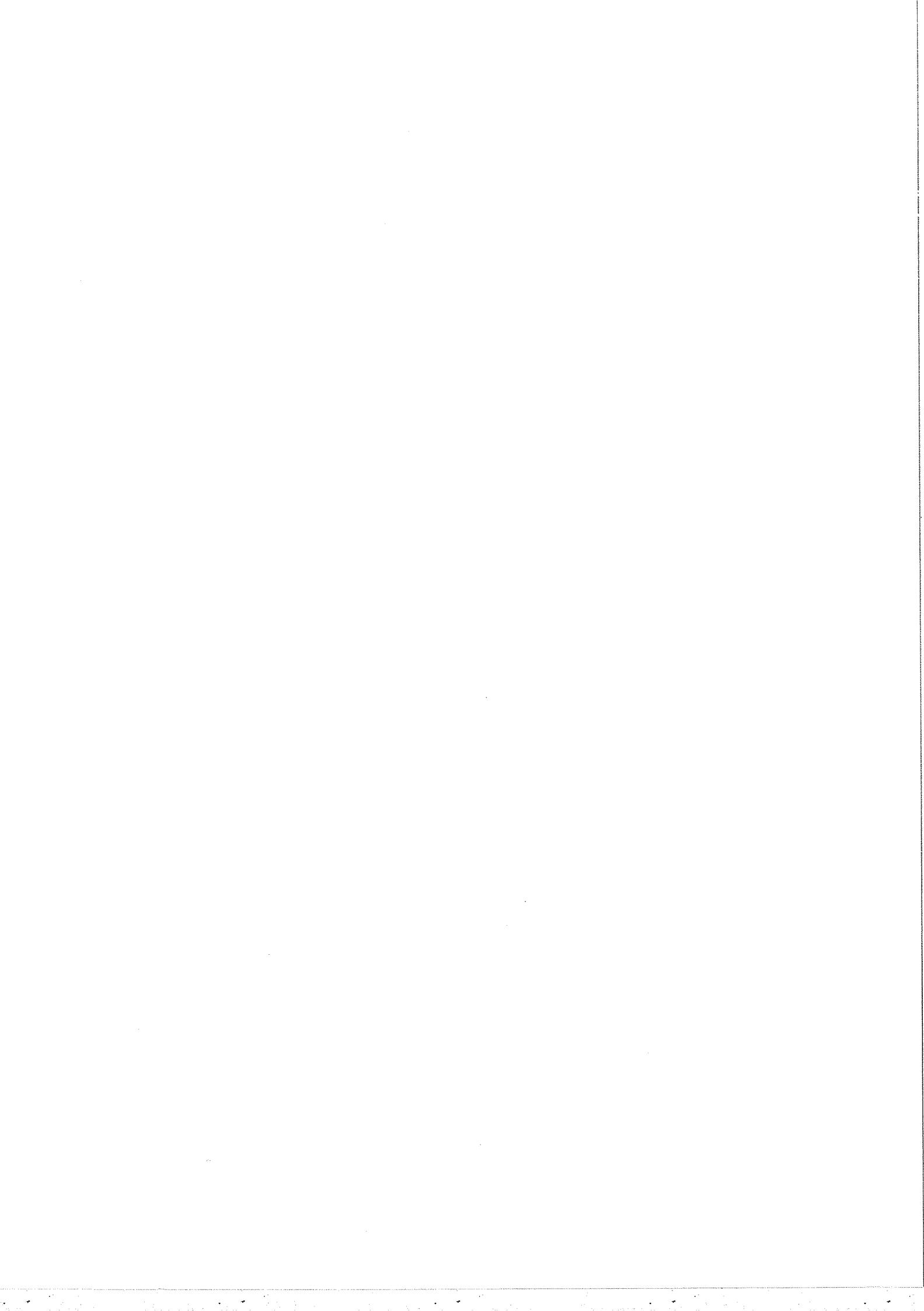




			Designation		Pos.	Matière	Poids	Observations							
Nombre de pièces	Mod.	Date	Nom	Tolérances générales											
III	II	I	A	4-10-63	PLuy M	de	à	±	Etat des surfaces selon VSM 10.320						
			B			de	à	±	Rugosité en μ VSM						
			C			de	à	±	Abréviations VSM 10.319						
			Ensemble		S. Ensemble				Dessiné	27.8.62	S. 2000				
			NEUTRINO BEAM						Controlé						
			CONNECTIONS TO CAPACITOR BANK						Vu						
Dossier No	Dossier No	Dossier No							Echelle						
CERN ORGANISATION EUROPÉENNE POUR LA RECHERCHE NUCLÉAIRE - GENÈVE						P.M. 09									

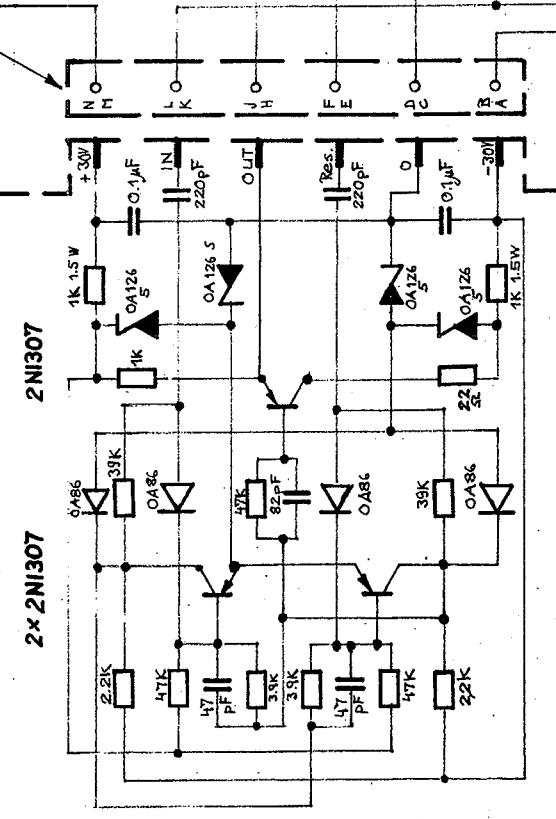






OC450K
PRINTED CIRCUIT SOCKET
16 PINS

12 PINS



PRINTED CIRCUIT

三

The diagram shows the internal circuitry of the T12M module. It consists of a rectangular frame containing several components. On the left side, there is a vertical column of components labeled 01, 02, 03, and 04 from top to bottom. A horizontal line connects these components. On the right side, there is another vertical column of components labeled 05, 06, 07, and 08 from top to bottom. A horizontal line connects these components. The top and bottom edges of the frame have small arrows pointing outwards, indicating the direction of signal flow or power supply.

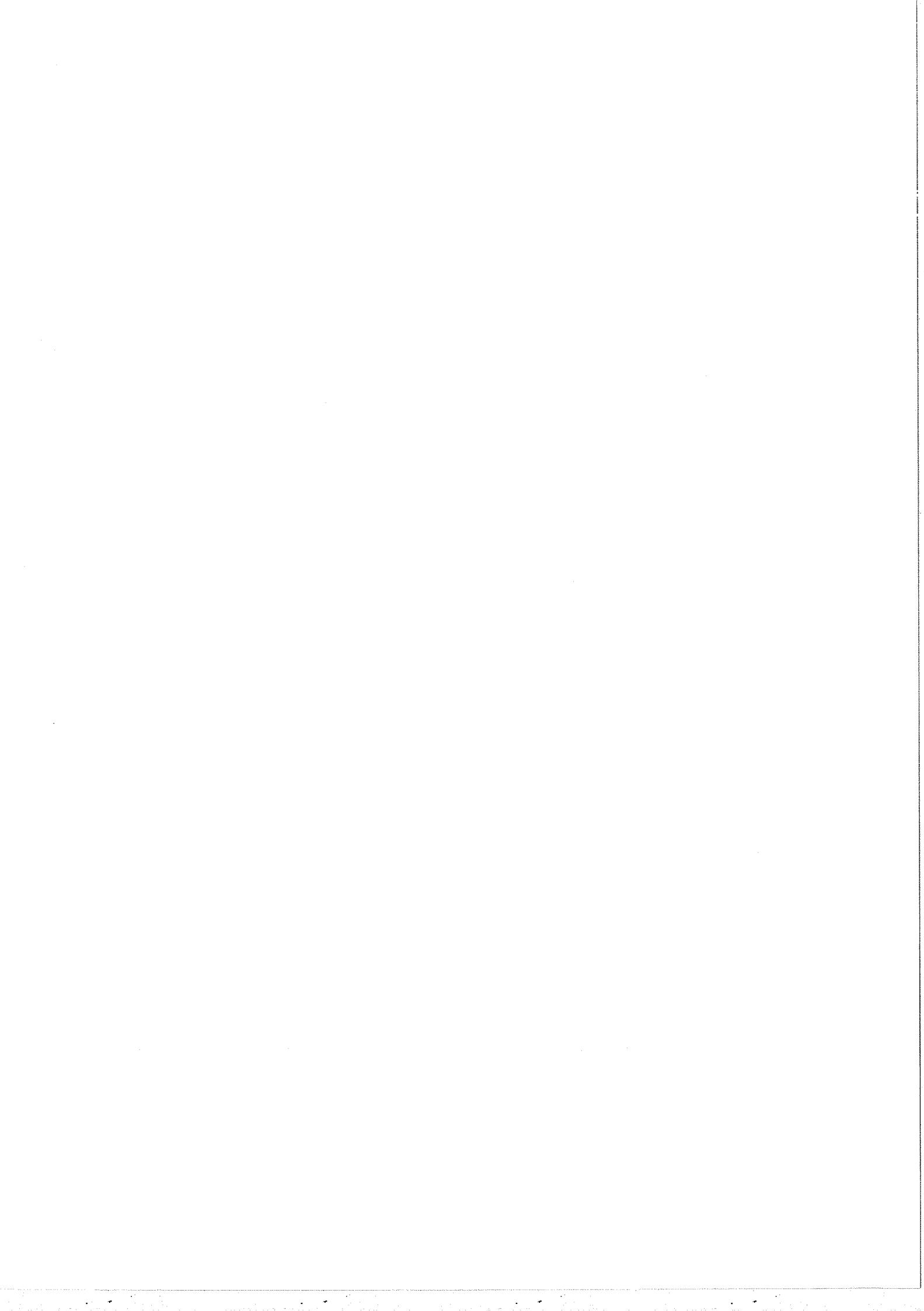
PULSE INDIC. OUT

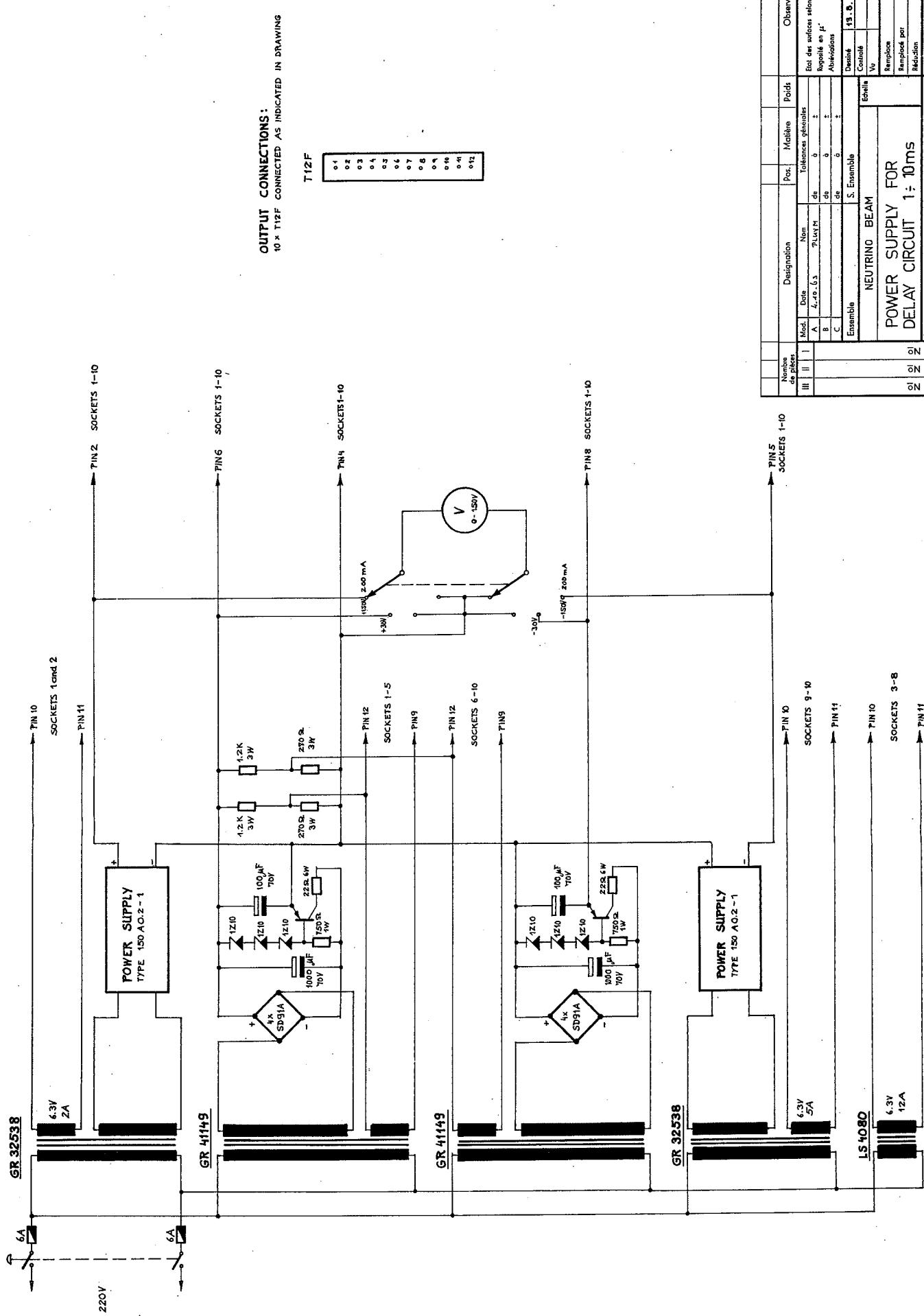
PULSE INDICATORS:	1	$\frac{1}{2}$
	2	$\frac{1}{2}$
	5	$\frac{1}{2}$
	3	$\frac{1}{2}$
	6	$\frac{1}{2}$

PULSE	10
	20
INDIC.	30
	40
OUT	50
	60

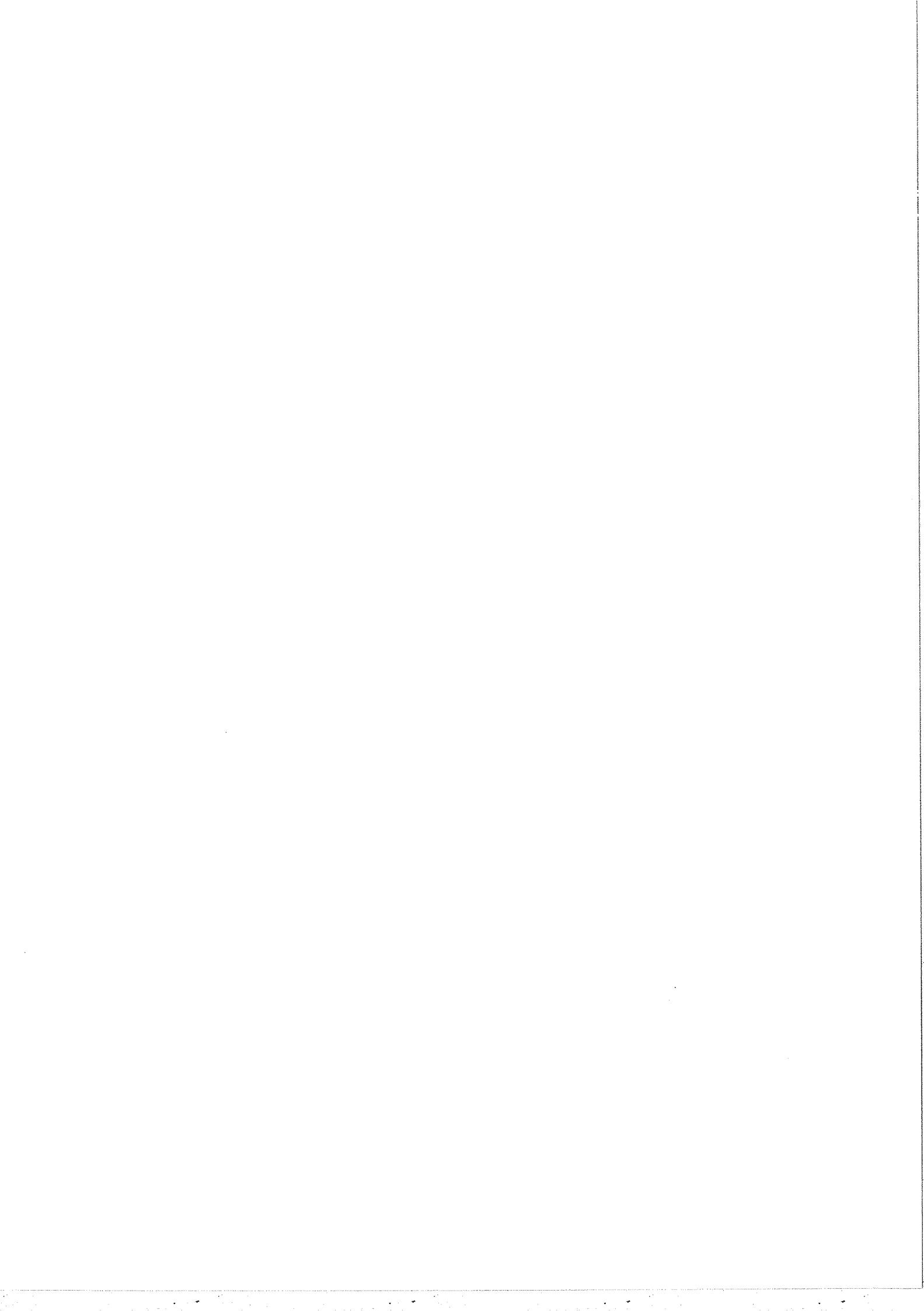
2" VARIABLE DELAY" CIRCUITS IN ONE "3UNITS" STANDARD RACK

SIS/R/7116





DIS/BR/12122



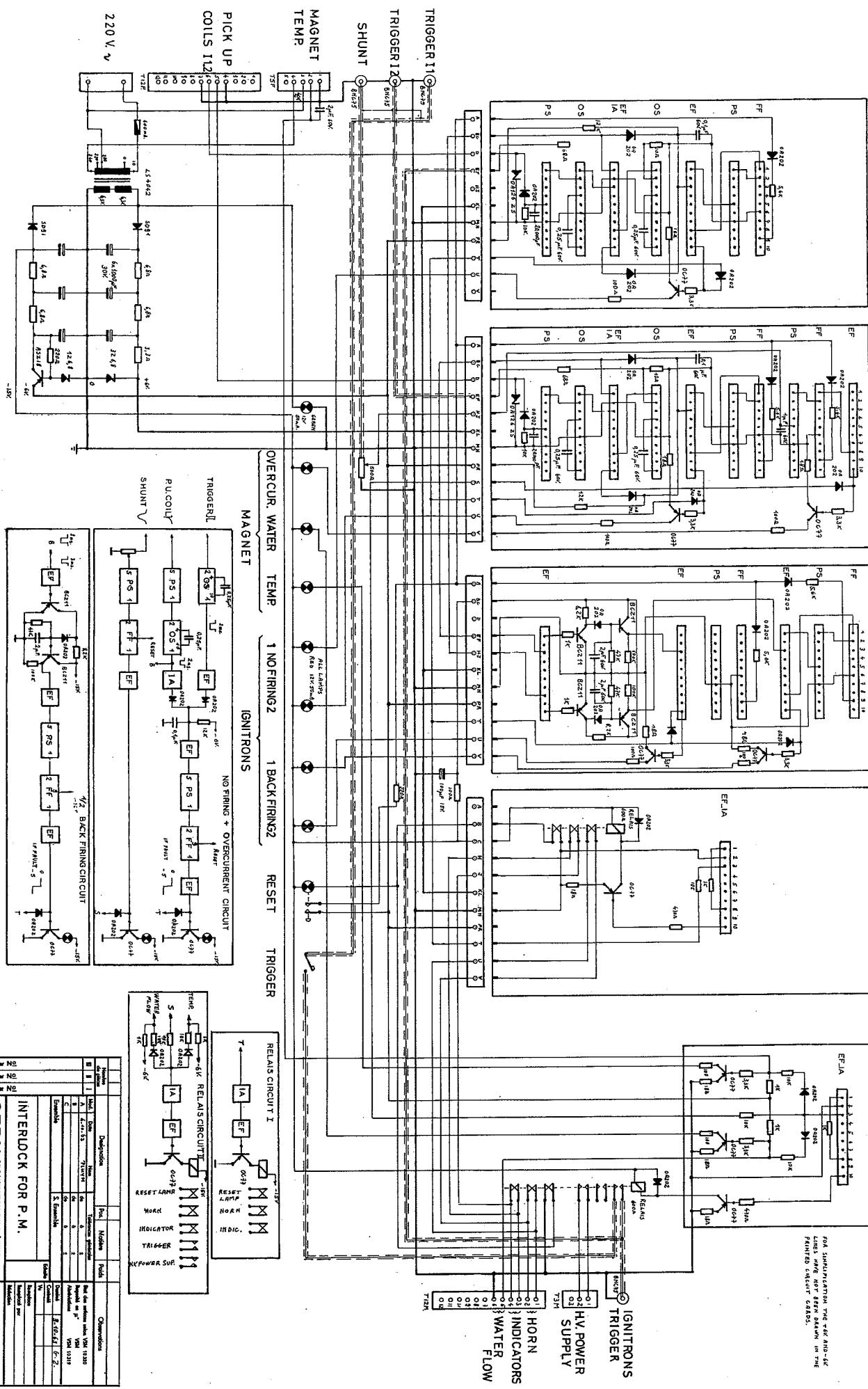
NO FIRING 11

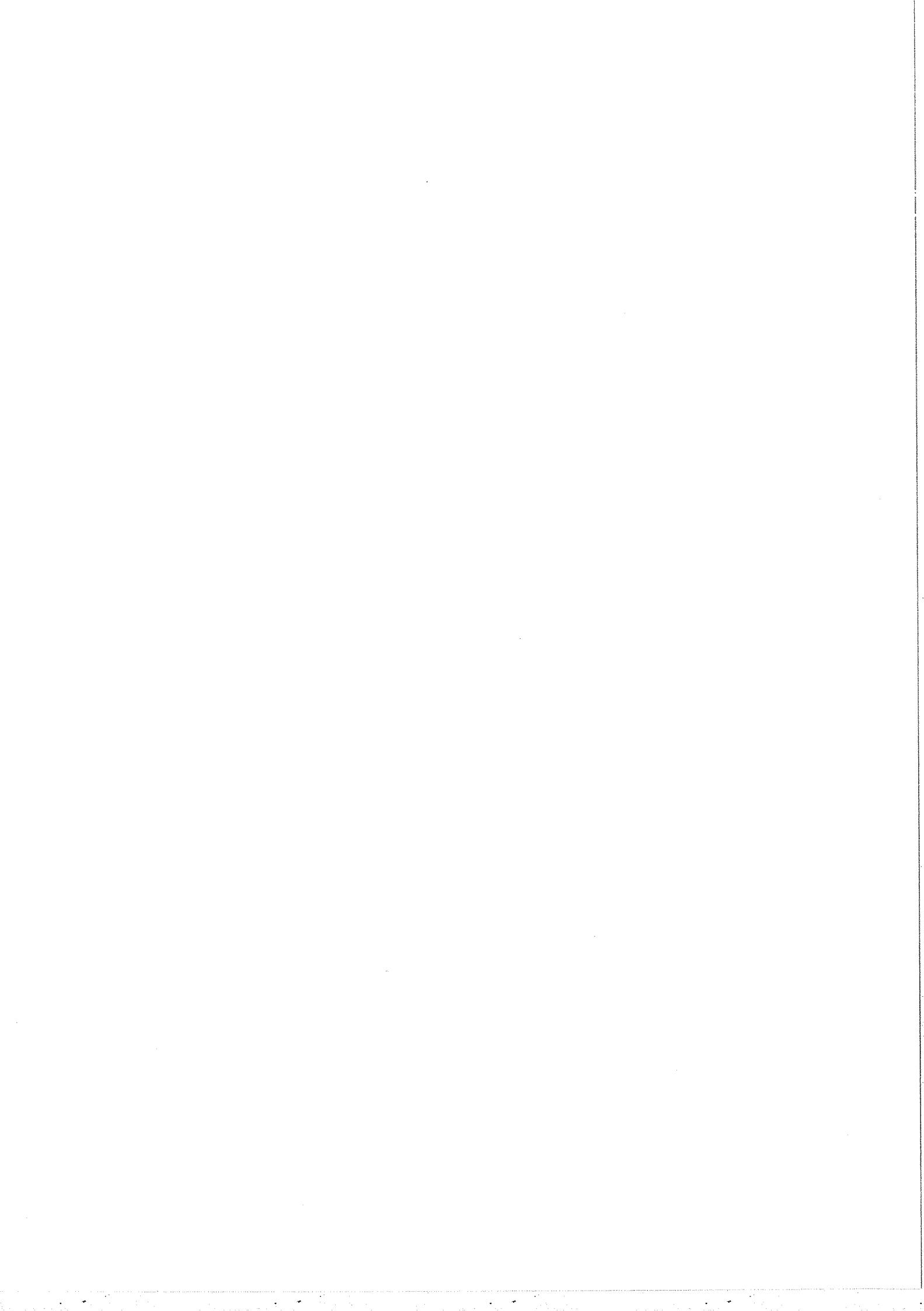
NO FIRING 12 & OVERCURRENT

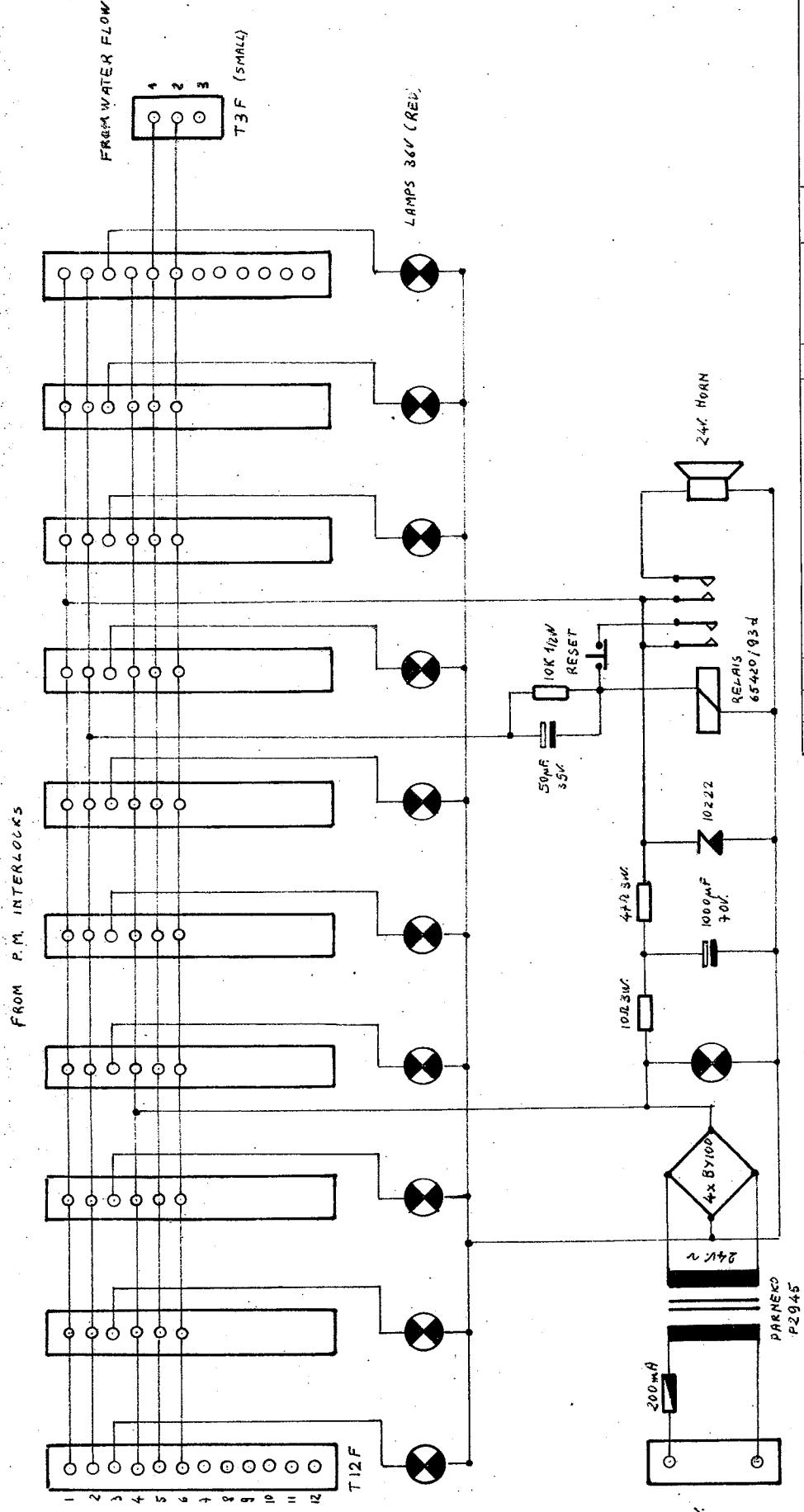
BACK FIRING 11-12

RELAYS CIRCUIT I

RELAYS CIRCUIT II







		Designation			Pos.	Matière	Poids	Observations	
Nombre de pièces	Mod.	Date	Nom	Tolérances générales					
III	II	I	A 4-10-63	Plastique	de	à	±	Etat des surfaces selon VSM 10320	VSM
			B		de	à	±	Rugosité en μ	VSM 10319
			C		de	à	±	Abréviations	
			Ensemble	S. Ensemble				Dessiné	4-10-63
								Controlé	6.2
								Remplacé	
								Remplacé par	
								Réduction	

INTERLOCK INDICATORS

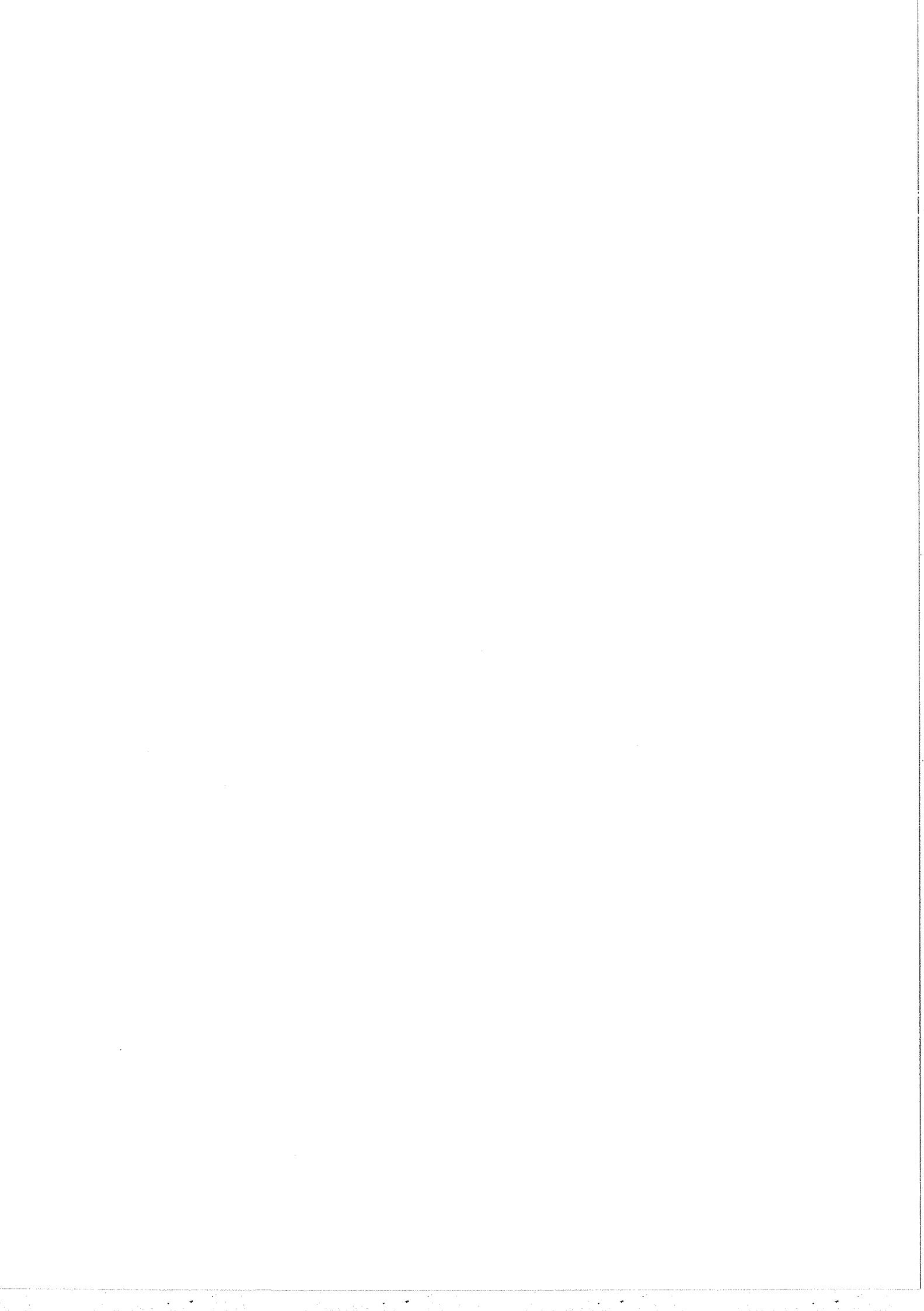
Dossier N° 10

Dossier N° 10

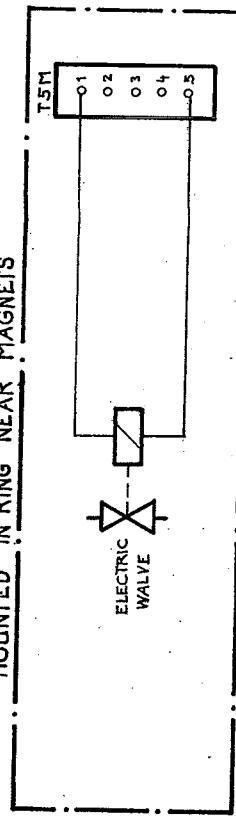
Dossier N° 10

CERN ORGANISATION EUROPÉENNE POUR LA RECHERCHE NUCLÉAIRE - GENÈVE

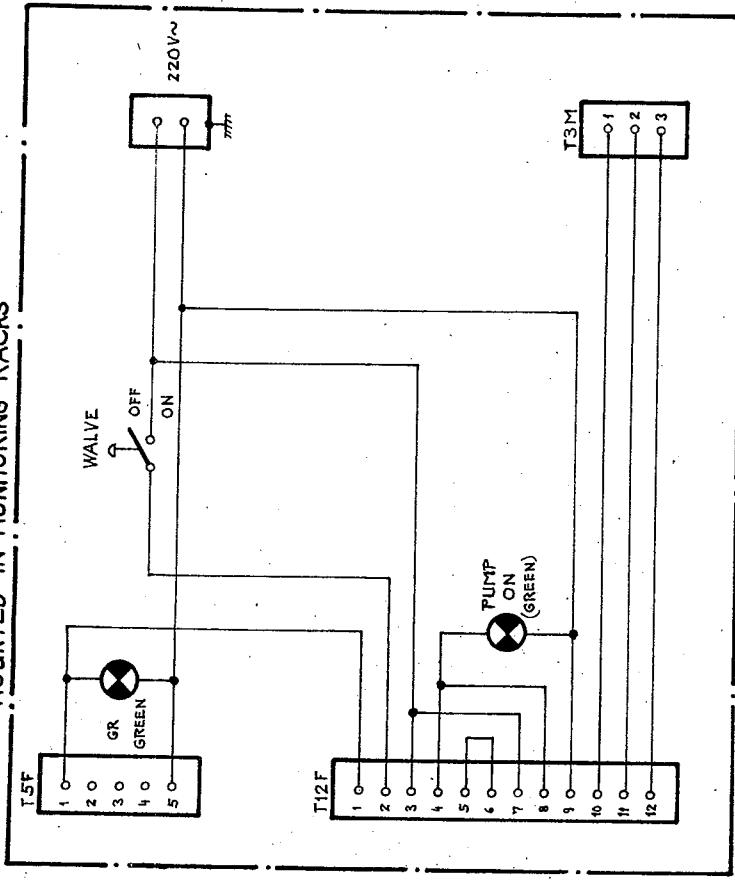
P. M. 14



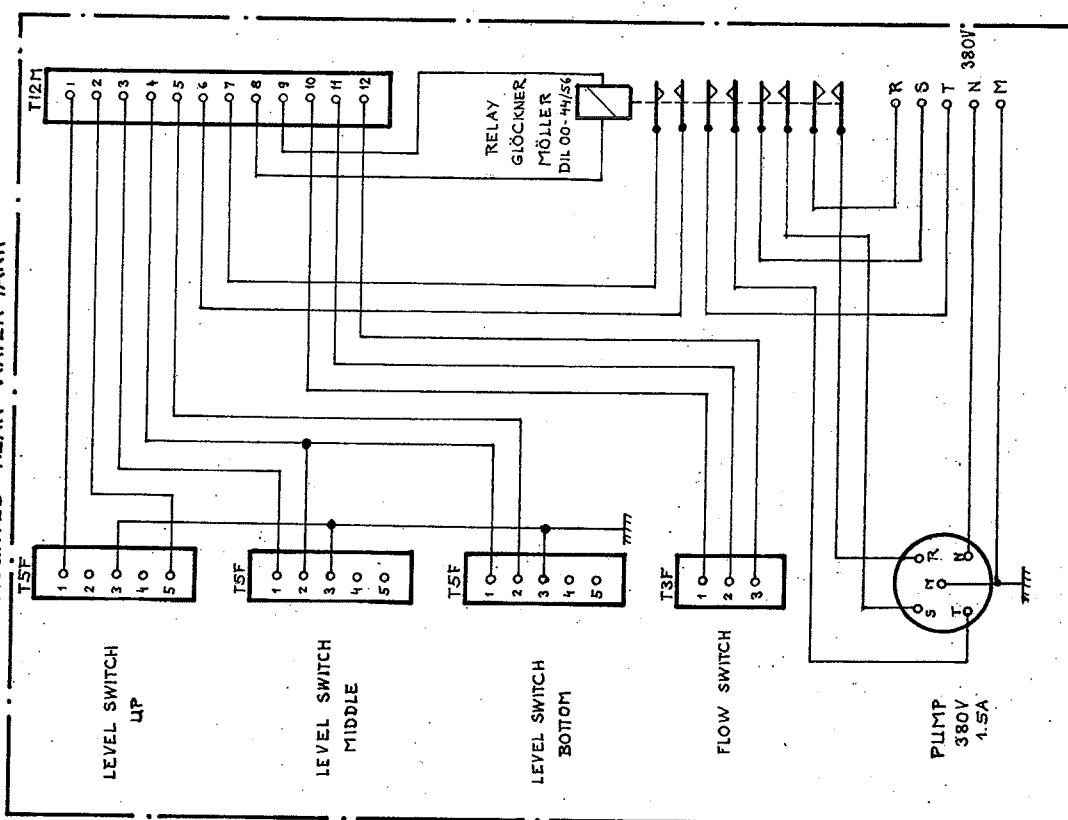
MOUNTED IN RING NEAR MAGNETS

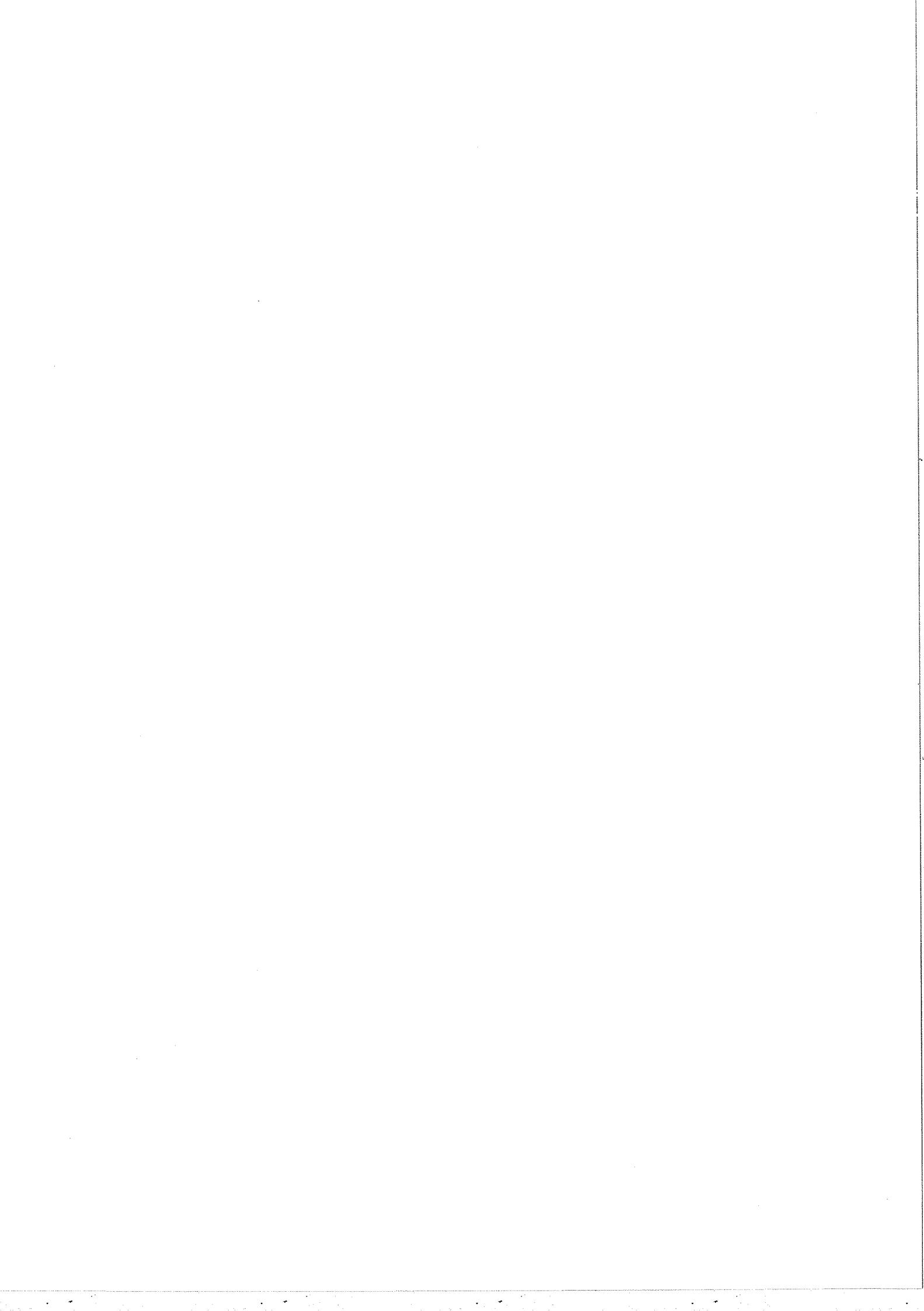


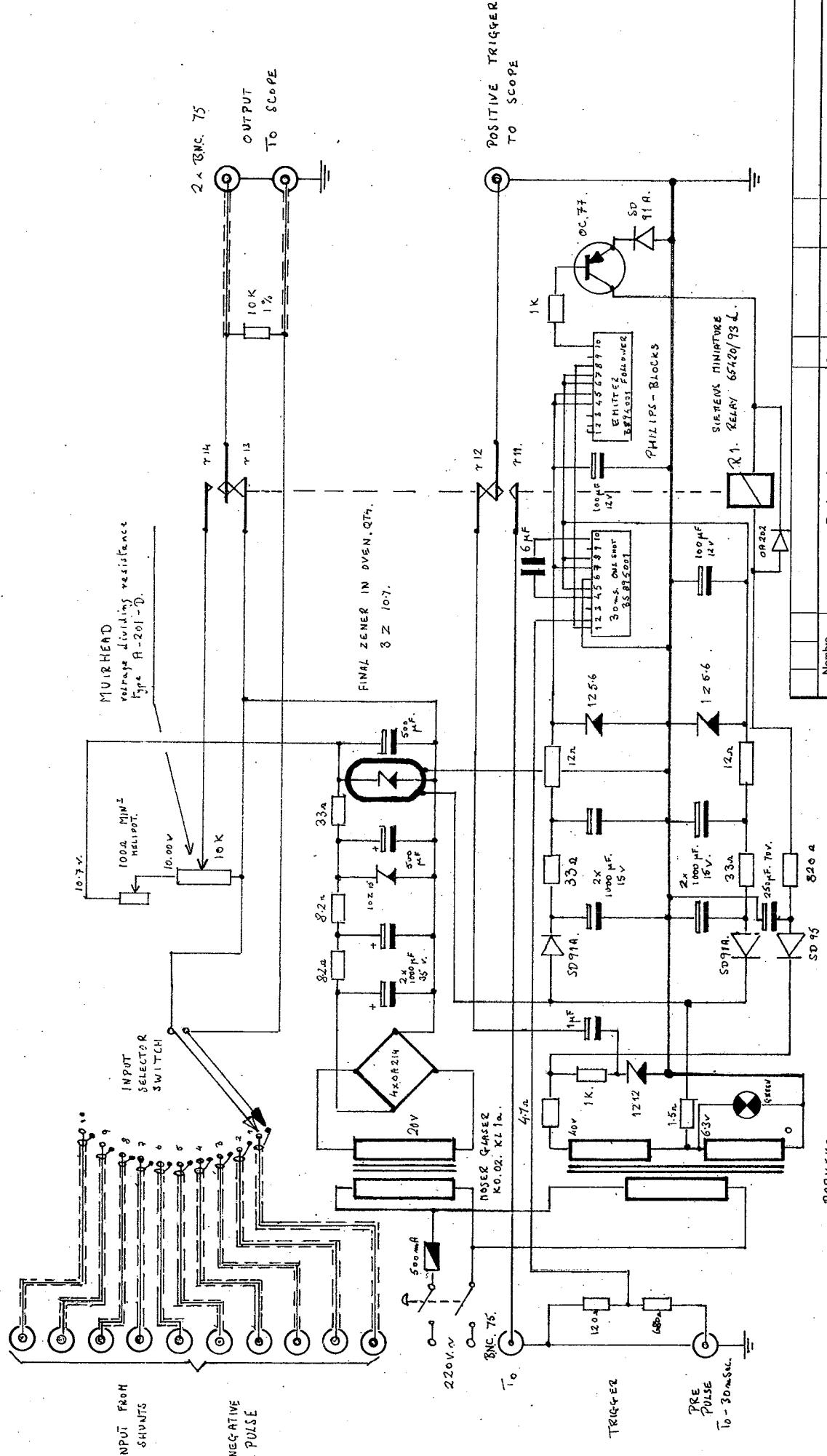
MOUNTED IN MONITORING RACKS



MOUNTED NEAR WATER TANK







SIS/R/7116

