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TOLERANCE STUDY FOR ACCELERATING STRUCTURE IN THE CLIC MAIN LINAC

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Abstract

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The tolerances are essential for defining the fabrication shape accuracy specification as well as the structure temperature control. In this paper, the tolerance study for a single accelerating structure of the CLIC main linac is described. The change in the nominal accelerating voltage caused by systematic temperature errors, systematic geometrical errors and random geometrical errors has been investigated. The tolerances related to the change in the field distribution and the reflections in the structure have also been analysed.

Ⅰ. Introduction

The Compact Linear Collider (CLIC) is a high-luminosity linear electron–positron collider covering a center-of-mass energy range from 380 GeV to 3 TeV under development [1,2]. For 3 TeV stage, the novel two-beam acceleration scheme is utilized to reduce the number of klystrons to a manageable value and to build at reasonable cost. The accelerating structure (AS) optimized for 3 TeV CLIC is named CLIC-G* [3,4] and the one designed for 380 GeV CLIC is named CLIC-380 [5]. For the first CLIC stage of 380 GeV, an alternative klystron-based scenario using X-band klystrons to produce the RF power is proposed. The accelerating structure designed for the main linac of the klystron-based CLIC is named CLIC-K [6]. The parameters of these three different structures are presented in Table. 1 for comparison.

In this paper, two effects of the errors on the performance of one travelling wave accelerating structure have been investigated. The change in the nominal accelerating voltage caused by three different effects including systematic geometrical and temperature errors and random geometrical errors has been analyzed in Sec. Ⅱ. The tolerances on the geometrical errors given by the acceptable reflection along the structure resulting in the change of filed distribution are described in Sec. Ⅲ. In the main body of the paper, we only present the results for the CLIC-K structure. The tolerance results in CLIC-G* and CLIC-380 are summarized in Appendix Ⅰ and Ⅱ, respectively.

Ⅱ. Tolerances given by the effect of the reduction of the nominal accelerating voltage

Accelerating voltage is one of the key performances of the accelerating structure. The errors on the

voltage will cause the deviation of the beam energy gain from the designed one. The acceptable AS-to-AS voltage error should be below \pm 1% [7].

Figure 1. Schematic diagram of an accelerating structure with N cells.

For an accelerating structure with N cells shown in Figure 1, the accelerating voltage can be calculated by adding up the voltages of each cell. The accelerating voltage of cell *i* is determined by the accelerating gradient G_i and the RF phase, as expressed in Equation (1), where L_c is the cell length, φ_0 is the input RF phase and $\delta \varphi_c^i$ is the phase error of cell i. The nominal accelerating voltage is calculated by Equation (2), where φ_s is the designed RF synchronous phase. If there is no phase error in every cell, the RF input phase *φ⁰* should be the same as the designed RF synchronous phase *φs*. Considering the phase errors, the RF input phase φ_0 can be optimized to minimize the accelerating voltage error so that the tolerances can be less critical for the same limited voltage error, as illustrated in Figure 2. The change in the nominal accelerating voltage caused by three different effects will be analyzed in the following parts.

$$
V = \sum_{i=1}^{N} V_i, \quad V_i = G_i L_c \cos(\varphi_0 + \delta \varphi_i), \quad \delta \varphi_i = \sum_{j=1}^{i} \delta \varphi_c^j
$$
 (1)

$$
V_a = V_0 \cos(\varphi_s), \quad V_0 = \sum_{i=1}^N G_i L_c \tag{2}
$$

Figure 2. Distribution of the RF phase of each cell for the voltage error of 1% when (a) φ_0 is equal to φ_s and (b) φ_0 is optimized to minimize the voltage error.

II.1 Systematic temperature errors

The operating temperature of the accelerating structure is controlled by the water-cooling system. The deviation of the temperature from the nominal one has a systematic effect on the accelerating structure. For an expanded cell working at higher temperature than the nominal one, the dispersion curve will shift down, as shown in Figure 3. The impact on the RF phase caused by the temperature errors is given by Equation (3), where $ΔΦ₀$ is the nominal phase advance of each cell ($2π/3$) and *α* is the copper thermal expansion coefficient, which is 17 μm/m/K. The distributions of group velocity and normalized gradient of CLIC-K used to calculate the tolerances are presented in Figure 4. The same analysis has also been applied to the CLIC-G* and CLIC-380 structures.

$$
\delta \varphi_c^j = L_c \delta k_c^j + k_c^j \delta L_c
$$
\n
$$
k_c^j = \omega_0 / c, L_c = \Delta \Phi_0 c / \omega_0, \delta L_c = L_c \alpha \delta T
$$
\n
$$
\delta k_c^j = -(dk_z / d\omega) \delta \omega_c = \omega_0 \alpha \delta T / v_s^j
$$
\n
$$
\Rightarrow \delta \varphi_c^j = \left(1 + \frac{c}{v_s^j}\right) \Delta \Phi_0 \alpha \delta T
$$
\n(3)

Figure 3. Dispersion curves of nominal cell and expanded cell.

Figure 4. The distributions of group velocity and normalized unloaded and loaded gradient in CLIC-K. Four different cases have been considered for the temperature errors. The distributions of the RF phase in each cell for these four cases are shown in Figure 5 for the unloaded gradient profile in CLIC-K structure. First, the RF input phase φ ⁰ is equal to the designed synchronous phase φ ^{*s*}. For the on-crest case where φ_s is 0 degree, the acceptable temperature error is 3.25 K in the unloaded case to satisfy the accelerating voltage requirement: $dV/V < 1\%$. For the off-crest operation where φ_s is equal to 30 degrees, which is the maximum value foreseen for operation in the CLIC main linac, the tolerance on the temperature, 0.47 K, is much more tight due to the more critical acceptable phase error for the same voltage error limited to less than 1%. If *φ⁰* is optimized, the required tolerances can be less critical. For the on-crest case, the voltage error can never be fully corrected by optimizing *φ⁰* because every cell is working on the maximum accelerating voltage in the nominal condition. Nevertheless, the acceptable temperature error is 5.95 K which is larger than for the non-optimized φ ⁰. For the off-crest operation, the deviation of accelerating voltage can always be corrected by optimizing *φ⁰* to utilize the cell-to-cell voltage compensation if the deviation of the temperature from the nominal one is less than 22.2 K in the unloaded case. Under the requirement of less than 1% and 2% accelerating voltage errors, the tolerances on the systematic temperature errors for four different conditions in both unloaded and loaded case in CLIC-K are summarized in Table 2.

Figure 5. The distributions of the RF phase and accelerating voltage error in each cell caused by systematic temperature errors in the unloaded case for the four different conditions in CLIC-K. Table 2. Tolerances on systematic temperature errors given by accelerating voltage error limited to < 1% and < 2% in CLIC-K.

II.2 Systematic geometrical errors

The RF phase errors are also caused by the geometrical errors in the process of fabrication. The error distributions can be divided into two categories: systematic geometrical errors and random geometrical errors. The effect caused by systematic geometrical errors is analyzed in the following part.

Figure 6. Sketch of a single cell geometry.

Table 3. Sensitivity of the frequency to geometrical error for different dimensions in the middle cell of CLIC-K.

Dimension x	df/dx [MHz/mm]			
	First cell	Middle cell	Last cell	
b	-1157.13	-1142.59	-1127.41	
a	442.34	323.40	205.53	
1W	-291.37	-293.96	-298.23	
W	-35.64	-34.45	-32.52	
L	-64.74	-40.76	-23.14	
d	161.83	101.78	54.01	
Rr	114.36	105.40	97.39	
R ₁	23.93	25.19	25.86	
R ₂	2.55	2.62	2.62	

The sketch of a single cell geometry is illustrated in Figure 6. The frequency of the cell is determined by a number of dimensions. The tolerances are dominated by the most critical geometrical parameter, which is the inner radius b of the cell, as presented in Table 3. The relationship between the phase advance of one cell and the frequency is shown in Figure 7. The RF phase errors caused by the geometrical errors can be calculated by Equation (4).

$$
\delta \varphi_c^j = L_c \delta k_c^j, L_c = \frac{\Delta \Phi_0}{2\pi} \frac{c}{f_0}
$$
\n
$$
\delta k_c^j = -\frac{dk_z}{d\omega} \frac{d\omega}{df} \delta f = -\frac{2\pi}{v_s^j} \frac{df}{dx} \delta x
$$
\n
$$
\begin{aligned}\n\mathbf{f} \\
\mathbf{f} \\
\mathbf{f}_0\n\end{aligned}
$$
\n1.11

\n1.12

\n1.13

\n1.14

\n1.14

\n2.15

\n3.15

\n4.16

\n5.17

\n6.18

\n7.19

\n8.10

\n9.10

\n1.10

\n1.11

\n1.11

\n1.12

\n1.13

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\n1.12

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\n1.15

\n1.10

\n

Figure 7. Dispersion curves of nominal cell and cell with larger radius.

The tolerances on the systematic geometrical errors of the most critical dimension b for four different conditions in both unloaded and loaded case to satisfy the limited accelerating voltage errors < 1% and < 2% are summarized in Table 4. The distributions of the RF phase of each cell for the same four cases as in the previous section in the unloaded case of CLIC-K are shown in Figure 8.

Figure 8. The distributions of the RF phase and accelerating voltage error of each cell caused by systematic geometrical errors in the beam unloaded case for the four different conditions in CLIC-K. Table 4. Tolerances on systematic geometrical errors given by accelerating voltage error limited to < 1% and < 2% in CLIC-K.

The systematic errors caused by the deviation of the temperature and of the geometry have similar impact on the accelerating voltage. Consequently, the temperature can be used to compensate the systematic geometrical errors on average along the structure. The compensation relationship between the temperature and systematic geometrical errors in CLIC-K is shown in Figure 9. 5.54 K temperature can compensate for 1 μm systematic errors on the most critical dimension b in CLIC-K. Even the deviation of the accelerating voltage caused by 20 μm systematic errors on dimension b can be corrected to be below 0.01% by temperature correction of -110.5 K. The distributions of the RF phase of each cell for this case is shown in Figure 10 for illustration only, since in practice such a large temperature correction is unlikely to be used.

Figure 9. Accelerating voltage compensation for systematic geometrical errors on dimension b by temperature in CLIC-K.

Figure 10. The distributions of the RF phase and accelerating voltage error of each cell caused by systematic geometrical errors in the beam unloaded case of CLIC-K after the compensation from temperature.

II.3 Random geometrical errors

The change in the nominal accelerating voltage caused by the random geometrical errors has also been investigated. As an example, the most critical dimension b has been taken for the following analysis but it is applicable to any dimension or to any combination of the dimensions resulting to the same frequency error.

Figure 11. Uniform distribution of the random geometrical errors on dimension b.

The distribution of the random geometrical errors on dimension b is assumed to be uniform, as shown in Figure 11. For the condition where φ_0 is equal to φ_s , the distributions of accelerating voltage errors of 1 million seeds in the structure CLIC-K are presented in Figure 12(a) and 12(b). The tolerance is given when the width of half maximum of the voltage deviation is 1%. For *φ⁰* is optimized, the impact of the random geometrical errors on dimension b is analyzed with 30 thousand seeds. In the case of to $\varphi_s = 0$, the tolerance is slightly larger compared in the case of optimized φ *₀*. For the off-crest case of φ _{*s*=30} degree, it can be concluded from Figure 12(d) that the accelerating voltage error caused by random geometrical errors on the dimension b can always be corrected by adjusting the RF input phase. The distributions of optimized RF input phase *φ⁰* for 0 degree and 30 degrees synchronous phase in beam loaded case of CLIC-K are illustrated in Figure 13.

Figure 12. The distributions of accelerating voltage errors caused by random geometrical errors for the four different cases in the loaded CLIC-K.

Figure 13. The distributions of optimized RF input phase *φ⁰* for two different synchronous phase in loaded case of CLIC-K.

Table 5. Tolerances on random geometrical errors given by limited < 1% and < 2% accelerating voltage error in CLIC-K.

Cases in CLIC-K		Random $ \Delta b $ [µm]		
			$ \Delta V/V_0 $ < 1%	$ \Delta V/V_0 $ < 2%
$\varphi_0 = \varphi_s$	$\varphi_s = 0^{\circ}$	Unloaded	5.5	7.8
		Loaded	5.8	8.2
		Unloaded	0.63	1.3
	$\varphi_s = 30^\circ$	Loaded	0.70	1.4
φ_0 optimized	$\varphi_s = 0^{\circ}$	Unloaded	7.1	10.2
		Loaded	7.3	10.5
	$\varphi_s = 30^\circ$	Unloaded	Corrected	Corrected
		Loaded	Corrected	Corrected

The tolerances on the random geometrical errors of the most critical dimension b for four different conditions and three structures in both unloaded and loaded cases in CLIC-K are summarized in Table 5.

III. Tolerances given by the acceptable reflections

The machining errors on the geometry will also lead to the reflections along the structure. The reflections will introduce standing waves in the structure, which will enhance the local field as shown in Figure 14. This may increase the breakdown rate. Consequently, the tolerances on the geometry errors is necessary to be investigate to ensure the high-gradient performance. The acceptable reflection should be below -40 dB in order to keep the local field enhancement caused by the reflections below 1%. The reflection caused by the deviation of frequency from the nominal one is analyzed in [8]. The local reflection caused by geometrical error in n-th cell is expressed in Equation (5). Seen in front of m-th cell, this local reflection is expressed in Equation (6), where $\alpha(n)$ is the attenuate due to the ohmic loss. The total reflection seen in front of m-th cell is calculated by adding up all the reflections caused by the geometrical errors of the downstream cells, as presented in Equation (7). When $m = 1$, it is written as Equation (8), which means the global reflection at the input port. Moreover, we also concern about the maximum reflection along the full structure, as expressed in Equation (9).

$$
\Delta S_{11}^{local}(n) = j \frac{c}{v_g(n)} \frac{\Delta \Phi_0}{f_0} \frac{df}{dx} \delta x \tag{5}
$$

$$
\Delta S_{11}^{n\to m}(n) = e^{-j2(n-m)\Delta\Phi_0} e^{\alpha(n)} \Delta S_{11}^{local}(n), \alpha(n) = -\sum_{p=m}^{n-1} \frac{c\Delta\Phi_0}{v_g(p)Q_0(p)}
$$
(6)

$$
\Delta S_{11}^{global} \left(\mathbf{m} \right) = \sum_{n=m}^{N} \Delta S_{11}^{n \to m} \left(n \right) \tag{7}
$$

$$
\Delta S_{11}^{global}\left(1\right) = \sum_{n=1}^{N} \Delta S_{11}^{n\rightarrow 1}\left(n\right)
$$
\n(8)

$$
\max\left(\Delta S_{11}\right) = \max\left\{\Delta S_{11}^{global}\left(1\right), \Delta S_{11}^{global}\left(2\right), \Delta S_{11}^{global}\left(3\right),...\Delta S_{11}^{global}\left(N\right)\right\}
$$
(9)

Figure 14. The impact of the reflections from the twentieth cell on the distribution of electric field along the structure with same input amplitude.

The tolerance of each cell on the most critical dimension b to keep the local reflection less than -40 dB in CLIC-K is presented in Figure 15. It is obvious that the tightest tolerance, 0.35 μm, is given by the last cell due to the lowest group velocity. For random geometrical errors, the tolerances can be given by limiting the global reflection seen at input port (Eq.8) and the maximum reflection along the structure (Eq. 9). The latter tolerance is more critical than the former one. The distributions of global reflection and maximum reflection in CLIC-K are shown in Figure 16 and 17. The tolerances on the most critical dimension b for three different level of reflections are summarized in Table 6.

Figure 15. The tolerance of each cell on dimension b given by -40 dB local reflection in CLIC-K

Figure 16. The distribution of global reflection at input port in CLIC-K when geometrical error on dimension b is random within \pm 0.34 μ m.

Figure 17. The distribution of maximum reflection along the structure CLIC-K when geometrical error on dimension b is random within \pm 0.173 μ m.

Ⅳ. Summary

The tolerance study for the single accelerating structure has been carried out. Two effects on the performance of the accelerating structure have been investigated: the change in the nominal accelerating voltage and the change in the field distribution along the structure. The tolerances given by the change in the nominal accelerating voltage caused by systematic temperature errors, systematic geometrical errors and random geometrical errors have been analyzed. The temperature can be used to compensate the systematic geometrical errors on the average along the structure due to the similar impact on the accelerating voltage. The deviation of the accelerating voltage caused by the random geometrical errors can be corrected by optimizing the RF input phase. The reflections in the structure which will enhance the local field and increase the breakdown rate can be divided into three categories: local reflection, global reflection at input port and the maximum reflection along the structure. Limiting the reflection below -40 dB requires that the tolerance on the most critical dimension b should be less than 0.5 μm. In the future, more complicated tolerance study on the AS-to-AS errors will be conducted.

Appendix Ⅰ

The same analysis has also been done for the CLIC-G* structure. The tolerances for systematic temperature errors, systematic and random geometrical errors, and acceptable reflections are following. Table 7. Tolerances on systematic temperature errors given by accelerating voltage error limited to < 1% and $<$ 2% in CLIC-G*.

Dimension x		df/dx [MHz/mm]	
	First cell	Middle cell	Last cell
b	-1132.72	-1097.91	-1027.65
a	355.29	284.84	209.79
iw	-299.21	-323.61	-360.05
W	-42.96	-46.95	-73.96
L	-48.13	-35.79	-24.82
d	109.99	77.60	47.29
Rr	106.85	103.92	99.64
R ₁	26.09	26.68	26.16
R ₂	2.72	3.40	3.50

Table 9. Tolerances on systematic geometrical errors given by accelerating voltage error limited to < 1% and $<$ 2% in CLIC-G*.

Figure 18. Accelerating voltage compensation for systematic geometrical errors on dimension b by temperature in CLIC-G*.

Table 10. Tolerances on random geometrical errors given by limited < 1% and < 2% accelerating voltage error in CLIC-G*.

Appendix Ⅱ

In this part, the results of tolerances in CLIC-380 is summarized.

Table 12. Tolerances on systematic temperature errors given by accelerating voltage error limited to < 1%

and $<2\%$ in CLIC-380.

Cases in CLIC-380			Systematic $ \Delta T $ [K]	
			$ \Delta V/V_0 $ < 1%	$ \Delta V/V_0 $ < 2%
$\varphi_0 = \varphi_s$		Unloaded	3.63	5.14
	$\varphi_s = 0^{\circ}$	Loaded	4.01	5.68
		Unloaded	0.51	1.01
	$\varphi_s = 30^\circ$	Loaded	0.58	1.14
φ_0 optimized		Unloaded	6.76	9.58
	$\varphi_s = 0^{\circ}$	Loaded	7.06	10.0
	$\varphi_s = 30^\circ$	Unloaded	26.1	26.9
		Loaded	27.3	28.2

Table 14. Tolerances on systematic geometrical errors given by accelerating voltage error limited to < 1% and $<$ 2% in CLIC-380.

Figure 19. Accelerating voltage compensation for systematic geometrical errors on dimension b by temperature in CLIC-380.

Table 15. Tolerances on random geometrical errors given by limited < 1% and < 2% accelerating voltage error in CLIC-380.

Cases in CLIC-380			Random $ \Delta b $ [µm]	
			$ \Delta V/V_0 $ < 1%	$ \Delta V/V_0 $ < 2%
$\varphi_0 = \varphi_s$	$\varphi_s = 0^{\circ}$	Unloaded	7.6	10.8
		Loaded	7.9	11.3
	$\varphi_s = 30^\circ$	Unloaded	0.87	1.75
		Loaded	0.96	1.93
φ_0 optimized	$\varphi_s = 0^{\circ}$	Unloaded	9.6	13.6
		Loaded	10.1	14.6
	$\varphi_s = 30^\circ$	Unloaded	Corrected	Corrected
		Loaded	Corrected	Corrected

Table 16. The tolerances on the dimension b giving by limiting the reflection below -40 dB, -30 dB and -20 dB in CLIC-380.

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