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Overview of the ATLAS Insertable B-Layer (IBL) Project

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The first upgrade for the Pixel Detector will be a new pixel layer which is currently under construction and will be installed during the first shutdown of the LHC machine, in 2013-14. The new detector, called the Insertable B-layer (IBL), will be installed between the existing Pixel Detector and a new, smaller radius beam-pipe. Two different silicon sensor technologies, planar n-in-n and 3D, will be used, connected with the new generation 130nm IBM CMOS FE-I4 readout chip via solder bump-bonds. A production quality control test bench was set up in the ATLAS inner detector assembly clean room to verify and rate the performance of the detector elements before integration around the beampipe. An overview of the IBL project, of the module design, the qualification for these sensor technologies, the integration quality control setups and recent results in the construction of this full scale new concept detector is discussed.

Keywords: ATLAS; IBL; Pixel Detectors; ATLAS Upgrade.

1. Introduction

During 2013-14, the Large Hadron Collider $(LHC)^1$ will upgrade to the design energy of 13 to 14 TeV and instantaneous luminosities of 10^{34} cm⁻²s⁻¹. At these energies and luminosities, the high number of interactions per bunch crossing can cause significant degradation of the performance of tracking, vertexing, and b-tagging algorithms of the ATLAS detector.² In addition, ATLAS is expected to acquire 300 fb^{-1} of data, for which the expected fluence of 50 MRad ionizing dose and 10^{15} n_{eq}/cm² non-ionizing dose will lead to significant radiation damage to the Pixel Detector.³ To mitigate these effects, a new fourth layer of pixel modules, the Insertable B-Layer (IBL) ,⁴ will be inserted between the current Pixel Detector and a new smaller radius beam-pipe . The IBL sensors will be located at a radius of 33mm from the beam-axis and cover $|\eta| < 2.9$. Due to the close proximity to the interaction point, the IBL must withstand 250 MRad of ionizing 2

dose and 5×10^{15} n_{eq}/cm² non-ionizing dose over the 300 fb⁻¹ lifetime, and maintain a hit efficiency of $> 97\%$. In addition, the front-end electronics must cope with high radiation levels and track occupancies, and must have a high active fraction due to the tight (2cm) space constraints of the IBL making shingling of sensors along the z-direction impossible. To meet these requirements, new sensors in conjunction with a new FE chip, the FE-I4, with reduced pixel size and low material budget have been developed.

2. IBL Geometry and Layout

The IBL consists of 14 staves arranged along the beam line and around the beam-pipe with a 14[°] incline allowing for an azimuthal overlap of the staves to ensure complete coverage in ϕ and to compensate for the Lorentz angle in the 2T solenoidal magnetic field of the ATLAS inner detector. The IBL staves are mounted on an Inner Positioning Tube (IPT), which surrounds a new smaller radius (∼24cm) beam-pipe, and inside of the IBL Support Tube (IST). The layout can be found in Figure 1. The (D_1) . The layer can be found in Figure 1.

Fig. 1. Left: Cross-section view $(r - \phi)$ of the IBL. Right: Longitu mounted on beam-pipe with connection to services Fig. 1. Left: Cross-section view $(r - \phi)$ of the IBL. Right: Longitudinal view of IBL

Each IBL stave is $64\mathrm{cm}$ $]$ bonded onto silicon sensors. Two kinds of sensors are used: the 12 central modules are Double-Chip (DC) planar sensors with two FE-I4s per sensor, and the outer-most 4 modules on each side are Single-Chip (SC) 3D sensors with one FE-I4 per sensor. IBL modules are constructed by PGP , it is also see that is a set of the PCB, the module flex, on each sensor and wire bonding the module flex to the front-end chip, allowing the routing of data and services to the FE-I4s. Each IBL stave is 64cm long and holds 32 FE-I4 chips which are bump system is foreseen and under construction. Limits \mathcal{L} ! Radiation hard: $\frac{1 \text{ m}{\text{m}}}{\text{m}}$ with one FE-I4 per sensor. IBL modules are constructed by gluing a flexible

The IBL staves are the mechanical support structure for the sensors and $\ddot{}$

electronics, and are built from a stiff K13C/RS3^a carbon fibre material filled with carbon foam^b (serving as the heat conductor) surrounding a titanium cooling pipe. The material budget of the staves is $\leq 0.6\%$ X_0 . In order to minimize radiation damage to the sensors, the IBL will be operated at $\leq -20^{\circ}\text{C}$ using evaporative CO₂ cooling. The FE-I4 side of the modules is glued onto the stave face-plate, and the module flex is wire-bonded to a flexible PCB bus (glued onto the side of the stave) via flex wings (one per front-end) to provide connection to services from outside of the stave.

The IBL planar sensors⁵ use an n^+ -in-n design and are manufactured by CiS^c. The sensors are 200 μ m thick. To minimize the inactive edge of the sensors to approximately 200 μ m, the structure of 13 guard rings is shifted into the active pixel region under elongated 500 μ m wide edge pixels.

The IBL 3D double-sided p-type sensors⁶ are 230 μ m thick with two n^+ electrodes per pixel surrounded by six p^+ biasing electrodes. The distance between electrodes in 67 μ m and the inactive edge of the sensor is minimized to approximately 200 μ m using a guard fence design. The small depletion width between electrodes leads to faster charge collection, lower charge trapping probability, and lower bias voltages than planar sensors. The sensors were produced by FBK^d and CNM^e.

The new FE-I4 front-end chip⁷ uses 130 nm feature size CMOS process to cope with the higher hit occupancy and radiation levels expected for the IBL. The FE-I4 has an active area of 16.8 mm \times 20 mm and contains the readout for 26880 hybrid pixels of size 50 μ m \times 250 μ m arranged in 80 columns and 336 rows which are connected to the sensors by bump bonding each individual pixel cell. Each pixel cell contains an amplification stage, with adjustable shaping, followed by a discriminator, with adjustable threshold, which allows the measurement of the discriminator firing time and the time-over-threshold (ToT). The pixel array is subdivided into 2×2 pixel regions which share a common Pixel Digital Region (PDR) holding memory, hit logic, and trigger processing. The PDR allows for high occupancy running, lower power consumption, and improved active areas. As such, the FE-I4 has a maximum trigger rate of 200 kHz and a hit inefficiency of $\langle 1\% \text{ at occupies of } 400 \text{ MHz } / \text{ cm}^2.$

The performance of unirradiated and irradiated (with fluences of \geq

^aProvided by TenCare, USA, http://www.tencare.com

^bProvided by Allcomp Inc., USA, http://www.allcomp.com

^cCiS, Erfurt, Germany, http://www.cismst.org

^dFondazione Bruno Kessler, Trento, Italy, http://www.fbk.eu

^eCentro Nacional de Microelectronica, Barcelona, http://www.cnm.es

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 5×10^{15} n_{eq}/cm²) IBL modules has been evaluated in test-beams⁸ at DESY $(4 \text{ GeV positrons})$ and at CERN (120 GeV pions) using the EUDET⁹ telescope to reconstruct the beam trajectories with high resolution as the main external reference system. Cell efficiencies of 98% for both planar and 3D sensors with tracks at a 0[°] incidence, and efficiencies of 97.7% for planar and 99% for 3D sensors with tracks at a 15◦ incidence were observed. The bias voltages needed for full depletion were $V_D = -1000(-160)$ V for planar (3D) sensors. The size of the inactive edge determined from edge pixel efficiencies was found to be $200 \mu m$ wide for both planar and 3D sensors, thus showing that slim-edges have been achieved.

3. Stave Testing

A dedicated testing and Quality Assurance (QA) stand has been setup at CERN for the simultaneous qualification at warm $(10\degree C)$ and cold $(-20\degree C)$ temperatures of two staves in an environmentally controlled box equipped with a $CO₂$ cooling system. The test-stand contains a linear motion device for aligning $90Sr$ and $241Am$ sources above modules, and has scintillators above and below the box for triggering on cosmic events. Each stave is systematically tested to check the modules break-down voltages and I-V curves, signal transmission, FE-I4 power consumption, behavior at high thresholds (3000 e^-) and low threshold (1500 e^-), noise levels, cross-talk, and for low numbers $(< 0.2\%)$ of pixel failures. To ensure the modules work in realistic data taking conditions, both cosmic and radioactive source data runs are taken. Figure 2 shows the stability of the -20◦C threshold (noise) for several tunings for each module on a stave in the left (middle) figure, and the right figure shows the occupancy of 4 modules irradiated by a ^{90}Sr source and showing $< 0.1\%$ bad pixels.

Fig. 2. Left (Middle): Threshold (noise) per module. Right: ⁹⁰Sr occupancy on modules.

4. Construction, Integration, and Installation Status

Construction of the IBL is currently underway. Module production, including bump-bonding of the FE-I4's to the sensors, the gluing of the module flex to the sensor back-side, and wire bonding of the module flex to the FE-I4's, will finish in October 2013, with a total of 168 DC and 112 SC high quality modules needed. High quality modules are loaded onto staves and connected to the stave flex via the flex wings, and subjected to indepth testing. Currently 9 staves have been produced with 6 fully tested and qualified. The first production staves have been of exceptional quality, with stable tunings, low noise levels, and $\lt 0.1\%$ of bad pixels.

The integration of the IBL then proceeds though brazing of the titanium cooling pipe extension to the stave, mounting of the stave around the new beam-pipe using the multi-purpose-container (MPC) loading device, connection of services, and final full retesting on the surface. The stave integration onto the will begin October 2013 and finish by early 2014, to allow for IBL installation into the ATLAS cavern in early 2014.

5. Summary and Outlook

The first upgrade of the ATLAS pixel detector, the Insertable B-Layer (IBL), is a new 4^{th} layer of pixels that will be installed on a new smaller radius beam-pipe during the 2013-14 shutdown of the LHC. The IBL will use slim-edge planar and 3D sensors with a new front-end chip, the FE-I4, to cope with the expected high occupancies and radiation levels. Test-beams show excellent results from both sensor designs, including a 200 μ m active edge. The first IBL staves have shown excellent behavior on a dedicated QA test-stand at CERN. Stave production will finish by the end of 2013, and integration of the staves onto the beam-pipe should finish by early 2014. The IBL will be installed into the ATLAS experiment in early 2014.

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