

Towards Knowledge Acquisition from Natural Language Documents
-- Automatic Model Construction from Hardware Manual --

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ABSTRACT

In this paper, we explore automatic model construction by analyzing natural language documents. The extracted model will be utilized by a CAD system. A system called hmU, in the course of development, is designed to allow knowledge on very complex hardware module like LSI or VLSI to be incorporated into its knowledge base. The acquired knowledge will be utilized for helping human designer understand the component from various levels of abstraction. The focus of this paper is attentioned more to issues on knowledge representation and model inference than that on natural language analysis. Hierarchical model is employed. In particular, cause-effect representation is used to make it clear how actions of each module and events are related to each other. A brief description is given to illustrate our approach.

1. Introduction

One of the difficulties with expert system is knowledge acquisition. The fact that most of human knowledge is integrated as natural language documents has led us to the development of a system which can automatically acquire knowledge from existing documents. In knowledge acquisition from documents, expressive power of natural language, in particular the ability of representing any complex object or situation from various levels of abstraction, should not be sacrificed but be effectively utilized by an expert system for CAD. The essential problem to be solved consists more in model representation and inference problem than in parsing. For example, the resolution of anaphoric expression depends more on domain specific knowledge than on linguistically general knowledge.

A system called hmU (hardware manual Understander) is in the course of development, which will analyze given natural language specification of LSI chips (such as microprocessor and the like), and which

will construct a knowledge structure specifying the behavior of the chip. The acquired model will be utilized by intelligent symbolic simulator to give human designer explanations about the chip. The intelligent symbolic simulator is also under development [Nishida 1983].

hmU's main components are natural language analyzer and model builder. Natural language analyzer utilizes domain specific knowledge to resolve ambiguities, anaphoras, etc. Accordingly, the model builder and the natural language analyzer communicates to each other. The details are described in [Nishida 1982]. The model builder receives internal representation obtained from natural language analysis and builds an automaton-based hardware model structure using common sense knowledge about hardware, time, action, events, etc. In what follows, we will describe the model representation and model building procedure to more detail.

2. A Model for Representing Multiple Agent
Co-Operating Situation

This section describes the hardware model which we used. The well defined hardware domain can be modeled as a world where multiple agents are cooperating each other to attain a common goal. Actions are done in parallel and are synchronized through events. Use of hierarchical representation gives human designer good understanding of hardware. For example, a phrase like "MREQ is asserted" can be given a good explanation, if a statement like "it indicates that address to the memory becomes valid" is accompanied.

Our hardware specification model is hierarchical. Hierarchies are linked together by *indicates* relations. Each hierarchy consists of an *event* model and a set of *action* models. To each hardware module, an action model is defined to specify the behavior of the module. The notion of hierarchy is important in digital circuit design [McDermott 1978, Mitchell 1981, Stefik 1982, Sakai 1982, Sussman 1980]. In the event model, cause-

effect representation is used to correlate actions of each agent along with the time axis. The notion of cause-effect relationship has been advocated to be useful in understanding cooperating actions and events [Rieger 1978]. The action model of an agent represents the actions taken by the agent when a specified input event takes place. This representation is independent of the internal structure of the agent, and makes it possible to give the abstract level description of any complex hardware.

3. Model Inference Procedure

The model builder is given some information from the natural language analyzer and other information from the diagram analyzer (currently pictures like time chart are manually encoded into symbolic expressions), and it attempts to construct a consistent model from inputs. Since the information from the natural language analyzer may be vague or not specified to full details, the model builder has to make inference in the hardware domain. Sometimes model revision may be needed to correct arbitrary choices that are made due to the lack of information. Accordingly, the task has much in common with truth maintenance system [Doyle 1979]. The model builder mainly makes forward inferences using common sense knowledge. In this paper, we concentrate on the descriptions on hardware behavior and assume other parts of the hardware manual such as pin descriptions, have already been analyzed and converted into the knowledge structure. The below illustrates a part of the knowledge:

Action-Event Definition

Example 1. asserts

a asserts s at t (a: agent, s: signal, t: time)

presupposition

a is an agent for s.

P: s is available for s at t.

a believes P at t.

s is not active at t.

definition

<gate level operation is given here>

effect

s becomes active at t+.

Example 2. sends

a sends s to b through x at t

(a, b: agent, x: data transfer device, t: time)

presupposition

a has s at t.

a believes b at t.

a believes x is available for a at t.

...

definition

a puts s on x at t.
a requests b to sample s from x at t+.
effect
b has s at t+.

Module Definition

Example 3. bus

bus : [isa data-transfer-device
with (consists-of address-bus data-bus
***MREQ ...) ...]**

Example 4. signal and line

***MREQ : [isa signal with (asserted-by CPU) ...]**
***MREQ-line : [isa line with (type NEG)**
(indicates *MREQ) ...]

Example 5. schema of a line

line : [isa : data-transfer-device
has-attribute (type NEG POS)
(state HIGH LOW (HZ))
where
if type = POS then
state = HIGH indicates ACTIVE
state = LOW indicates NEGATIVE
else
state = HIGH indicates NEGATIVE
state = LOW indicates ACTIVE]

The model inference procedure involves the following types of reasoning:

- (a) Seeking an action which causes a given event: this task will be done using action-event definitions.
- (b) Seeking values of case slots which were not filled by the natural language analyzer: this task will be done using definitions for individuals.
- (c) Linking descriptions of each hierarchy using *indicates* links.
- (d) Reasoning about cause-effect relations between events: this reasoning utilizes action-event definitions.
- (e) Making vague expressions more accurate: this task is done using axioms for event and action. Example of such axioms can be found in [McDermott 1982].
- (f) Revising a model if needed: each position where arbitrary choice was made is marked. Those positions are candidates for reconstruction when any contradiction takes place.
- (g) Verifying constraint condition.

Fig.1 illustrates how the description: "*MREQ lines goes low at T1" is incorporated into the model. As is seen from the figure, action and event models of each hierarchy are revised so as to be able to give explanation

to the input. Symbols attached to the figure indicates which inference rule is made.

4. Conclusion

Currently, a simplified version of hmU is in the course of development, where the focus is mainly attentioned to rather basic issues, i.e., natural language analysis, discourse analysis, canonical transformation, and reasoning about action, time, and event. However, the initial experiments by hand reveal fundamental validity of our approach. Some of them are illustrated in the appendix.

References

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[McDermott 1982] McDermott,D., A Temporal Logic for Reasoning About Processes and Plans, Cognitive Science 6, 1982, 101-155.

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[Nishida 1983] Nishida,T., Kosaka,A., and Doshita,S., On Action Description Model and its Inference for Hardware Manual Understanding, in Annual Convention Records of IPS Japan, 7C-1, 1983, (in Japanese).

[Rieger 1978] Rieger,C. and Grinsberg,M., A System of Cause-Effect Representation and Simulation for Computer-Aided Design, in Lamtombe (ed.), Artificial Intelligence and Pattern Recognition in Computer Aided Design, North-Holland, 1978, 299-333.

[Sakai1982] Sakai,T., et al., An Interactive Simulation System for Structured Logic Design, in Proc. ACM IEEE 19th Design Automation Conference, 1982, 747-754.

[Stefik 1981] Stefik,M. and Bobrow,D.G., Linked Module Abstraction: A Methodology for Designing the Architectures of Digital Systems, KB-VLSI-81-9,

XEROX PARC, 1981.

[Sussman 1978] Sussman,G.J., SLICES: At the Boundary between Analysis and Synthesis, in Lamtombe (ed.), Artificial Intelligence and Pattern Recognition in Computer Aided Design, North-Holland, 1978, 261-298.

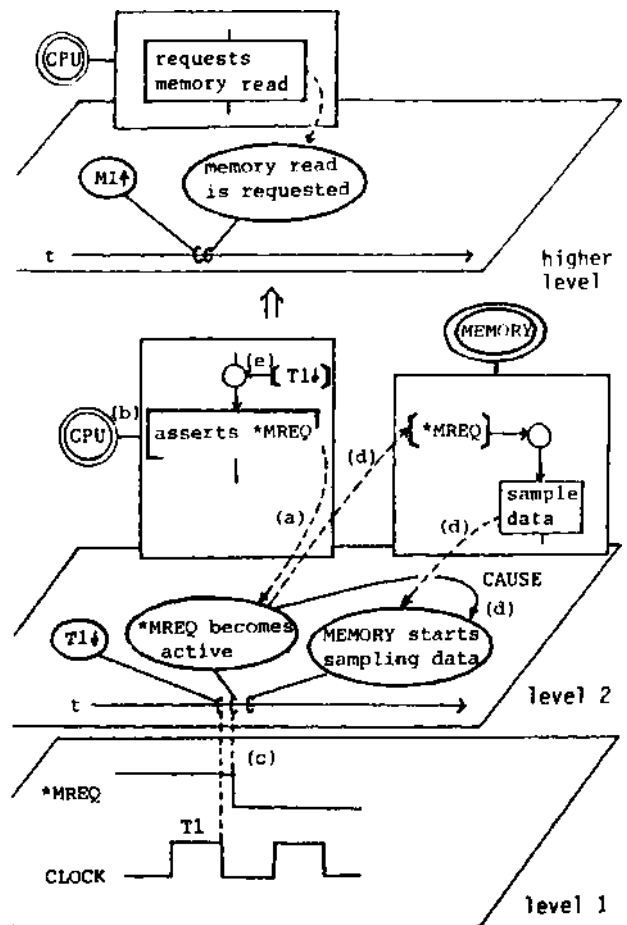


Figure 1. An Example of Model Inference.

Appendix: Overview of the Simplified Version of hmU

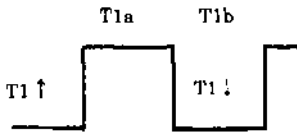
This appendix illustrates an overview of the simplified version of hmU. Usually, input text can be divided into diagrams and natural language text portion. The acquisition task consists of diagram analysis and natural language analysis. After each step is completed, the results are matched together and consistent model will be instantiated.

Diagram Encoding

Diagrams are assumed to be somehow encoded into symbolic expressions. The encoding rule is as follows:

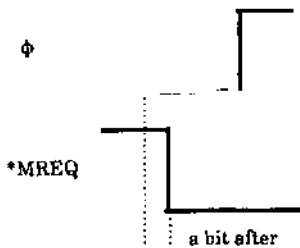
(step 1) labeling clock: generating new symbols to name each clock pulse.

Example 1.



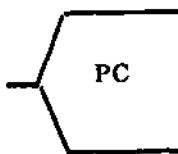
(step 2) assigning symbolic names to events other than clock: This labeling is carried out in terms of Active/Negative instead of High/Low.

Example 2.



Δ afterT1 \downarrow : asserted(*MREQ)

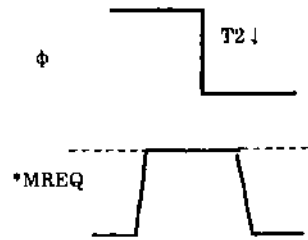
Example 3. If a name is given to the event, it is copied to the symbolic expression.



placed-on(PC)

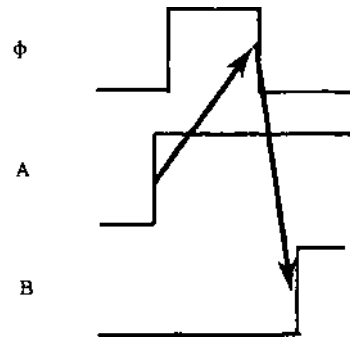
(step 3) special cases:

Example 4. indication of sampling a signal.



at T2 i : sampled(*MREQ)

In time chart arrows are often used to indicate causal-effect relationships. Such information should be effectively utilized during the process of diagram encoding. For example,



state-1: A T : \rightarrow state-2;
state-2: \$i : asserts(B);

Analysis of Diagram

From encoded diagrams, simple model inference rules can be used to extract model structure. Here, automaton description is based on DDL [Duley 1968].

(rule 1) supplementing agents: signal specification is used. For example,

from: AafterT1 / : asserted(*MREQ),

infer: AafterT1 / : asserted(*MREQ) by CPU.

(rule 2) inference of automaton transition arc:

for example,

from: AafterT1 / : asserted(*MREQ),

infer: T1a : \$i : asserts(*MREQ).

Natural Language Analysis

Natural language portion of input text is analyzed sentence by sentence. The result of the phrase structure analysis is translated into intermediate representation. The intermediate representation is designed using the formalism of lexical functional [Kaplan 1982]. Then it is further transformed into canonical representation. During the process, discourse analysis is carried out to solve simple cases of definite noun phrase reference and ellipsis. Intermediate structure is used as a discourse structure.

The below illustrates an intermediate representation and canonical form for a simple sentence:

The CPU turns off the *RD signal. ... (1)

The Intermediate Representation

Category = Sentence
 Subject = Category = NounPhrase
 Determiner = [Lexicon = The, ...]
 MainNoun = [Lexicon = CPU, ...]
 SemanticFunction
 = Evaluate[↑ Determiner]
 Predicate = Category = VerbPhrase
 MainVerb = [Lexicon = Turn, ...]
 SemanticFunction = ↑ MainVerb
 AdverbialParticle = Off
 Object = Category = NounPhrase
 Determiner = The
 MainNoun = [Lexicon = *RD-Signal, ...]
 SemanticFunction
 = Evaluate[↑ Determiner]
 SemanticFunction = Evaluate[↑ Predicate]

Semantic Mapping Function for "turn"

AdverbialPhrase = 'On'
 → Create Unit: {Type ← 'Event',
 Self ← 'Asserts',
 Level ← 'DDL',
 Agent ← ↑ Subject,
 Object ← ↑ Object}

AdverbialPhrase = 'Off'
 → Create Unit: {Type ← 'Event',
 Self ← 'Negates',
 Level ← 'DDL',
 Agent ← ↑ Subject,
 Object ← ↑ Object}.

Canonical Form

Unit:a: Type = Event
 Modality = Description
 Self = Negates
 Level = DDL
 Agent = Unit:b
 Object = Unit:c

Unit:b: Type = Individual
 Isa = CPU

Unit:c: Type = Individual
 Isa = *RD

Comparing Outputs from Natural Language Analyzer and Diagram Analyzer

Roughly speaking, information from diagrams like time charts, tells a lot about the described hardware. So our strategy is first to construct a model based on the information from diagram and then to check it against natural language information. Figure A-1 illustrate this process for a version of sentence (1):

This same edge is used by the CPU to turn off the *RD and *MREQ signal. ... (1)

References of Appendix

[Kaplan 1982] Kaplan, R.M. and Bresnan, J., Lexical-Functional Grammar: A Formal System for Grammatical Representation, in Bresnan (ed.), *The Mental Representation of Grammatical Relations*, The MIT Press, 1982, 173-281.

[Duley 1968] Duley, J.R. and Dietmeyer, D.L., A Digital System Design Language (DDL), *IEEE Trans. Computers*, Vol. C-17, No.9, 1968.

*Examples are cited from: Z80-CPU/Z80A CPU Technical Manual.

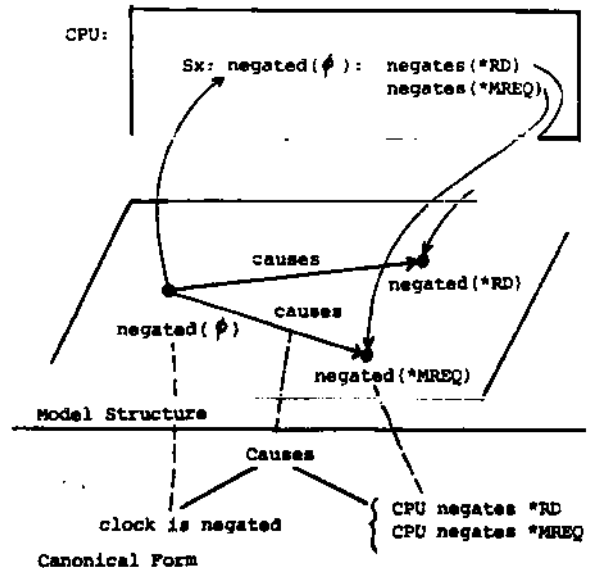


Fig.A-1. Matching input against the Model.

PROLOG IN 10 FIGURES

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Abstract; Prolog is presented in a rigorous way, through 10 easily understandable figures. Its theoretical model is completely rewrought. After introducing infinite trees and inequalities, this paper puts forth the minimal set of concepts necessary to give Prolog an autonomous existence, independent of lengthy considerations about first order logic and inference rules. Mystery is sacrificed in favor of clarity.

Artificial Intelligence interacts with many fields including psychology, linguistics, history, geology, biology, medical science ... These sciences *are* complex, and special tools *are* needed to represent and process the knowledge they deal with. Furthermore, these tools should not introduce new problems, inherent to computer science. Traditionally, the science of knowledge has been mathematical logic. Therefore it was reasonable to turn to logic for help in developing a tool for Artificial Intelligence: that was how Prolog was born.

Prolog, developed in 1972 by A.Colmerauer and P.Roussel, was at first a theorem prover, based on A.Robinson's resolution principle (1965) with strong restrictions to narrow the search space. Credit is given to R.Kowalski and M.van Emden for having pointed out these restrictions as equivalent to the use of clauses having at least one positive literal (Horn clauses), and for having proposed the first theoretical model of what is computed by Prolog: a minimal Herbrand interpretation.

However, Prolog's close links with Logic proved sometimes to be inhibiting vis-a-vis its implementation. It was necessary to reformulate the theory to take into account implementation constraints: this new theory is unencumbered by distinctions necessary only in logic, and is enriched by concepts indispensable for programming purposes (such as inequalities). We can say that, after a careful implementation, a new theoretical model of Prolog emerged and it is this new model that we present here in 10 commented figures.

The reader interested in further readings on this subject is referred to the following:

On automatic theorem proving and logic:

ROBINSON J.A. (1979). "Logic: Form and Function", Edinburgh University Press and Elsevier North Holland.

On the links between logic and Prolog:

KOWALSKI R.A. (1979). "Logic For Problem Solving", Artificial Intelligence series, (Ed- Nilsson, N.J.), North Holland.

On the genesis of Prolog:

COLMERAUER A., KANOUI H., PASERO R. et ROUSSEL Ph. (1973), "Un systeme de communication homme-machin en frangais", Research Report, Groupe Intelligence Artificielle, Faculte des Sciences de Luminy, Marseille.

ROUSSEL Ph. (1975). "Prolog, Manuel de Reference et d'Utilisation, Groupe Intelligence Artificielle, Faculte des Sciences de Luminy, Marseille.

A Prolog system, based on the ideas developed here, and implemented on several computers (Apple II, Vax/Vms, etc.), is described in three Internal Reports of the Groupe Intelligence Artificielle, Faculte des Sciences de Luminy, Marseilles

COLMERAUER A. (1982). "Prolog II, Reference Manual and Theoretical Model".

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KANOUI H. (1982). "Prolog II, Manual of Examples.

1. TREES

From an abstract point of view, one may say that the knowledge of an intelligent being on a given subject, is the set of facts that he or she can generate on the subject. Therefore, knowledge can be viewed as a set of facts, specified by a set of rules. Each of these facts can be represented by a declarative sentence. In our case we represent a fact by a tree, drawn upside down, as the one shown in Fig 1a. Each leaf and each node is labeled with an "atom" of information: this atom can be a word, a group of words, a number, or a special character. Only the structure of the tree is relevant. Therefore, Figs 1a and 1a' are equivalent. Trees in Figs 1a, 1b and 1c are examples of facts in three different fields: arithmetic, (stylistic) permutations, and meal planning. Facts *are* always trees, but not all trees *are* facts: obviously the trees in Figs 1d and 1e *are* not facts in arithmetic, even if *tree* in Fig 1d is a sub-tree of the fact in Fig 1a.

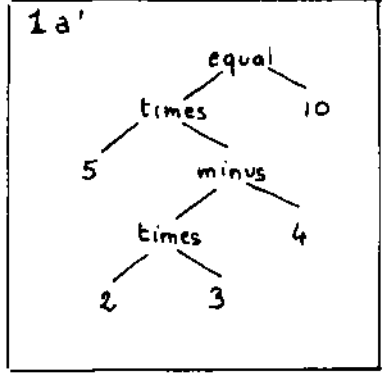
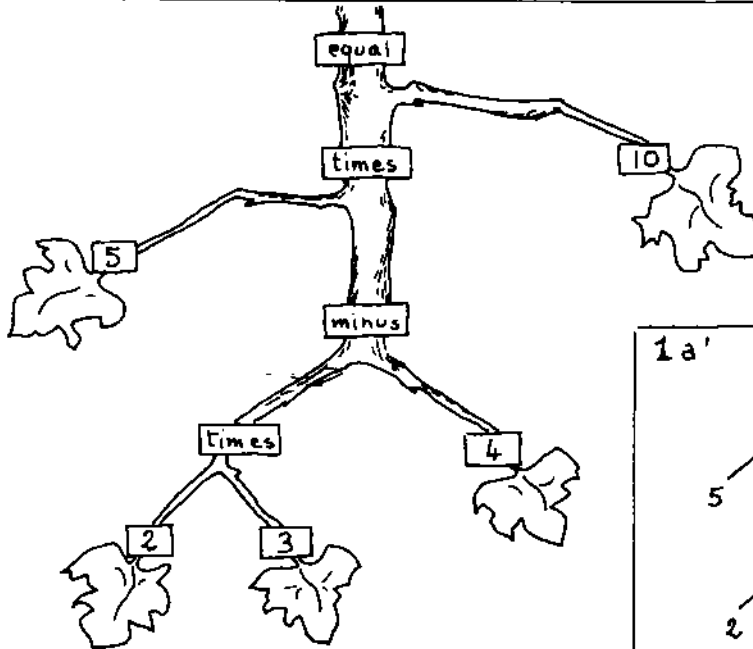
Trees were purposely chosen as data structures: they *are* capable of expressing complex information and, at the same time, simple enough to be handled algebraically, and by a computer.

2. TERMS

Formulas *are* used to represent tree patterns. These formulas called "terms", consist of atoms of information, variables, parentheses and commas. Recall that an atom of information is either a group of words, a number, or a special character. In the left column of Fig 2a the syntactic structure of a term is defined; this is a recursive definition where complex terms are defined from simpler terms; the simplest terms are variables or atoms of information. Examples of terms can be found in the left part of Figs 2b and 2c.

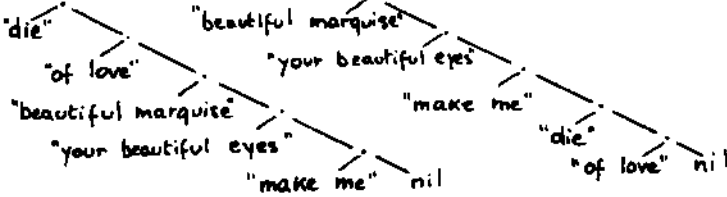
Je remercie Jacques Cohen de m'avoir aide a rediger cet article en anglais.

1a



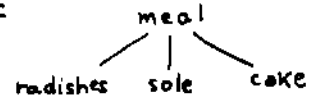
5 times the difference between 2 times 3 and 4 equals 10
 that is to say: $5 \times (2 \times 3 - 4) = 10$

1b is-permutation-of



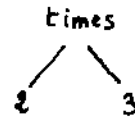
The sequence: "die", "of love", "beautiful marquise", "your beautiful eyes", "make me", is a permutation of the sequence: "beautiful marquise", "your beautiful eyes", "make me", "die", "of love".

1c

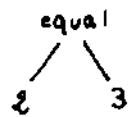


The trio "radishes, sole, cake" constitutes a meal

1d

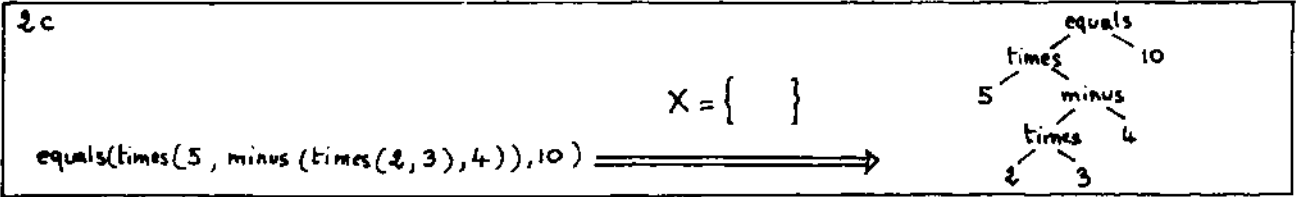
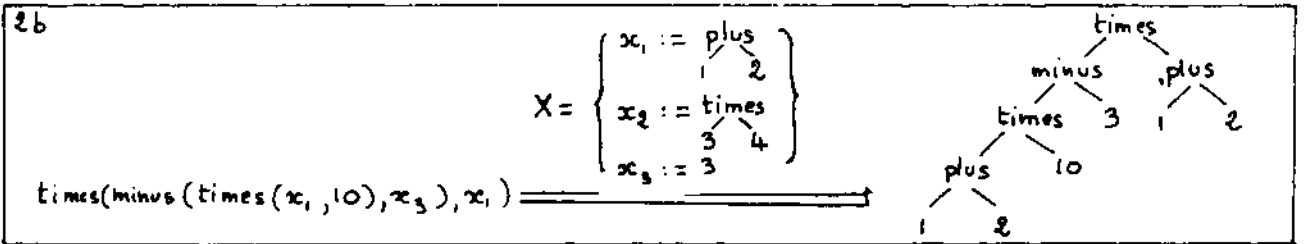
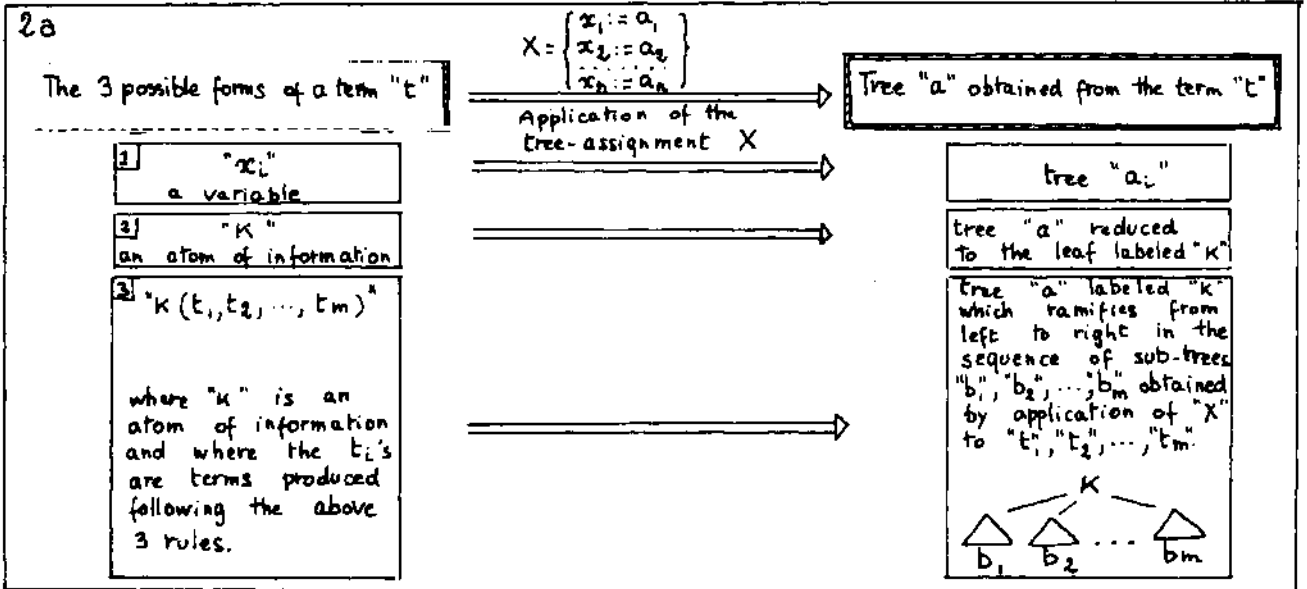


1e



Variables occurring in terms represent unknown trees. Therefore, the tree expressed by a term will depend upon the trees assigned to the variables. Such an assignment "X", called a "tree-assignment", is just a set of pairs "xi:=ai", "ai" being the tree assigned to the variable "xi". The right column of Fig 2a gives the tree "a" represented by the term "t" after the application of tree-assignment "X". It is assumed that if "t" contains no variable, an empty tree-assignment can be applied.

Figs 2b and 2c depict two examples of tree-assignments. Example 2b shows that it is possible to find in the assignment "X", variables which do not occur in the term, but the contrary is not possible. In example 2c, the term contains no variable; this means that the corresponding tree does not depend on the assignment. The last example shows a systematic way of coding a finite tree by a term without variables.



3. CONSTRAINTS

Prolog is a language which "computes" on trees "aj" represented by variables "xi". This computation is done by accumulating constraints that final trees must satisfy. These constraints limit the values variables can take, that is the tree-assignment of variables "xi" by trees "ai". As shown in Fig 3a, a constraint ^MC consists of a set of elementary constraints, each of them to be satisfied. An elementary constraint is either a

pair of terms "<SJ,SJ'>" which will represent equal trees, or a pair of terms "<t,t'<'>" which will represent unequal trees. Fig 3a illustrates the general condition under which a tree-assignment "X" satisfies a constraint "C". "X" is also said to be a solution of "C". Fig 3b shows an example of a constraint "C1" satisfiable by the tree-assignment "X1". In Fig 3c there are three constraints which cannot be satisfied by any tree-assignment.

During the execution of a Prolog program, the basic operation consists of verifying whether a constraint is "satisfiable" or not (by at least one tree-assignment). This is done by "reducing" it, as seen in Fig 3d: the purpose of "reducing" is to simplify the constraint in order to make all its solutions explicit. This involves exhibiting variables distinct from each other as left members of equalities. To do so, we use a specific property of trees: the unique decomposition of a tree into immediate subtrees. This property permits us to replace:

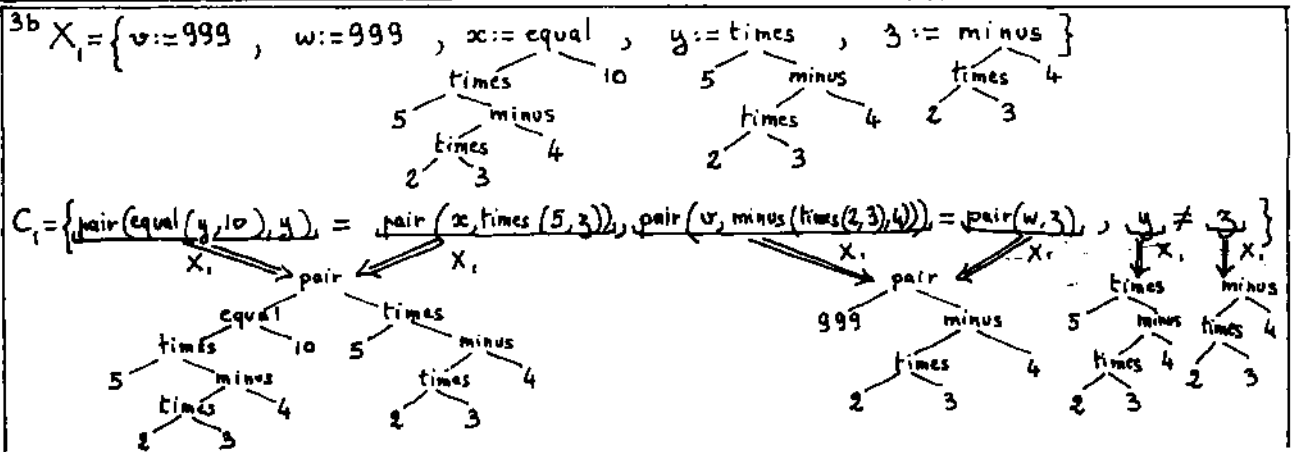
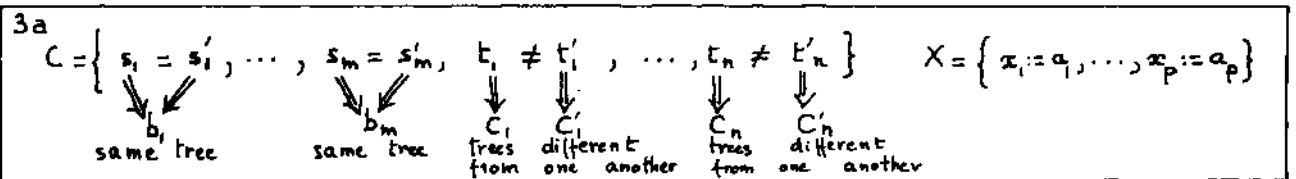
$$\begin{aligned} & \text{(pair(equal(y,10),y)=pair(x,times(5,z)))} \\ & \quad \text{by} \\ & \text{(equal(y,10)=x, y=times(5,z)).} \end{aligned}$$

Note that if this property would hold for numbers, we would wrongly conclude that the two constraints "(x+3=2+y)" and "(x=2,3=y)" are equivalent! If we succeed in producing equalities where left members are distinct variables and where there are no inequalities, then the constraint is satisfiable. Its solutions are directly obtained by assigning arbitrary trees to variables not appearing as left members.

If inequalities are left, let "n" be their number.

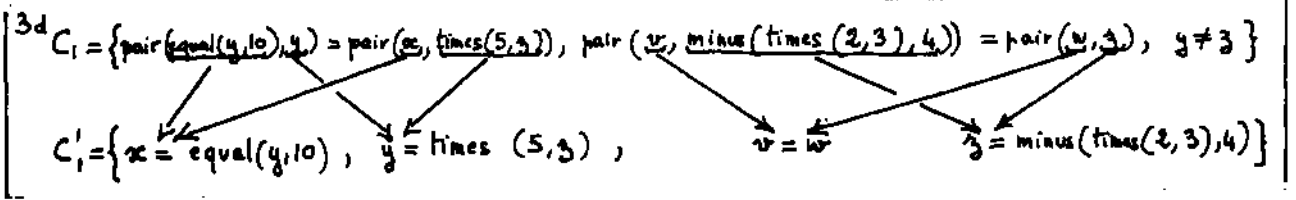
Another basic property allows us to split the initial problem into "n" independent and simpler sub-problems: a constraint of the form "Cu(t₁#t₁'...,t_n#t_n')" is satisfiable if and only if each of the constraints "Cu(t₁#t₁')", ..., "Cu(t_n#t_n')" is also satisfiable. Again, this is not true in the domain of natural integers "0,1,2,...", because it would be possible to show that the constraint "(x+y=2,x#0,x#1,x#2)" has at least one solution since the constraints "(x+y=1,x#0)", "(x+y=1,x#1)" and "(x+y=1,x#2)" have at least one! In order to verify that the constraint "Cu(t₁#t₁')" is satisfiable (knowing that "C" already is) we must check that the constraints "C" and "Cu(t₁=t₁')" are not equivalent. If the constraint "Cu(t₁=t₁')" is not satisfiable, we can even remove the inequality "t₁#t₁'", as in example 3d.

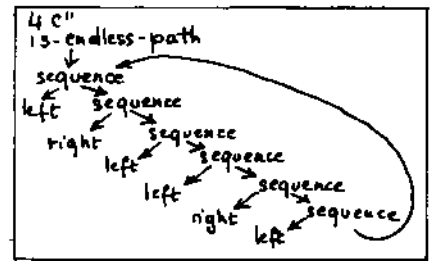
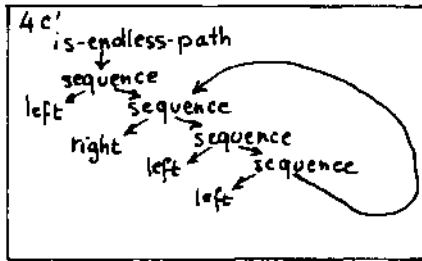
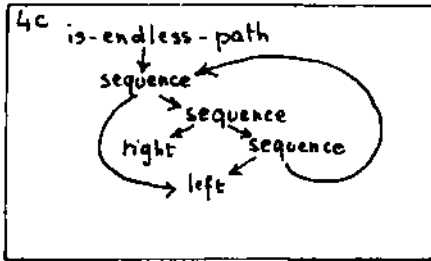
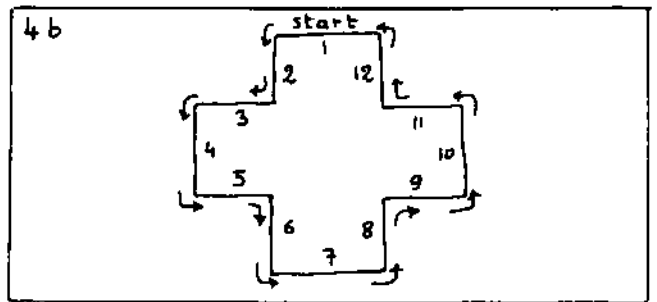
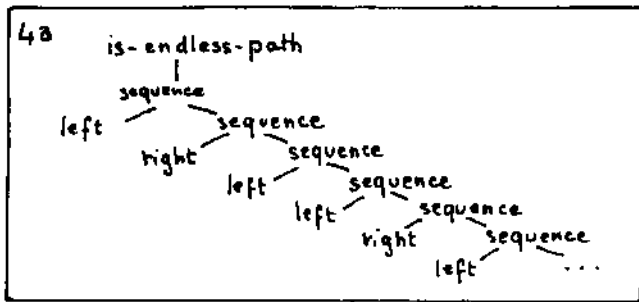
In the same way as we simplify equalities, it is possible to simplify inequalities. This allows us to present any satisfiable constraint in a "reduced form": this reduced form shows that the constraint is satisfiable by making all its solutions explicit. The general form of a reduced constraint containing inequalities is beyond the scope of this paper (see Colmerauer 1982).



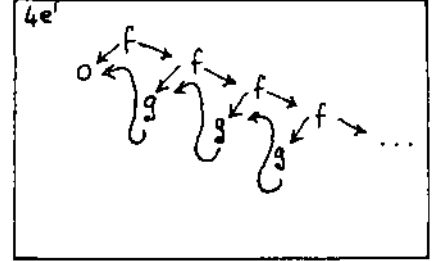
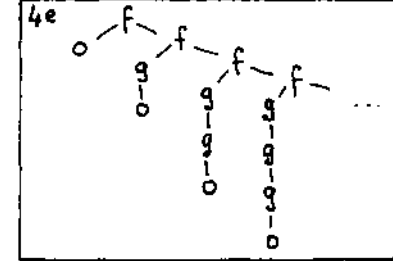
3c { equal(y,10) = times(2,3) } { times(5,3) != times(5,3) } { times(x,y) = times(y,x), minus(x,y) != minus(y,x) }

insatisfiable insatisfiable satisfiable





4d

$$\left\{ \begin{array}{l} x = \text{is-endless-path}(y_1) \\ y_1 = \text{sequence}(\text{left}, y_2) \\ y_2 = \text{sequence}(\text{right}, y_3) \\ \dots \\ y_n = \text{sequence}(\text{right}, y_{n-2}) \\ y_{n-1} = \text{sequence}(\text{left}, y_n) \end{array} \right\}$$


4d' { x = is-endless-path(y), y = sequence(left, sequence(right, sequence(left, y))) }

4. INFINITE TREES

As surprising as it may be, it is also possible to handle in-finite trees. Such a tree is shown in Fig 4a: it represents an endless path along the cross-like figure shown in Fig 4b. It is possible to present this tree by the diagram with a loop in 4c, obtained by merging all the nodes from which isomorphic subtrees arise, that is, from which equal subtrees arise. If we omit to merge a few nodes, we obtain the different diagrams in 4c' and 4c'' which still represent the same tree. That Fig 4c is a finite diagram means that the initial tree in 4a contains a finite set of configurations or, more precisely, that the set of its subtrees is finite: this is the definition of a "rational" tree. Of course, all finite trees are rational. Although finite trees can be defined by simple terms without variables, infinite rational trees can only be defined by the constraints they must satisfy. Taking into account successively all sides "1,2,...,12" of the cross-figure in 4b, we construct the constraint 4d which is satisfied only in case of the assignment of "x" by the tree in Fig 4a. From the diagram shown in Fig 4c, we can construct a simpler constraint 4d', having the same property.

For the curious reader we provide in Fig 4e an example of a non-rational infinite tree. After

merging all possible nodes this tree yields the infinite diagram in Fig 4e'. Note that it would be necessary to have a constraint, made from an infinity of elementary constraints, to completely describe this type of tree.

5. A PROLOG PROGRAM: LET'S EAT WELL

We now interrupt our theoretical development to present an example of a Prolog program. The program computes the composition of "light" meals and consists of the three parts shown in Figs 5a, 5b and 5c.

In Fig 5a we describe, by 11 rules, a possible set of meals, regardless of their dietetic qualities. The first rule states that:

- if "a" is an appetizer and,
 - if "m" is a main course and,
 - if "d" is a dessert,
- then the triplet "a,m,d" is a meal.

The next two rules state that:

- if "m" is a fish, "m" is a main course and,
- if "m" is a meat, "m" is a main course.

The remaining eight rules classify a few courses. In Fig 5a', the computer answers two questions based on the knowledge described in Fig 5a. The

first question is:

what are the values of "m",
that make "m" a main course?

There are several possible answers and each answer
is given as a reduced constraint on "m". The
second question is:

what are the triplets "a,m,d"
which constitute a meal?

The corresponding answers are also presented in
Fig 5a'.

In Fig 5b we introduce a minimal knowledge of
arithmetic of positive integers: the addition
" $x+y=z$ " with " $z < 10$ ", denoted by
"small-sum(x,y,z)". The fact that " $x+y=z$ " implies
" $(x+1)+y=(z+1)$ " is used to define the notion
"small-sum" from the notion "small-successor",
utilizing two rules. The notion "small-successor"
is defined by eight rules so that
"small-successor(x,y)" corresponds to the equality
" $y=x+1$ " with " $y < 10$ ". Fig 5b' presents the

computer's answers to a few questions about
arithmetic. Observe that, according to the
formulated questions, the same small Prolog
program of Fig 5b also computes the sum, the
difference, or decomposes a number in all possible
sums of two numbers.

Fig 5c defines a light meal (based on Figs 5a
and 5b) by assigning a certain amount of caloric
units to each course, and restricting to meals
which add up to a number of units smaller than
10. The main rule of Fig 5c states that:

```
- if the triplet "a,m,d" is a meal and,  
- if the number of units of "a" is "x" and,  
- if the number of units of "m" is "y" and,  
- if "x+y=u" and "u < 10" and,  
- if the number of units of "d" is "z" and,  
- if "z+u=v" and "v < 10",  
then the triplet "a,m,d" is a light meal.
```

In Fig 5c', the computer lists the seven allowed
meals!

```
5a meal(a,m,d) ->
    appetizer(a)
    main(m)
    dessert(d);

main(m) -> fish(m);
main(m) -> meat(m);

appetizer(radishes) ->;
appetizer(pate) ->;

fish(sole) ->;
fish(tuna) ->;

meat(porc) ->;
meat(beef) ->;

dessert(cake) ->;
dessert(fruit) ->;
```

```
5b little-sum(1,x,y) ->
    little-successor(x,y);
little-sum(x',y,z') ->
    little-successor(x,x')
    little-sum(x,y,z)
    little-successor(z,z');

little-successor(1,2) ->;
little-successor(2,3) ->;
little-successor(3,4) ->;
little-successor(4,5) ->;
little-successor(5,6) ->;
little-successor(6,7) ->;
little-successor(7,8) ->;
little-successor(8,9) ->;
```

```
5c light-meal(a,m,d) ->
    meal(a,m,d)
    units(a,x)
    units(m,y)
    little-sum(x,y,u)
    units(d,z)
    little-sum(z,u,v);

units(beef,3) ->;
units(fruit,1) ->;
units(cake,5) ->;
units(pate,6) ->;
units(porc,7) ->;
units(radishes,1) ->;
units(sole,2) ->;
units(tuna,4) ->;
```

```
5a' main(m)?
(m=sole)
(m=tuna)
(m=porc)
(m=beef)

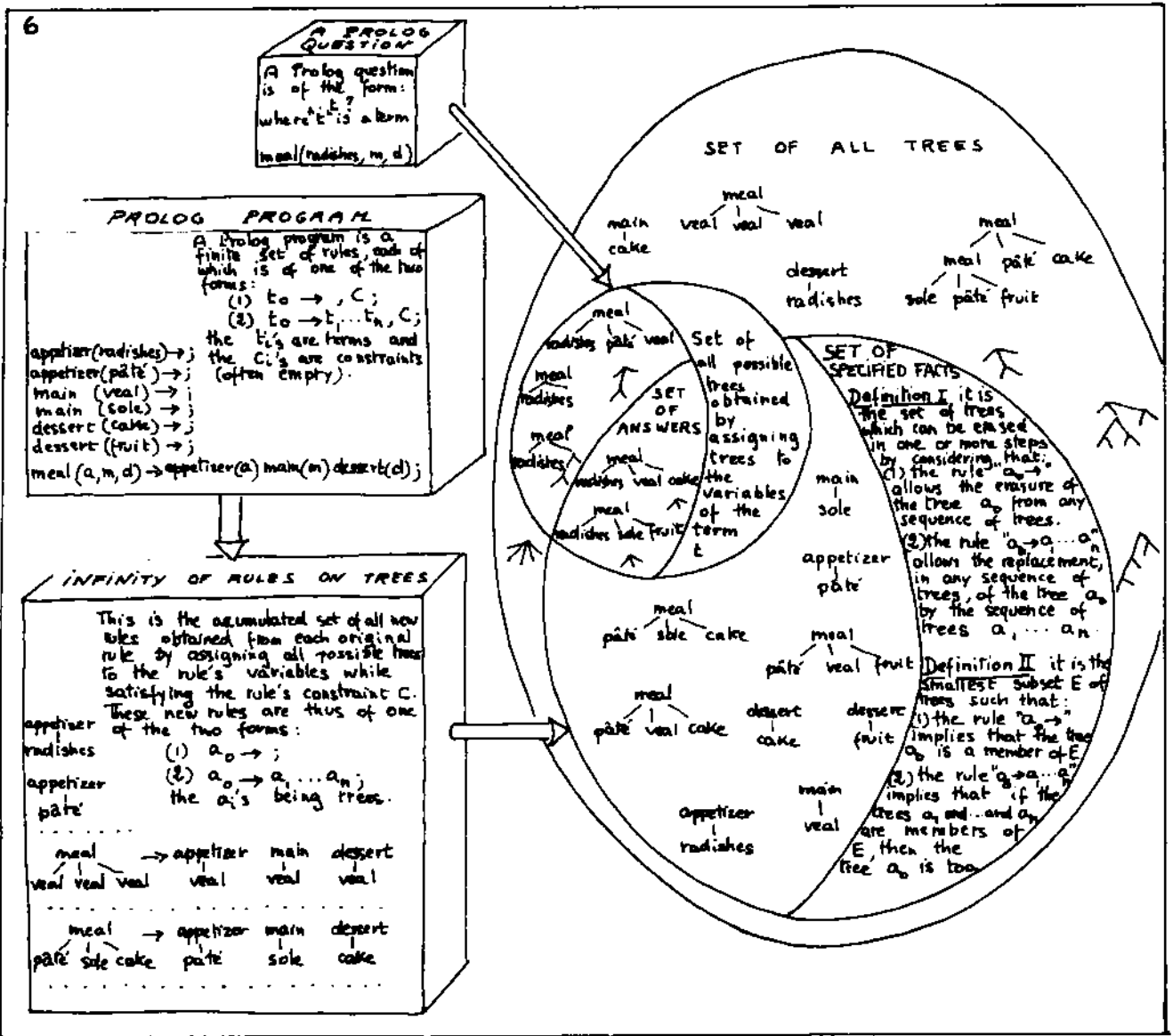
meal(a,m,d)?
(a=radishes, m=sole, d=cake)
(a=radishes, m=sole, d=fruit)
(a=radishes, m=tuna, d=cake)
(a=radishes, m=tuna, d=fruit)
(a=radishes, m=porc, d=cake)
(a=radishes, m=porc, d=fruit)
(a=radishes, m=beef, d=cake)
(a=radishes, m=beef, d=fruit)
(a=pate, m=sole, d=cake)
(a=pate, m=sole, d=fruit)
(a=pate, m=tuna, d=cake)
(a=pate, m=tuna, d=fruit)
(a=pate, m=porc, d=cake)
(a=pate, m=porc, d=fruit)
(a=pate, m=beef, d=cake)
(a=pate, m=beef, d=fruit)
```

```
5b' little-sum(4,3,x)?
(x=7)

little-sum(4,x,7)?
(x=3)

little-sum(x,y,5)?
(x=1, y=4)
(x=2, y=3)
(x=3, y=2)
(x=4, y=1)
```

```
5c' light-meal(a,m,d)?
(a=radishes, m=sole, d=cake)
(a=radishes, m=sole, d=fruit)
(a=radishes, m=tuna, d=fruit)
(a=radishes, m=porc, d=fruit)
(a=radishes, m=beef, d=cake)
(a=radishes, m=beef, d=fruit)
(a=pate, m=sole, d=fruit)
```



6. FORMAL MEANING OF PROLOG PROGRAMS

Fig 5 informally described a Prolog program. We now formalize its meaning. For most languages, the meaning of a program is given by the succession of elementary operations which the computer is supposed to perform. This is not true of Prolog which, as presented, is a formalism capable of representing knowledge and to express questions about it, independently of any computer. The computer's simply computes the answers to these questions.

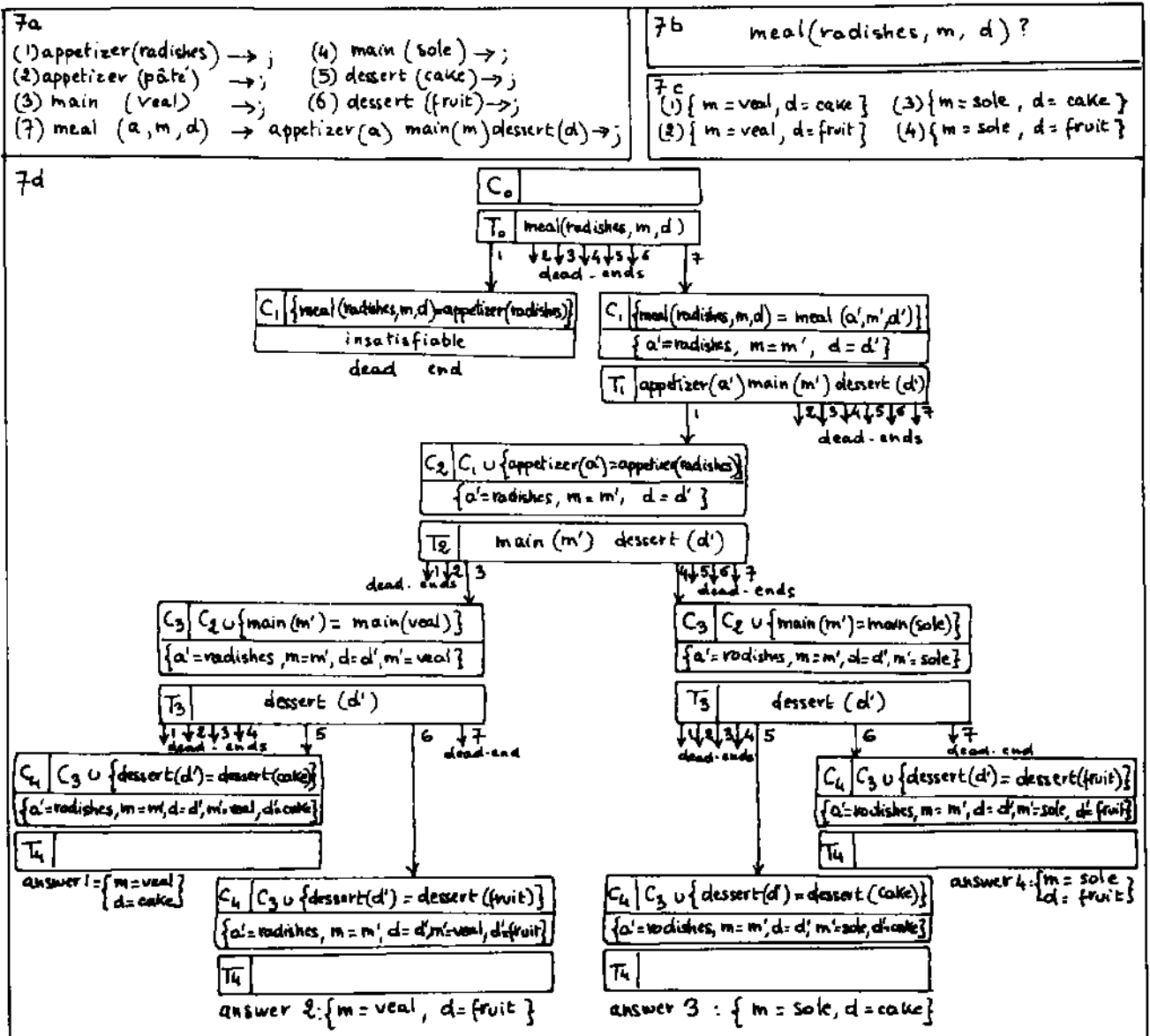
In Fig 6 we derive in two steps the set of facts (a circle) specified by a Prolog program from the original program (a block). This set represents the potential knowledge contained in the program. Each of the facts is a tree, taken from all

possible trees. The first step is required since the rules of a Prolog program are, actually, pattern of rules, and since it is first necessary to generate precise rules dealing with trees. The second step can be performed in two ways: either by considering the rules as rewriting rules (definition I), or by considering them as logical implications (definition II).

Fig 6 also characterizes the set of facts which yield the answer to a Prolog question. A question is a single term "t" which stating:

what are the facts of the form "t"?

The set of valid answers is the intersection of the set of specified facts with the sub-set of trees, obtained by assigning all conceivable trees to the variables of term "t".



7. THE SEARCH SPACE

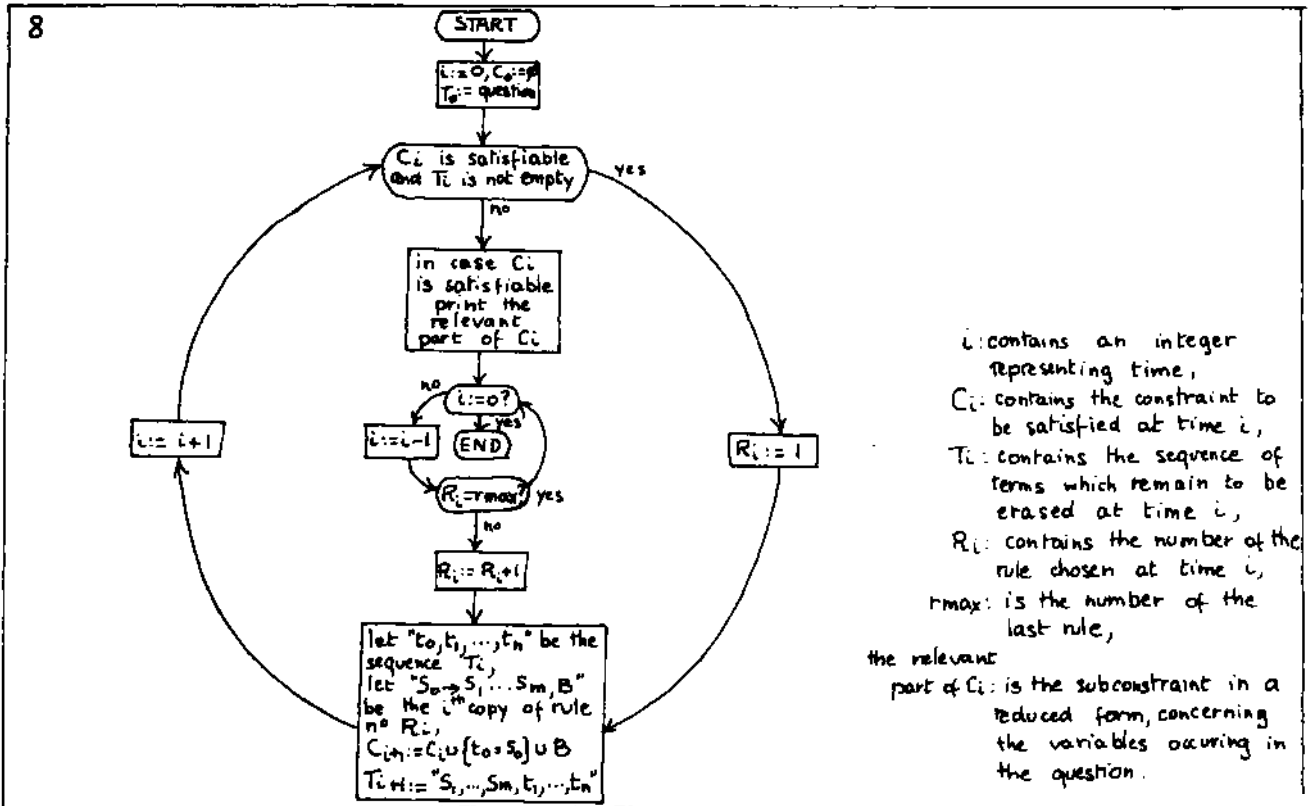
Fig 6 illustrated the double definition of the meaning of a Prolog program. Although both definitions are conceptually satisfying, they cannot be directly used to compute the answer to a given question.

However, this computation can be performed on the light of definition 1 by rewriting trees patterns instead of trees, with the use of a finite set of rules patterns instead of an infinite set of rules. A tree pattern is a "term-constraint" pair, the constraint limiting the represented trees; a rule pattern is, in fact, just a Prolog rule.

In Fig 7b the question on program 7a, states: under which constraints does "meal(radishes,m,d)" represent only facts? To compute these constraints the computer inspects the tree-shaped search space

common with already existing variables. The constraints " C_i " which are satisfiable are those which can be reduced; in this case, we place their reduced forms just below them. The answers are

subparts of the reduced constraints " C_i 's" appearing in nodes which cannot longer be expanded, since their " T_i 's" are empty. The four valid answers are found in Fig 7c.



8. THE PROLOG CLOCK

The best way of explaining in detail how a Prolog program runs on a computer is to idealize this computer by a simple abstract machine. We call our abstract machine "the Prolog clock" because its basic function is to keep track of the time. This machine consists of:

1. a cell " i ", containing a non negative integer representing the time;
2. an infinity of cells " C_0, C_1, C_2, \dots " containing the constraints to be satisfied at times $0, 1, 2, \dots$;
3. an infinity of cells " T_0, T_1, T_2, \dots " containing the sequences of terms which, at times $0, 1, 2, \dots$, remain to be erased;
4. an infinity of cells " R_0, R_1, R_2, \dots " containing the numbers of the rules which have been chosen at times $0, 1, 2, \dots$.

The rules are numbered "1" to " r_{max} " and the machine has means of accessing them.

The machine's operation is depicted by two concentric circles in Fig 8: one of them is swept clockwise as time increases by one unit, the other is swept in the opposite direction as time

decreases by one unit. It is possible to reverse the time progression by crossing one of the two one-way bridges which link one circle to the other.

The execution of a Prolog program consists of answering a question represented by a term. All answers to be computed are constraints by which the term represents specified facts. We start with the pair " C_0, T_0 ", " C_0 " being empty and the sequence of terms " T_0 " being reduced to the term that constitutes the question. Each turn around the outward circle increases the current constraint " C_i " and transforms the sequence " T_i ". Note that if the rule already contains a constraint " B ", this constraint is added to the current set of elementary constraints. The process stops as soon as a non-satisfiable constraint is generated, or the sequence " T_i " becomes empty: in these cases, we backtrack to the "past", to try other rules. On the fly, if " C_i " is satisfiable, an answer is printed.

In fact, the above process corresponds to sweeping the tree-shaped search space of Fig 7, from top to bottom and from left to right, the time " i " being the level of the visited node.

The two programs in sections 9 and 10 provide additional examples of more intricate Prolog programs.

insert "e" before "x" (third rule of Fig 9c), or we insert "e" in the sequence which has its -first element removed (-fourth rule of Fig 9c). These four rules of Fig 9c constitute the entire permutation program.

Fig 9d presents the computer's answers to two questions:

what *are* all permutations "x" of the sequence "1,2,3"?

and

what *are* the values of the variables "a", "b", "c" and "d" so that "2,4,c,d" is a permutation of "3,a,1,b"?

Finally in Fig 9e, we ask the question producing the 120 stylistic variants that the "bourgeois gentilhomme" might have said'

10. SEND MORE MONEY

The purpose of this example is to solve a classical cryptarithmic puzzle: assign 8 different digits to the 8 letters "S,E,N,D,M,O,R,Y", such that the sum "SEND+MORE=MONEY" becomes valid. To do so, we introduce in Fig 10a⁷ the four carry-overs "r1", "r2", "r3" and "r4" which can be null and which

have to be added to each column of the sum.

The program consists of the three parts shown in Figs 10b, 10c and 10d. In Fig 10d, the table of sums up to 20 is programmed: any elementary school student knows this table by heart but the machine has to compute it over and over again since it only knows how to add "1" to a number. We use "plus(x,y,z)" to mean "x+y=z". Each number, is represented by two digits, with a dot between them (we use infix notation as in Fig 9). Fig 10c presents the definition of a sequence without repetition (note that the last rule of Fig 10c contains a non-empty constraint). In Fig 10b, it is stated that to compute a solution it is necessary to assign distinct values to the letters "S,E,N,D,M,O,R,Y", and that, in each column of the sum, a property called "admissible", has to be satisfied between the carry-over, the three letters of the column and the preceding carry-over. Of course, this property "admissible" is defined using the property "plus" and the property "plus-one". Since the numbers "SEND", "MORE", and "MONEY" should not begin with the digit 0, an inequality constraint is added to the first rule of Fig 10b. In Fig 10e, we challenge the computer to provide us with the three mystery numbers.

10 a

```

SEND
+ MORE
-----
MONEY

```

10 a'

```

r: 1254
SEND
+ MORE
-----
MONEY

```

10 d

```

plus(0.0,x,x) ->
  less-than-twenty(x);
plus(x',y,z') ->
  plus-one(x,x')
  plus(x,y,z)
  plus-one(z,z');

```

```

less-than-twenty(0.0) ->;
less-than-twenty(y) ->
  plus-one(x,y);

```

```

plus-one(0.0,0.1) ->;
plus-one(0.1,0.2) ->;
plus-one(0.2,0.3) ->;
plus-one(0.3,0.4) ->;
plus-one(0.4,0.5) ->;
plus-one(0.5,0.6) ->;
plus-one(0.6,0.7) ->;
plus-one(0.7,0.8) ->;
plus-one(0.8,0.9) ->;
plus-one(0.9,1.0) ->;
plus-one(1.0,1.1) ->;
plus-one(1.1,1.2) ->;
plus-one(1.2,1.3) ->;
plus-one(1.3,1.4) ->;
plus-one(1.4,1.5) ->;
plus-one(1.5,1.6) ->;
plus-one(1.6,1.7) ->;
plus-one(1.7,1.8) ->;
plus-one(1.8,1.9) ->;

```

10 b

```

solution(S.E.N.D,M.D.R.E,M.O.N.E.Y) ->
  without-repetition(S.E.N.D.M.O.R.Y.nil)
  admissible(r1,0,0,M,0)
  admissible(r2,S,M,0,r1)
  admissible(r3,E,0,N,r2)
  admissible(r4,N,R,E,r3)
  admissible(0,D,E,Y,r4),
  (S=0, M=0);

```

```

admissible(0,u1,u2,u3,r) ->
  plus(0.u1,0.u2,r.u3);
admissible(1,u1,u2,u3,r) ->
  plus(0.u1,0.u2,x)
  plus-one(x,r.u3);

```

10 c

```

without-repetition(nil) ->;
without-repetition(u.1) ->
  out-of(u,1)
  without-repetition(1);

```

```

out-of(u,nil) ->;
out-of(u,v.1) ->
  out-of(u,1),
  (u=v);

```

10 e

solution(x,y,z)?	9567
(x=9.5.6.7, y=1.0.8.5, z=1.0.6.5.2)	+ 1085

	10652