# Real-time monitoring of the LHCb interaction region with FPGA-based hit reconstruction

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# Real-time processing on FPGA

Reconstructing relevant physical proxies, such as particle hits and tracks, at the earliest stages of the DAQ allows to speed up the reconstruction stages. **FPGAs** are the best suited architecture for these low-level reconstruction tasks.

The availability of high-quality primitives at the readout level also creates an opportunity to perform measurements in real time.

# The LHCb VELO detector

#### Online luminosity measurement

The instantaneous luminosity is defined as:

$$\mathcal{L}_{\text{inst}} = N_{\text{bb}} \frac{\mu_{\text{vis}}}{\sigma_{\text{vis}}} f_{\text{LHC}}$$

- $\mu_{vis}$  = number of visible interactions = (hits per event)
- $N_{\rm bb}$  = number of colliding bunches •  $f_{LHC} = 11.245 \, \text{kHz}$ 
  - $\sigma_{vis}$  = visible cross section specific to each counter, calibrated via van der Meer scans<sup>1;5</sup>
    - $\int \frac{\mu(\Delta x, \Delta y)}{d(\Delta x)} d(\Delta y)$

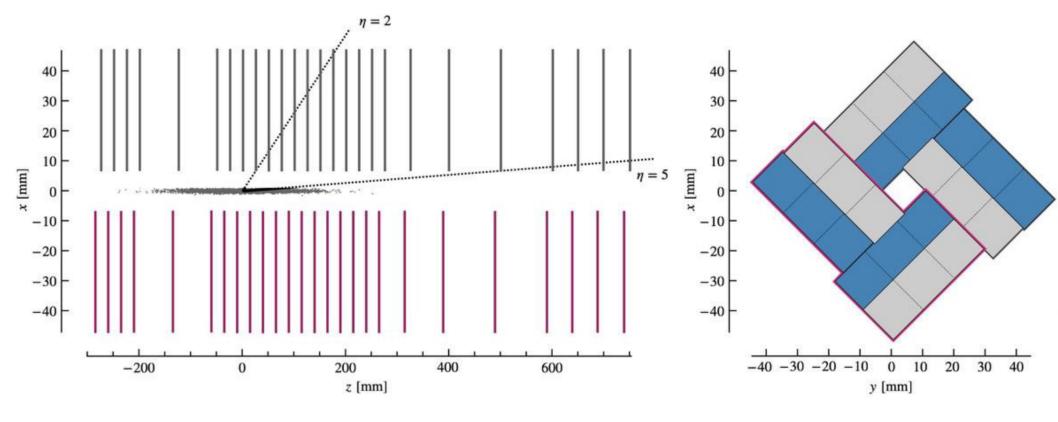




NORMALE

**SUPERIORE** 

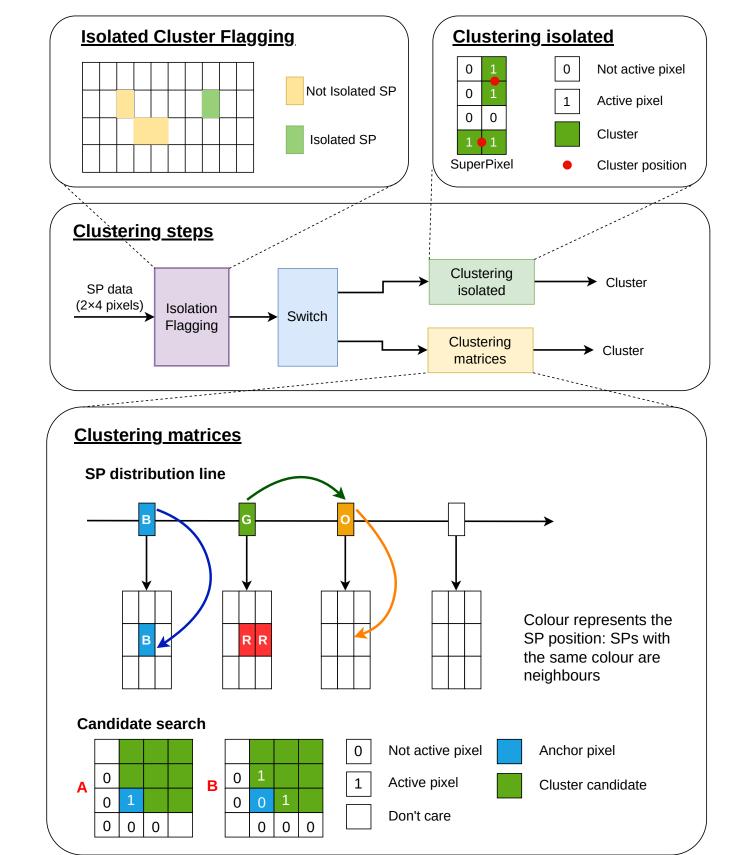
The LHCb VErtex LOcator (VELO) measures Primary Vertices (PVs) with resolution of  $O(10 \,\mu \text{m})$ . It is composed of two retractable halves with 26 layers of silicon pixel sensors each (41M in total). It is positioned at 2.5 mm from the beam in nominal conditions



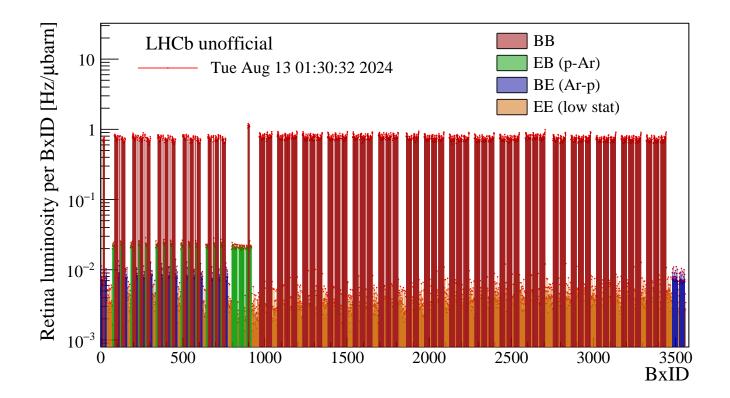
## Cluster-finding algorithm on FPGA

The VELO is the first LHC detector to implement **clustering**<sup>2;3</sup> at **30 MHz** directly on the FPGAs of the readout boards.

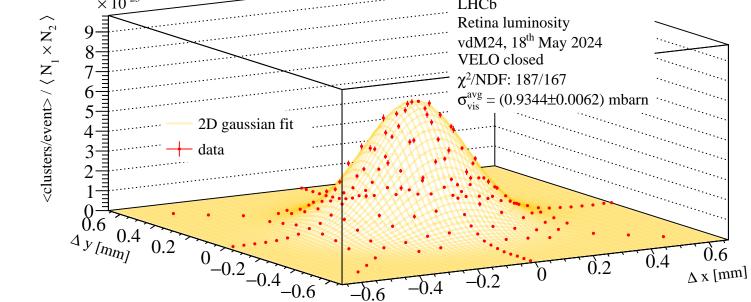
improves the software trigger



 $= -\ln(\text{Prob}(\text{empty event}))$ 



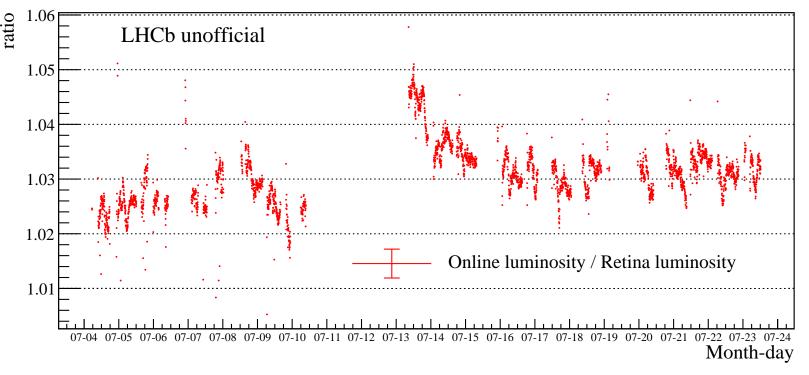
 $N_1N_2$ LHCb Retina luminosity vdM24, 18<sup>th</sup> May 2024



Good stability:

or

- O(%) in the same fill
- O(%) in between fills. The main deviations are due to beam spot shifts along z.



The hit-based luminosity estimator is now **used as backup** of the LHCb luminometer for **luminosity-levelling** feedback to LHC.

## Track-less beam spot position monitoring

The position of the luminous

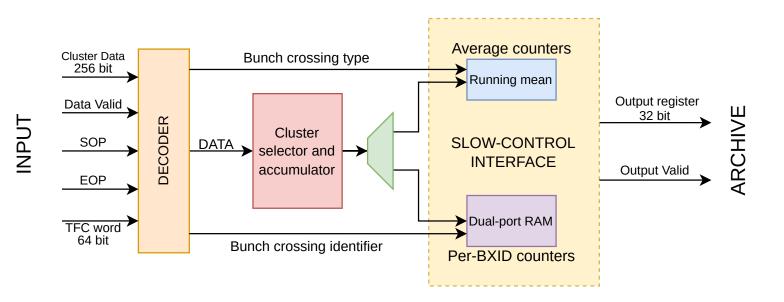
- throughput by  $\geq 11\%$
- reduces the VELO readout bandwidth by  $\sim 30\%$
- requires  $O(50 \times)$  less power than the HLT (GPU-based) clustering

Clustering on FPGA results in the same tracking efficiency of the software-based reconstruction

#### Hit counters as interaction region monitors

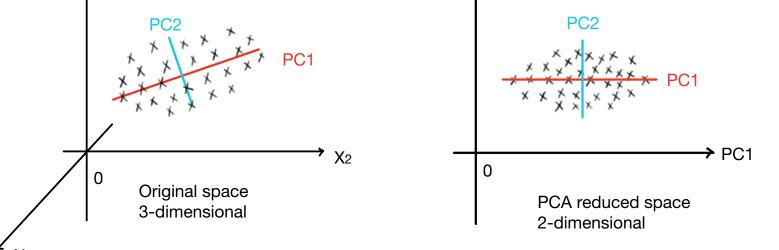
The occupancy on the VELO sensors depends on: 1. the number of collisions *i.e.* luminosity 2. beam spot spatial parameters. Counting the number of hits provides a **powerful tool** to perform a **real-time** diagnostic of the luminous region, without the need for tracking.

We implemented a set of programmable hit counters on each readout FPGA board of the



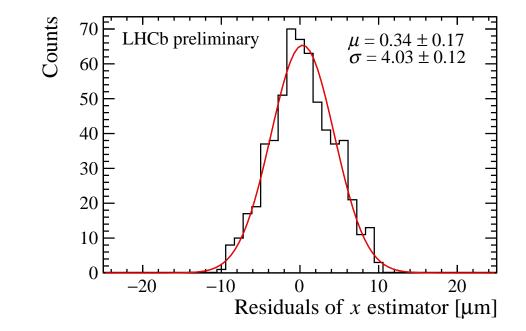
region is determined<sup>4;6</sup> with a linear estimator:

$$\mathbf{x}_i = \alpha_i \vec{\mathbf{c}} \cdot \vec{\mathbf{w}}_i + \beta_i$$

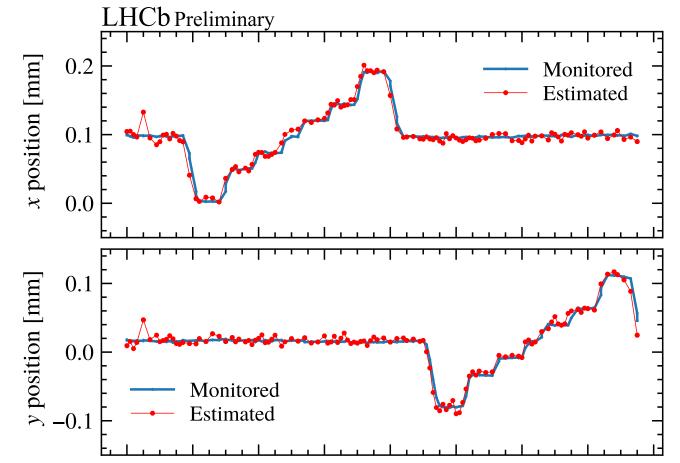


- $\vec{c}$  is the vector of counter rates normalised by the pile-up  $\mu$ •  $\vec{w}_i$  are weights calculated in MC using the PCA technique<sup>4</sup> •  $\alpha_i$  and  $\beta_i$  are coefficients obtained from a calibration on data Advantages of this method:
- Pre-reconstruction, Does not rely on tracks / alignment immediate estimate

Our estimates are compared with the PVs reconstructed using VELO tracks. Resolution:  $\sigma_{x,y} \approx 4 \mu m$ .



 Does not require complex computing

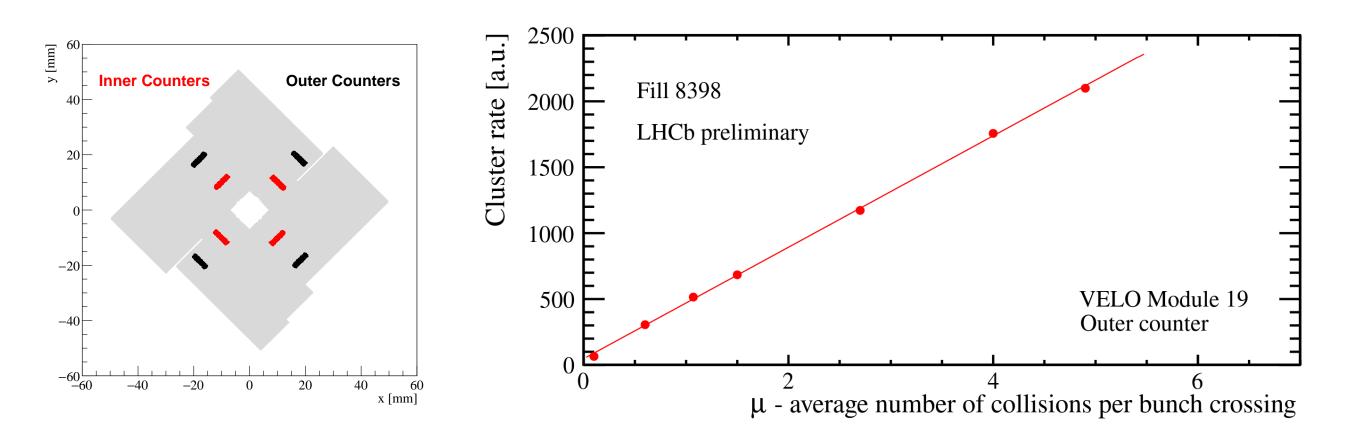




• Averaged counters as feedback to the LHC beam control

• Per-bunch-crossing counters as feedback to the LHC injection operations

Good linearity  $\Rightarrow$  suitable and robust for beam spot measurements



Using the counters positioned on only one side of the VELO, it is possible to measure the relative position of the two halves with respect to the luminous region. Resolution:  $O(4-7) \mu m$ .

#### Acknowledgements & References

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