

Upgrade of ATLAS Hadronic Tile Calorimeter for the High-Luminosity LHC

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Abstract

The Tile Calorimeter (TileCal) is a sampling hadronic calorimeter covering the central region of the ATLAS experiment, with steel as absorber and plastic scintillators as active medium. The High-Luminosity phase of LHC, delivering an instantaneous luminosity up to $7.5 \times 10^{34} \text{ cm}^{-2} \text{ s}^{-1}$, is expected to begin in 2029. TileCal will require new electronics to meet the requirements of a 1 MHz trigger (calorimeter output rate), higher ambient radiation, and to ensure better performance under high pile-up conditions. Both the on- and off-detector TileCal electronics will be replaced during the shutdown of 2026-2028. The TileCal upgrade program has included extensive R&D and test beam studies. A Demonstrator module with reverse compatibility with the existing system was inserted in ATLAS in August 2019 for testing in actual detector conditions. The ongoing developments for on- and off-detector systems, together with expected performance characteristics and recent results of test-beam campaigns with the electronics prototypes will be discussed.

Keywords: High-energy calorimetry, Hadronic calorimeter, Data acquisition system, ATLAS upgrade, High-luminosity LHC

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1. Introduction

High-energy physics experiments are built to explore and study the smallest known particles which are the fundamental building blocks of matter. In this context, the LHC (Large Hadron Collider) [1] has been designed to collide particles at CERN (European Organization for Nuclear Research) where two proton beams travel in opposite directions within a circular accelerator and collide, with a period of 25 ns. Dedicated experiments are built in particular points along the beam track to detect and measure the collision subproducts. The ATLAS (A Toroidal LHC ApparatuS) [2] detector is the largest experiment at LHC (a general-purpose experiment) with a large physics program. ATLAS records measurements of the particles created in collisions such as their tracks, energies, and their identities.

The ATLAS Tile Calorimeter (TileCal) [3] is the main hadronic calorimeter of the ATLAS experiment at LHC. From the construction point of view, TileCal is a sampling detector made of steel plates as absorber and scintillator tiles as active medium. TileCal absorbs energy from particles that are primarily hadrons and that pass through the ATLAS electromagnetic calorimeter. Physically, TileCal comprises three cylindrical parts: one central Tile barrel logically divided into two partitions (Long Barrel A and Long Barrel C) and two Tile extended barrels (Extended Barrel A and Extended Barrel C). Each part is composed of 64 modules to build the entire cylinder. The central barrel module and both extended barrels are further divided, respectively, into 45 and 16 cells with double readout. Figure 1 illustrates the ATLAS calorimeter system.

The light produced by each scintillating tile is collected by wavelength shifting (WLS) fibers and transmitted to Photo-Multiplier Tubes (PMT) where a fast electric signal is produced composing each readout channel (see Figure 2). This electric signal is further conditioned in the on-detector electronics through a shaper circuit [4] before being digitized at a sampling frequency of 40 MHz with 10-bit Analog-to-Digital Converters (ADCs) [5]. In total, almost 10,000 readout signals are available at every collision.

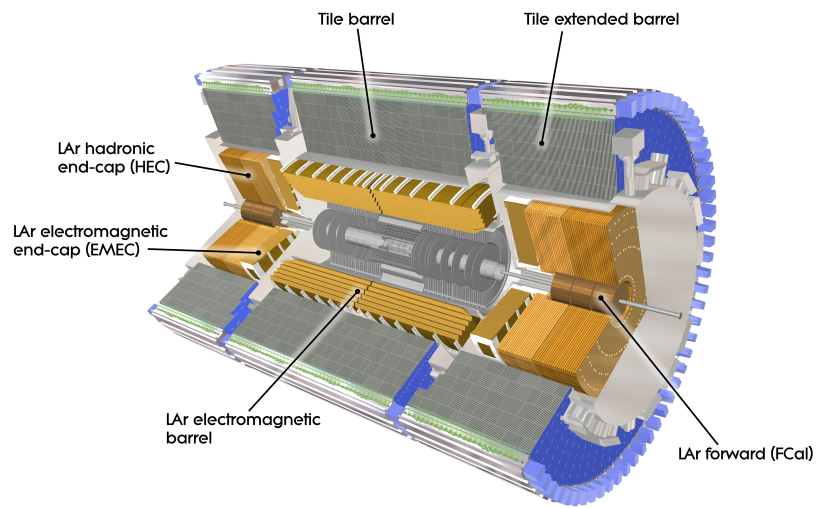


Figure 1: The ATLAS calorimeter system [2].

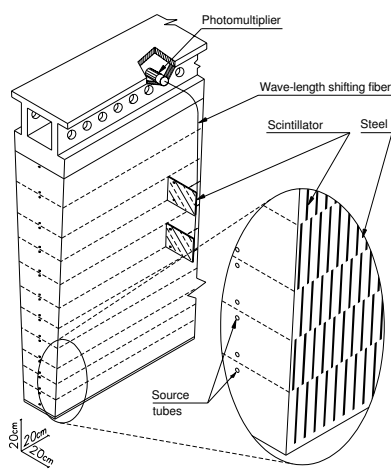


Figure 2: Components and mechanical structure of a TileCal module [2].

In the current TileCal electronics, the digitized data are stored in pipeline memories until a first level trigger acceptance signal is received. Then, the selected events are read out to the off-detector electronics. In the off-detector electronics, the Read-Out Drivers (ROD) gather the data coming from the front-end electronics at a maximum average trigger rate of 100 kHz [6]. After performing the energy and time reconstruction for each channel, RODs transmit the processed data to Read-Out Buffer in the ATLAS Level-2 trigger system. In total, 32 RODs are required for the complete TileCal readout system.

The High-Luminosity Large Hadron Collider (HL-LHC) project aims to increase the potential for discoveries after 2027 [7]. The goal is to increase luminosity by a factor of almost 10 beyond the LHC's design value, reaching an instantaneous luminosity of $7.5 \times 10^{34} \text{ cm}^{-2} \text{ s}^{-1}$. These upgrades will lead to an expected average number of collisions per bunch crossing up to 200, providing a total integrated luminosity of 4000 fb^{-1} by around 2040. In order to cope with such unprecedented operation conditions imposed by the new ATLAS trigger and data acquisition systems [8], the TileCal readout electronics needs to be completely revised. Some of the detector components such as the steel absorbers and the scintillating tiles will not be replaced.

2. The TileCal new electronics

In the ATLAS upgrade program, both on- and off- detector electronics of the TileCal will undergo major upgrade in order to meet the requirements of the HL-LHC, especially high radiation levels and 1 MHz trigger output, as well as good performance for reconstructed jets, tau leptons and missing energy in the environment with high pile-up conditions [9].

The photomultipliers (PMTs) and on-detector electronics of a module will be housed within a new modular configuration. This configuration consists of three-meter long drawers, so-called Super Drawers (SD). Each SD is itself composed of either four (for the long barrel drawers) or three (for the extended barrel drawers) mechanically attached independent Mini-Drawers [10]. The SD are lo-

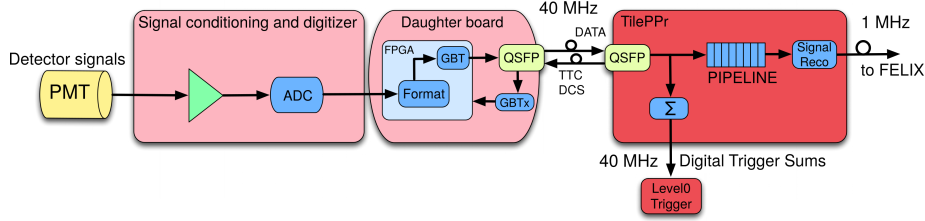


Figure 3: The HL-LHC upgrade TileCal signal readout chain [14].

cated at the outer radius of the Tilecal to minimise the radiation effects. The new configuration simplifies installation and manipulation. The mini-drawers contain the on-detector electronics required for the acquisition of the PMT signals as well as a high-speed interface with the off-detector electronics and a high voltage distribution system for the PMTs. The modularity, redundancy and robustness achieved by the new configuration improve the reliability of the on-detector acquisition system [11].

A Main Board (MB) is placed on top of each Mini-Drawer structure and is responsible to receive the PMT signals and digitize them before sending these data to the Daughter Board (DB). The DB synchronizes the digitized data and sends them to the TileCal Pre-Processor (PPr) at a rate of 40 MHz [12]. The full new read-out system and processing chain are shown in Figure 3.

In the new architecture, the output signals of the Tile detector cells will be digitized using the radiation hard on-detector front-end electronics, and then transferred off-detector for every bunch crossing for further processing. High Voltage Active Dividers (HVADs) are located at the end of every PMT. The function of the dividers is to distribute the proper input high-voltage power received from the high voltage system amongst the 8 PMT dynodes with an allowed variance of the set voltage of 0.5 V in the range of 600 to 900 Volts. Active components are used within the HVADs in addition to passive components thereby ensuring the PMT response linearity requirement of better than 1% at high luminosity [13].

Front End board for the New Infrastructure with Calibration and signal Shaping (FENICS) are attached to every HVAD. FENICS is a readout electronics board that is responsible for the amplification and shaping of the analog signals received from an individual PMT. The two main roles of a FENICS are to read the fast signals using two gains and to read the average current using six gains. The modular front-end electronics feature radiation-tolerant commercial off-the-shelf components and redundant design to minimise single points of failure. The timing, control and communication interface with the off-detector electronics is implemented with modern Field Programmable Gate Arrays (FPGAs) and high speed fibre optic links running up to 9.6 Gb/s.

All interfaces between the TileCal and Trigger and Data Acquisition (TDAQ) system are implemented in the TDAQ interface (TDAQi) modules. The TDAQi constructs the trigger primitives and interfaces with the trigger. It also communicates with the Front End Link eXchange (FELIX) system which is responsible for interfacing between TileCal and the ATLAS TDAQ. The Trigger FPGAs compute trigger objects for electrons, jet, global, and muon triggers and the results are transmitted digitally through low latency high-speed optical links to the Level-0 trigger.

A special version of the upgrade electronics, called Demonstrator, was built as a TileCal spare module to read out signals at the test beam campaigns [15]. It comprises a hybrid module (using both MB and DB components) that serves as a test case for the new components while ensuring compatibility with the current system. The Demonstrator system exhibited stable links between the on- and off-detector electronics as well as being well integrated with the TDAQ software used for the legacy system. It is worth mentioning that the Demonstrator readout achieves a 17-bit dynamic range using a two-gain 12-bit fast ADC readout with ratio of 1:32. In the legacy system, the readout provides a 16-bit dynamic range also with a two-gain ADC readout with a ratio of 1:64.

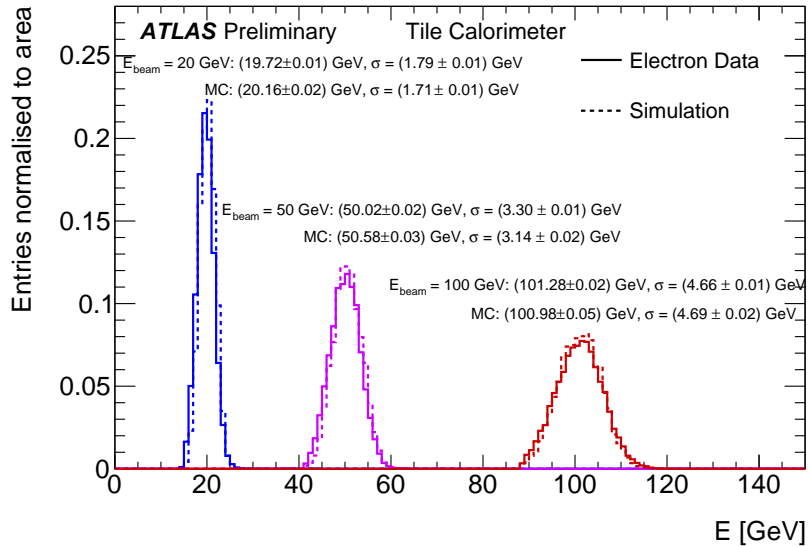


Figure 4: Distribution of total electron deposited energy for different beam energies. The solid line corresponds to data, while the dotted line shows the simulation result [16].

3. Preliminary results

Test beam campaigns took place during 2015-2021. The purpose of such activities was to evaluate and validate the proposed new TileCal electronics to be used in the HL-LHC operation. For example, Figure 4 shows the distributions obtained using experimental and simulated data samples considering electron beams of 20, 50 and 100 GeV incident in a particular TileCal cell. It can be seen that, for each energy condition, both distributions agree, proving the efficient selection of experimental electron samples.

Furthermore, tests with projective muons were also carried out. High energy muons traverse the entire TileCal modules for any angle of incidence, thereby allowing a study of the module response in great detail through their entire volume. It should be stressed that the energy deposited in the calorimeter by high energy muons is still smaller than electron energy depositions with the same energy scale. Figure 5 shows the distribution of muon energies (in ADC counts) processed by the new electronics where the pedestal (electronic offset)

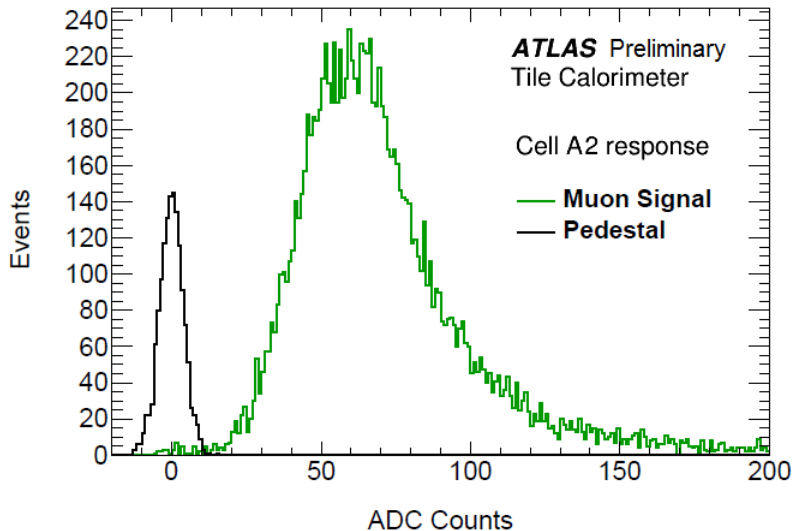


Figure 5: Muon and pedestal energies (in ADC counts) considering muons hitting the Demonstrator system [16].

distribution can also be seen for comparison.

Moreover, in order to achieve a good performance, the study of the response to isolated hadrons is also important for a hadronic calorimeter. In TileCal, the description of the response to hadrons is crucial to validate and improve the modeling of the jet energy characterization. Using the new electronics, the response of low energy hadrons (30 GeV Kaons) is shown in Figure 6 where the energy distribution from the hadrons is well described through the fitted curve.

4. Conclusions

The High-Luminosity LHC, which should be operational from the beginning of 2029, will allow scientists to study known mechanisms in greater detail and observe rare new phenomena. However, the conditions imposed by the HL-LHC program requires a full replacement of some key on- and off-detector components. In this context, for the ATLAS TileCal a new electronics has been designed to operate during the HL-LHC. The new system has been tested and

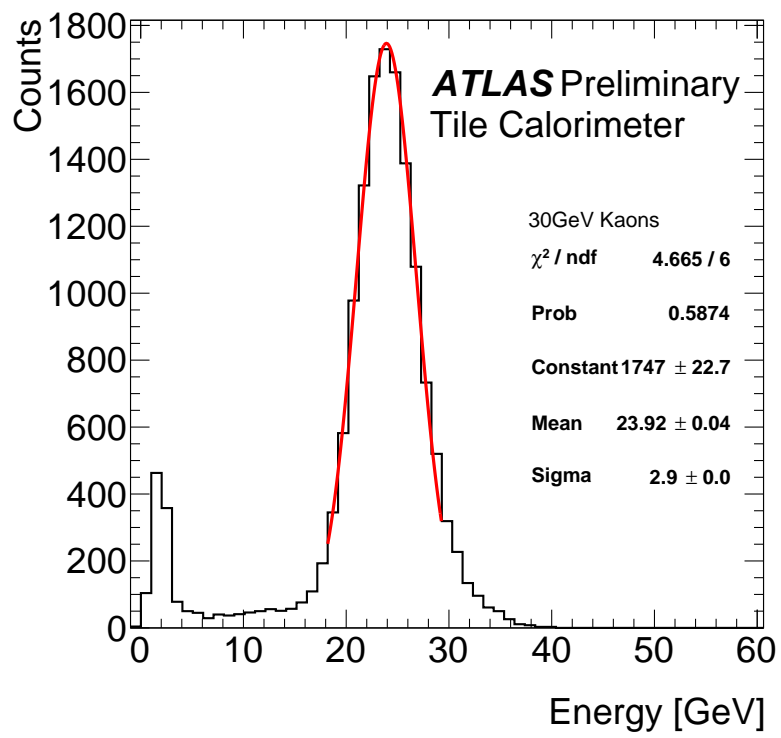


Figure 6: Reconstruction of hadrons using the new electronics system [16].

validated through test beam campaigns, using the so-called Demonstrator, populated with the new electronics which is fully integrated with the current system. The sensitive components have been radiation certified within the Demonstrator system. Test beam campaigns from 2015 to 2021 proved the good performance of the prototypes of new electronics installed in the Demonstrator module. More valuable experience of the new system will be gained through the next LHC operation period (Run-3), envisaging the installation towards the HL-LHC.

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