#### UPGRADED READOUT AND TRIGGER ELECTRONICS FOR THE ATLAS LIQUID-ARGON CALORIMETERS AT THE LHC AT THE HORIZONS 2018-2022

Denis Oliveira Damazio (BNL) on behalf of the ATLAS Liquid-Argon Calorimeter Collaboration

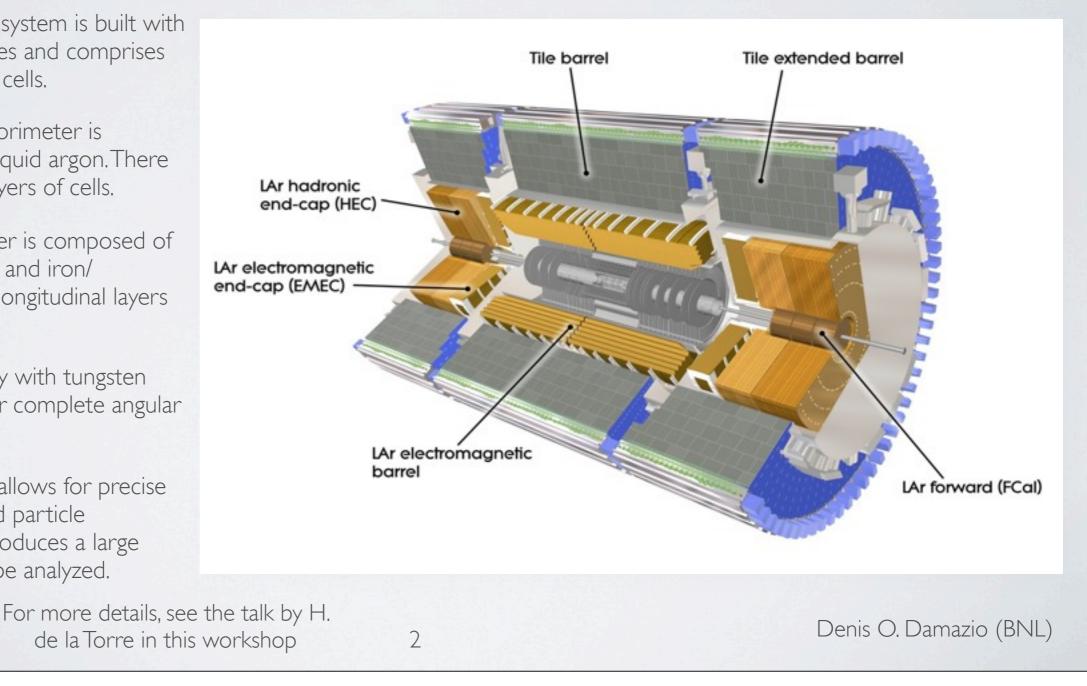


CHEF 2013, Paris, France



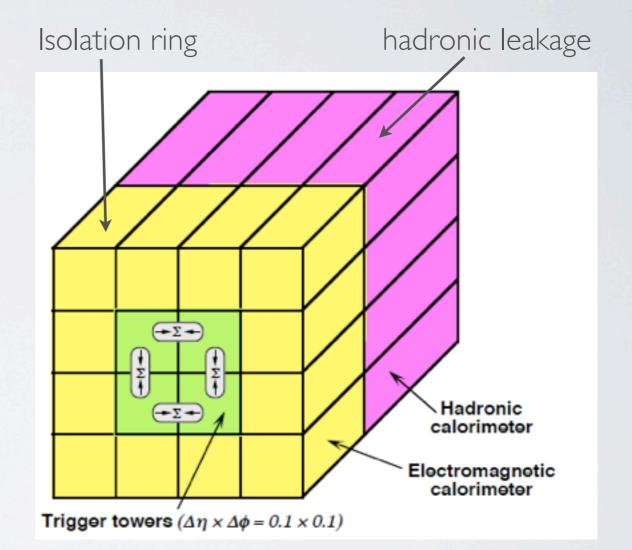
# ATLAS CALORIMETER SYSTEM

- ATLAS calorimeter system is built with different technologies and comprises more than 187.000 cells.
- Electromagnetic calorimeter is composed of lead/liquid argon. There are 4 longitudinal layers of cells.
- Hadronic calorimeter is composed of copper/liquid argon and iron/ scintillator. Up to 4 longitudinal layers of cells.
- Forward calorimetry with tungsten rods/liquid argon for complete angular coverage.
- Refined granularity allows for precise energy measure and particle identification but produces a large amount of data to be analyzed.



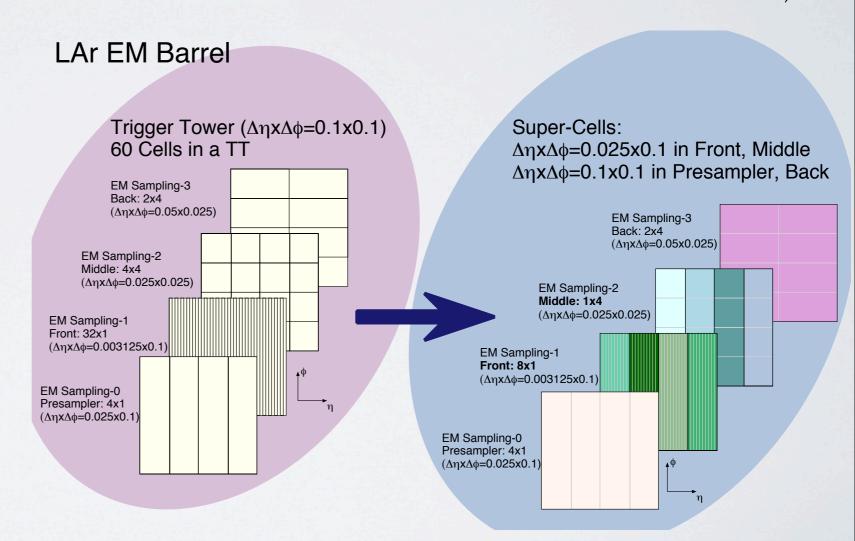
# LI CALORIMETER SYSTEM

- The ATLAS trigger system is staged in 3 levels.
- The LevelI (LI) Calorimeter uses a coarser granularity of the detector, grouping analogically many cells into Trigger Towers (TT - up to 60 cells in the EM barrel). Readout with ~I GeV Least Significant Bit.
- Two longitudinal segmentations (EM and hadronic a total of 6400 TT in the EM region) available to the L1 : reduction of data volume for real-time and lowlatency L1 processing.
- A local maximum of the vertical or horizontal sums (green) of two trigger towers is considered to be a possible electron candidate.
- The L1 (see talk by J. Bracinik in this meeting) behaves quite well up to the luminosity levels reached so far by the LHC.



# INCREASE OF GRANULARITY

- When beyond the present luminosity levels (>10<sup>34</sup>cm<sup>-2</sup>s<sup>-1</sup>), some of the isolation variables used, may loose effectiveness.
- In order to deal with the increasing amount of pile-up events, it is important to increase the granularity accessible at the L1 : hence the concept of Super-Cells.
- Also the energy resolution of the object shall increase (initial analysis indicates improvements when using LSB of 65 MeV - first layer - and 250 MeV - second layer).
- There will be 4 layers of Super-Cells in the EM session : could later be explored for energy calibration techniques similar to the ones used offline.



Δφ=0.1

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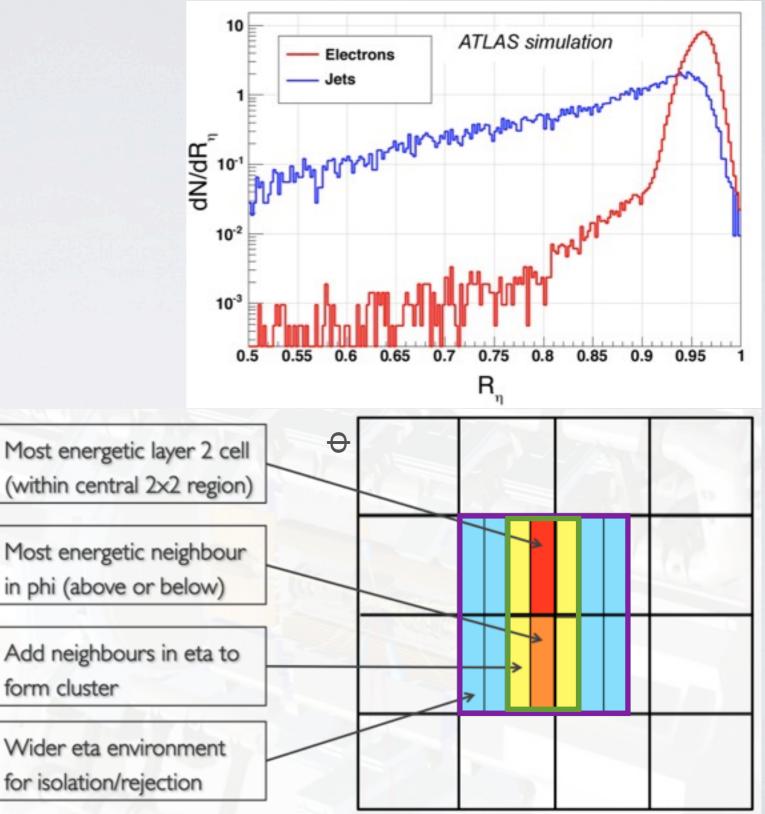
Δn=0.075

(EM cluster showing only cells with Et> 100 MeV)

#### NEW SHOWER SHAPE VARIABLE

 $R_{\eta} = E_{3\times 2}/E_{7\times 2}$ 

- Inspired by the offline software, we define a new variable called Rη of the second layer.
- This shower shape variable is more robust to the change of pile-up level (given its intrinsic energy normalization).

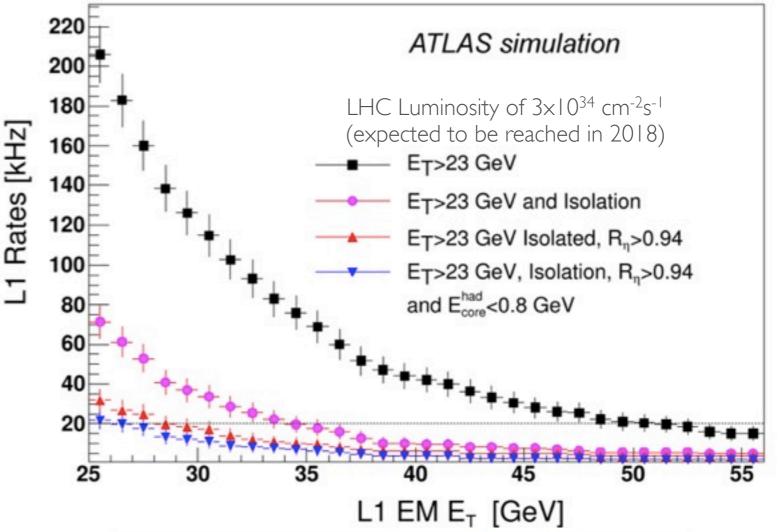


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# RATE REDUCTION

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- Offline electron detection efficiency of 98%.
- The present L1 trigger would fire at 20 kHz with a 34 GeV threshold.
- Using R<sub>n</sub>, we manage to reduce the threshold even further to 28 GeV.



Selection criteria	Rate reduction	
	Fraction of (1)	Fraction of (2)
(1): Level-1 EM $E_T > 23$ GeV	100%	-
(2): (1) and Level-1 isolation	34.9%	100%
(3): (2) and $R_{\eta}$	14.25%	40.8%
(4): (3) and E <sup>had</sup> <sub>core</sub>	11.45%	32.8%

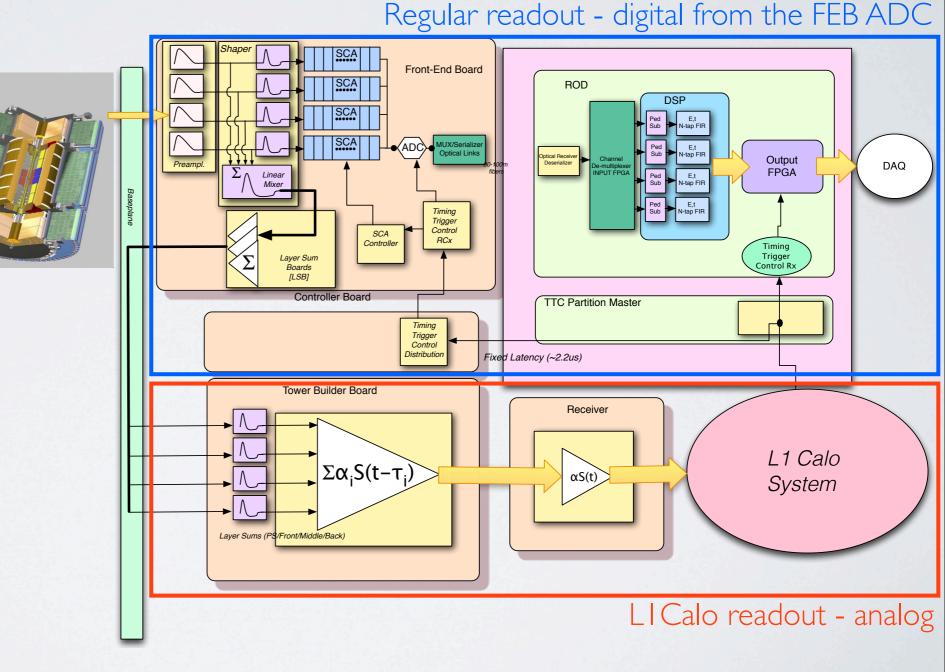
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# PRESENT LAR-LI INTERFACE

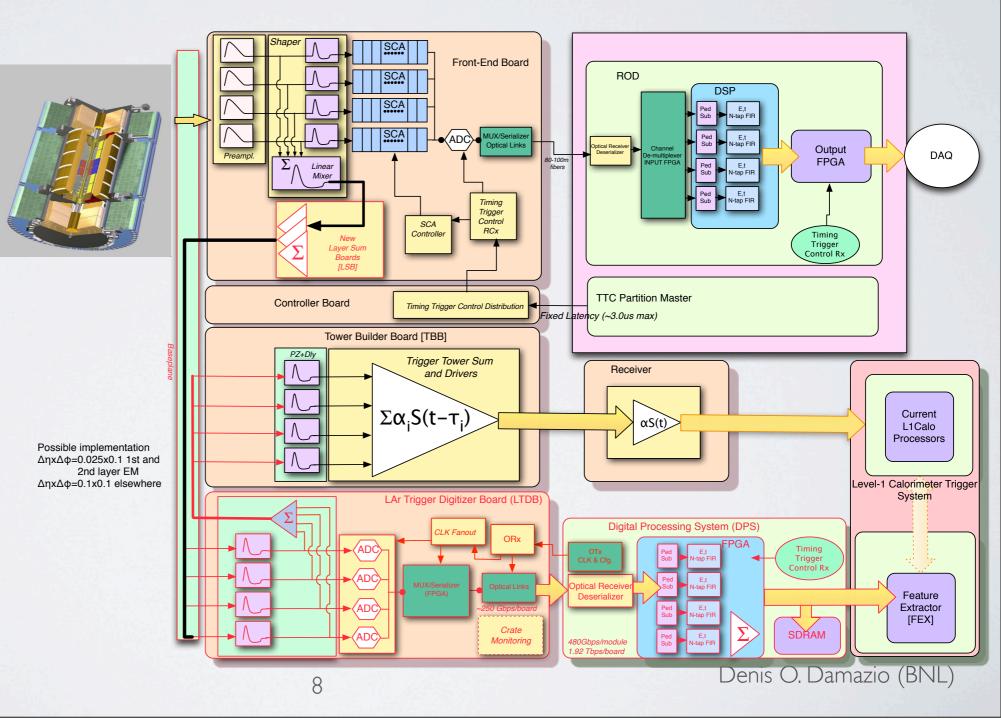
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- The cells energy values wait in a Switch Capacitor Array (SCA) of the Front-End Boards (FEBs) and digitization ONLY performed for accepted events.
- A partial sum of the analogue pulses is performed within each FEB by the Layer Sum Board.
- These partial sums are fed to the Tower Builder Board which makes the final Trigger Tower sum, sending the results to the LI Trigger.
  - All sums and operations up to the LI Calo System are analogue.



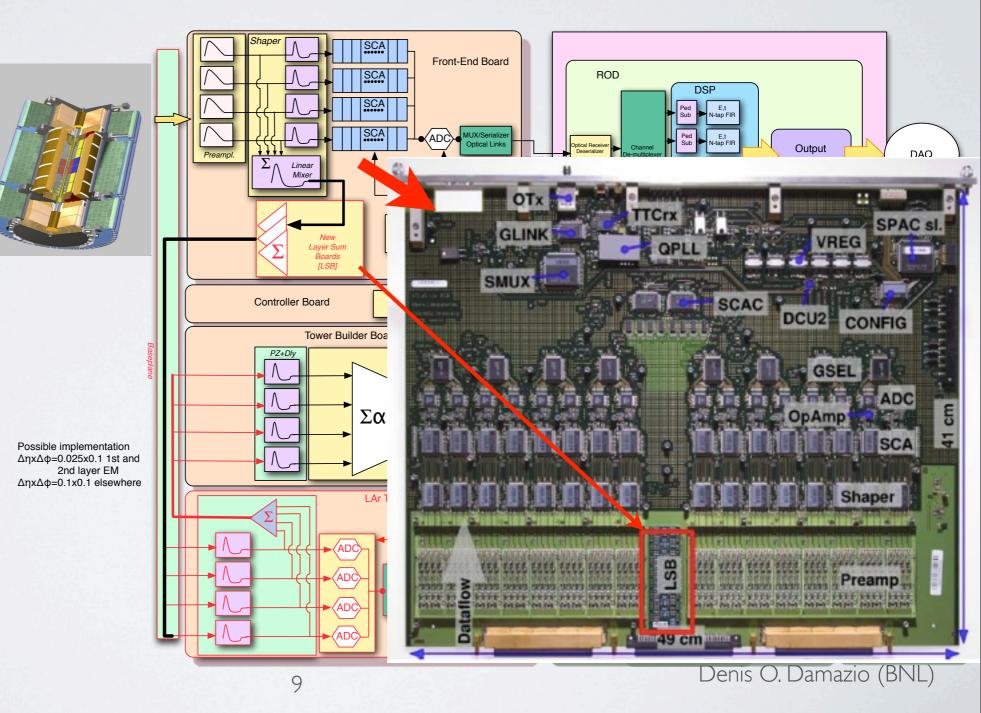
#### PHASE-I SYSTEM

- The Layer Sum Boards (LSB) are planned to be changed to produce partial sums restricted to the Super-Cell within each FEB for each layer.
- New LAr Trigger Digitizer Boards - LTDB are planned to be added to each crate. They digitize the signals and send data using optical links for further processing.
- Analogue signals are foreseen to be sent to the present L1 infrastructure to provide back compatibility.
- The analogue signals would be digitized at the LHC clock rate and the energy calculated per bunch crossing in the Digital Processing System (DPS).



# HARDWARE MODIFICATIONS

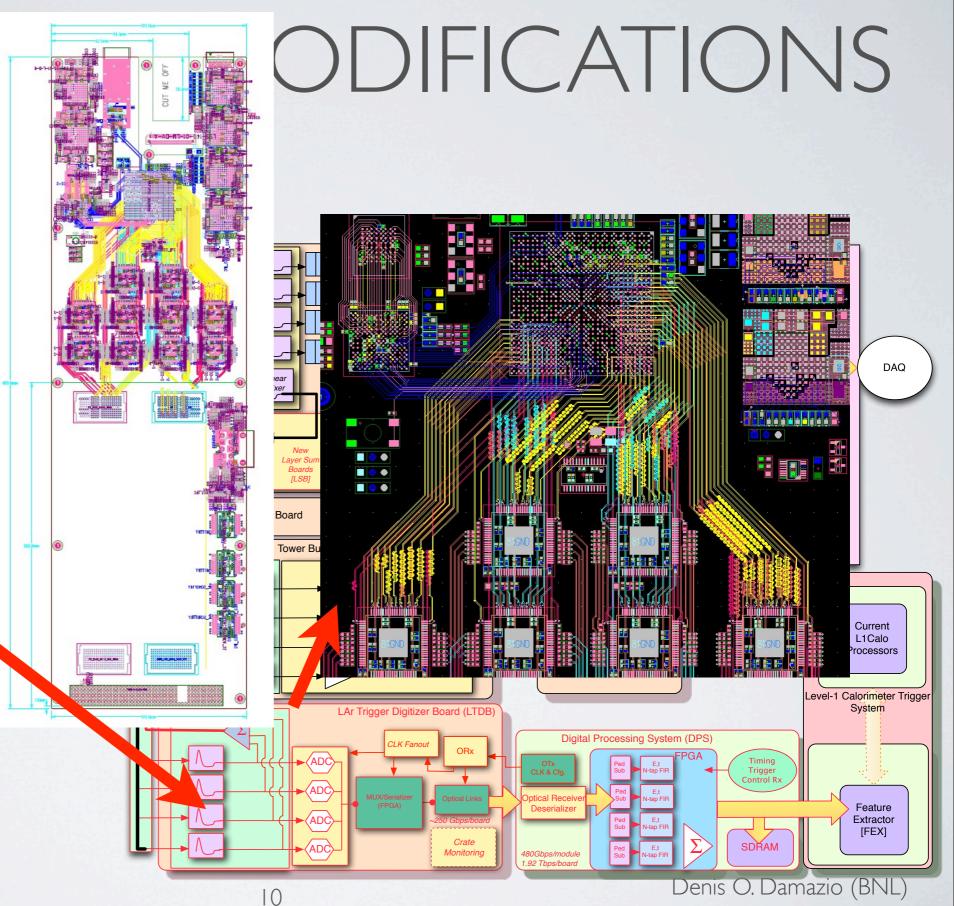
- The present LSBs are mezzanine boards.
- More than 1500 FEBs will be removed from the detector, modified and re-installed in the period of less than one year (2017).
- The baseplanes inside each front-end crate are planned to be replaced.



## HARDW

LTDB initial design considered.

- Phase-II upgrade compatibility requires radiation tolerance to a total luminosity of 3000 fb-1.
- Pre-Amplifier : custom ASICs currently being tuned and tested with radiation.
- Total Ionization Dose up to 1.74 kGy; Non-Ionizing energy loss of 5.0x10<sup>13</sup> n<sub>eq</sub>/cm<sup>2</sup>; Single-Event Effect of 9.6x10<sup>12</sup>h/cm<sup>2</sup>.



#### NEW ADC

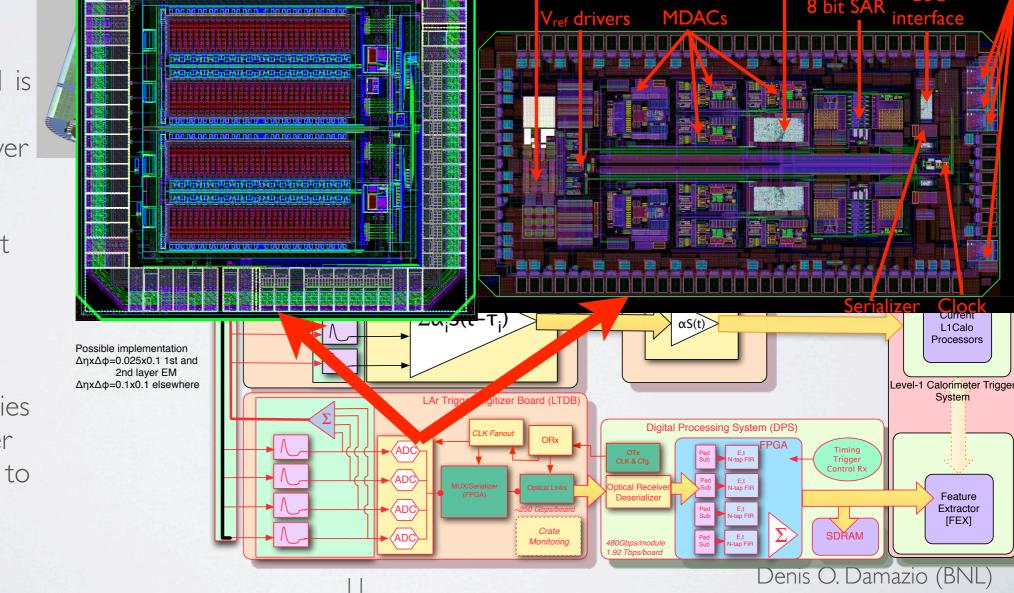
Three different solutions being investigated for the LAr calorimeters.

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- 12 bits ADCs (present L1 is effectively 8 bits), low latency (<70ns), low power (<100mW/channel).
- 40 MSamples/s with Least Significant Bit yet to be defined (65 - first layer; 250 MeV - second layer).
- Power and radiation studies on-going also for serializer ASICs of the optical links to the DPS.



**Band Gap** 

SLVS Dri

L1Calo

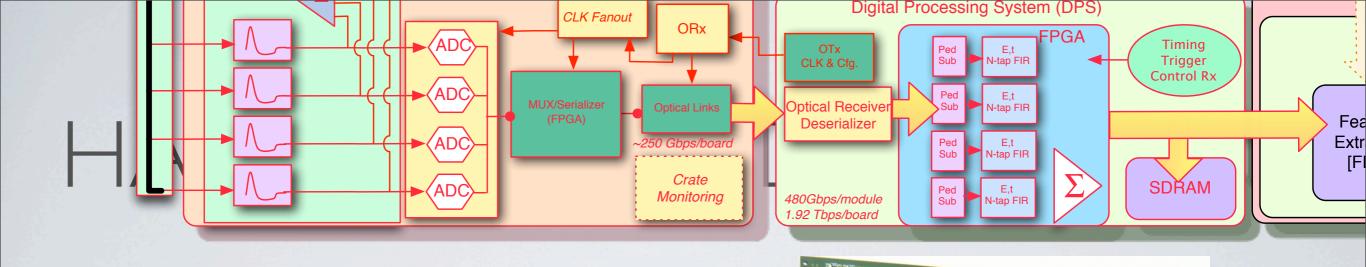
Processors

System

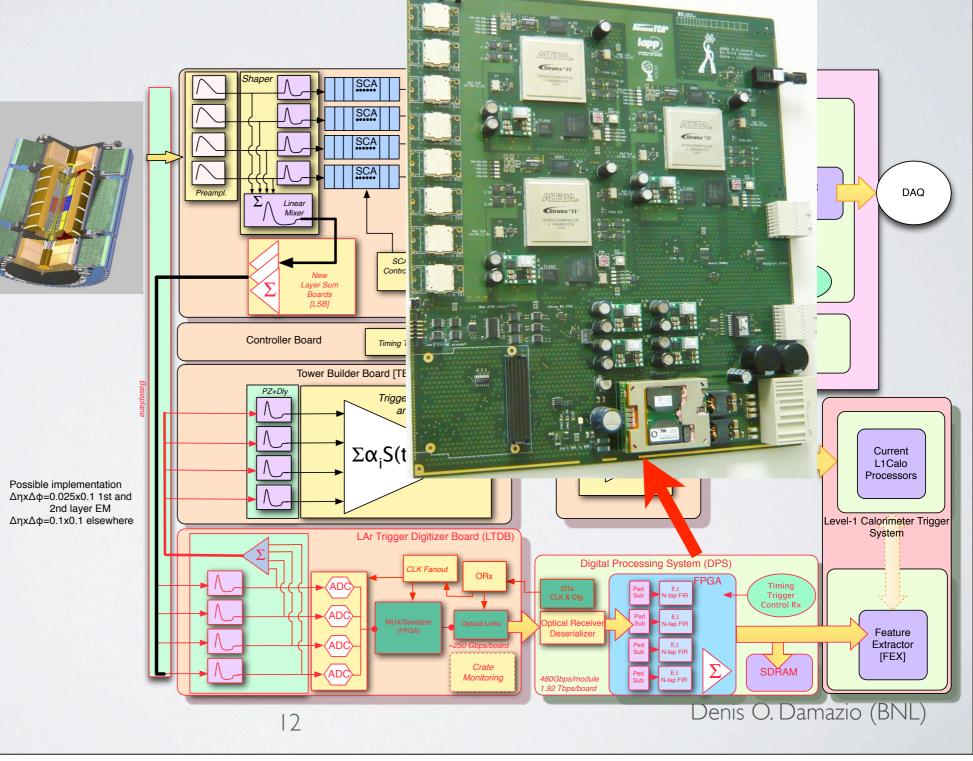
Feature

Extractor [FEX]

DDPU

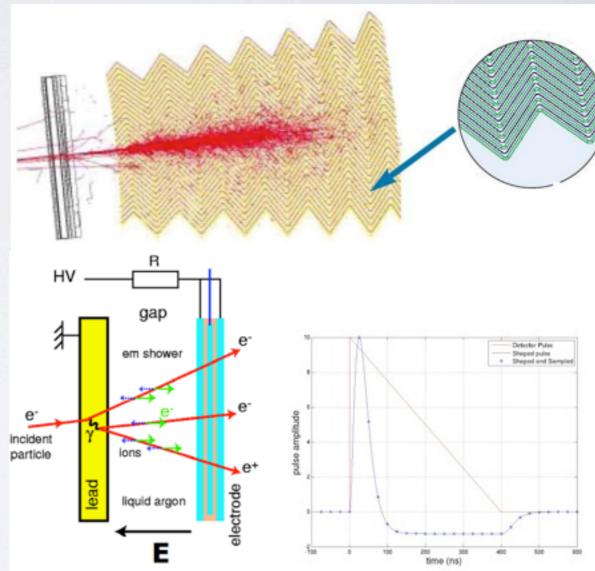


- The Digital Processing System performs the energy reconstruction for each of the 320 Super-Cells per LTDB.
- Based on the ATCA standard, a prototype with 3 FPGAs was prepared.
- This board is controlled via IPMI network-based system.
- Initial prototypes and test systems being prepared now at CERN : a demonstrator system is planned to be installed during the current LHC shutdown.



# SIGNAL PROCESSING CHAIN

- The Super-Cells signal pulses will have similar shape as pulses from regular cells : Digital filter techniques are developed for Super-Cell signal treatment. These are optimized for high-luminosity phase and can be applied for Phase-II digital readout.
- The usage of solutions to decorrelate the multiple samples of the signal in a given bunch-crossing from pulses in neighbor bunch crossings is a fundamental step to provide a better energy reconstruction. This approach is called the Optimum Filter technique.
- Other signal processing techniques, such as Wiener filters, deconvolution and matched filter techniques are being considered for implementation in the DPS and later in the phase-II Read-Out Drivers (RODs). These techniques are now being implemented for MC simulations.

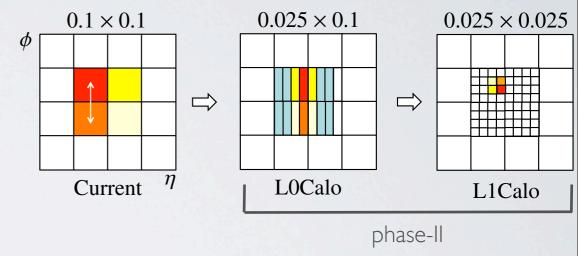


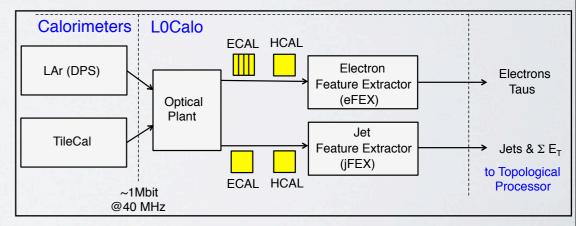
~1Mbit @40 MHz ECAL HCAL

Processor

## PHASE-II

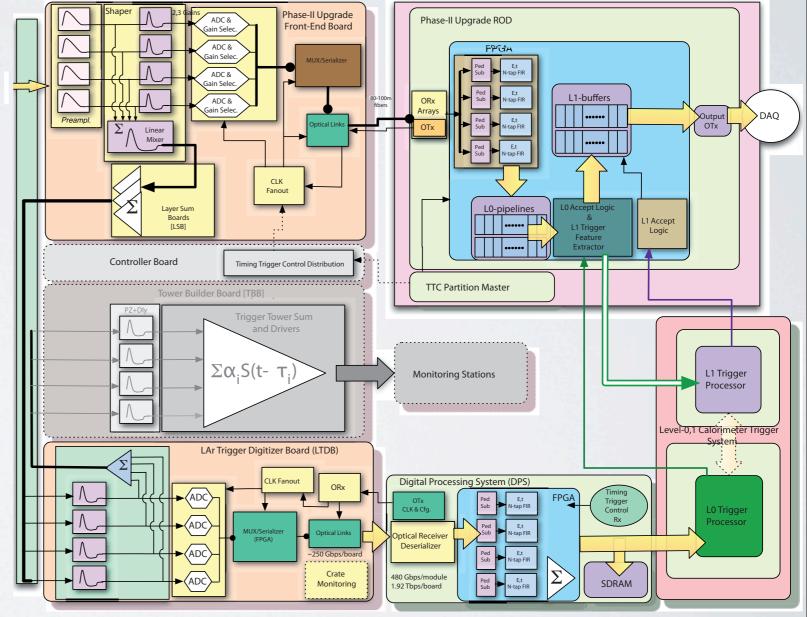
- In the phase II upgrade of the LHC (after 2022), instantaneous luminosities of up to 5×10<sup>34</sup>cm<sup>-2</sup>s<sup>-1</sup>.
- A short-latency trigger signal is possibly still required in phase-II: the L1 trigger developed for phase-I can become a L0 trigger in phase-II.
- Higher granularity information is planned to be used at the future L1 (larger-latency) => Readout of all detector cells => much more complex algorithms can be used.
- Optical links will bring the information from the detector to the L1. Possible combination with a L1 tracking system being investigated.





# PHASE II SCHEMATICS

- All FEBs will be replaced.
- Signals are planned to be digitized and serialized in free-running mode.
- required bandwidth for the optical connection to the ROD will be much higher.
- The phase-II ROD will implement data buffering and transfer on L0 and L1 trigger accept signals.
- The ADC solutions used in phase-I for the LTDB may be adapted to the phase-II FEBs.
- The phase-I DPS solution (ATCA-based) may evolve to a future phase-II ROD.



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## CONCLUSIONS

- The improved granularity of the Liquid Argon Calorimeter based trigger readout for the phase-I LHC upgrade is discussed. We also discuss the plans to improve the future detector readout in phase-II. Most of the design being considered now to the Super-Cells readout is directed towards the phase-II regular cells readout (ADC, Digital Signal processing hardware and energy reconstruction algorithms).
- Initial analysis demonstrates clear advantage of using the higher segmentation for electron detection. A reduction of the minimal threshold guarantees keeping the electro-weak sector of the physics to be explored in the High-Luminosity LHC scenario.
- Initial design of the new hardware and modification of the existing hardware is already being planned in details.
- Other triggers are also being explored for jets, taus and missing transverse energy to evaluate performance improvement thanks to the increased granularity (and new trigger algorithms) as well as new energy reconstruction techniques for higher pile-up.

#### REFERENCES

- I. ATLAS liquid argon calorimeter: Technical design report ATLAS Collaboration CERN-LHCC-96-41
- 2. Status of the ATLAS Liquid Argon Calorimeter Performance after 2 years of LHC operation. Hass AbouZeid on behalf of the ATLAS Collaboration.
- 3. Letter of Intent for the Phase-I Upgrade of the ATLAS Experiment CERN-LHCC-2011-012.
- 4. W. E. Cleland and E. G. Stern. Signal processing considerations for liquid ionization calorimeters in a high rate environment 1994. Nucl.Instrum.Meth.,A338,467.
- 5. A. Strässner et al.. Development of new readout electronics for the ATLAS LAr calorimeter at the sLHC 2009.