

A CIMBUS Compatible 16k byte static RAM

To complement the M6800 microprocessor system, described in Int. Note SPS/AMR/RBF/CA/78-2, a 4 x 4k byte static RAM card has been developed which is fully CIMBUS compatible.

In the event of power failure, provision for short term data retention has been included by means of an external battery.

Technical specifications

Memory - 4k bytes; expandable in 4k increments to 16k.
The circuits used are the TMS 4046-45

Access time - < 450 ns

Address Decoding - Full address decoding is provided by means of on board switches.

Bus connections - The fully buffered memory bus may either be connected to CIMBUS or the microprocessor external memory connector.

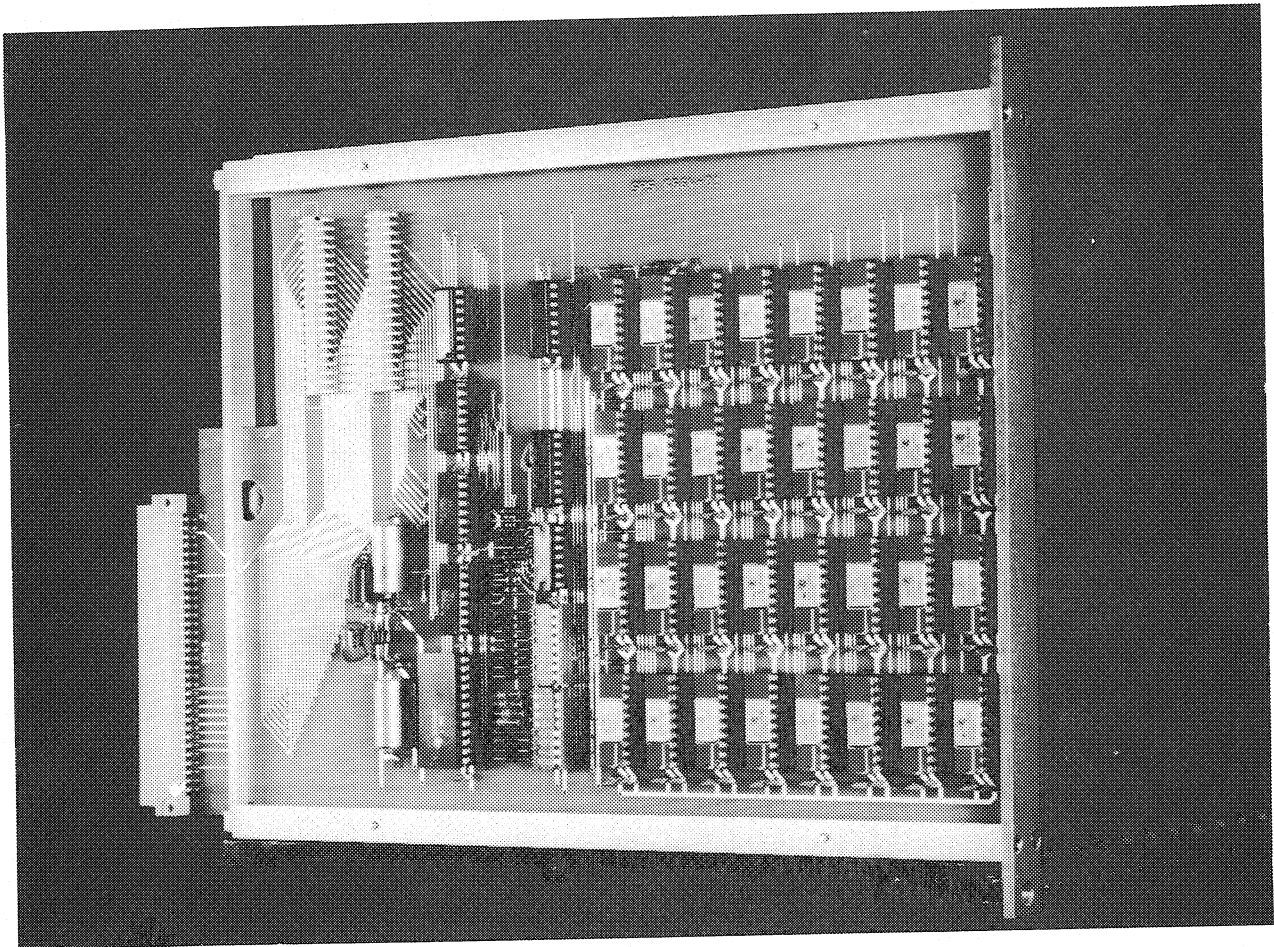
The unit has been designed in a 5 unit high, 1 unit wide, CIM module.

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CM-P00063687



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