



# SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECrate2017\_int\_base = 83.8

### Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

CPU2017 License: 3175

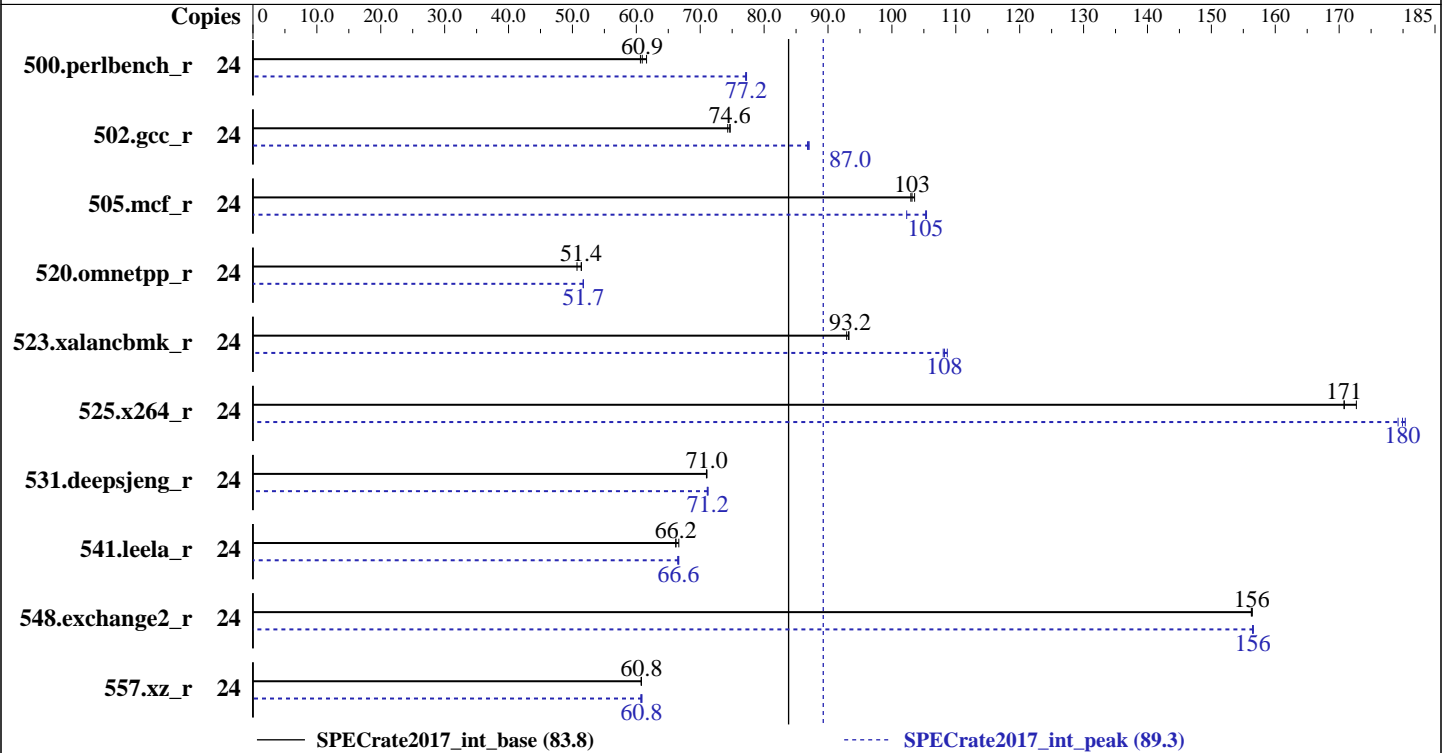
Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018



### Hardware

CPU Name: Intel Xeon Gold 6128  
 Max MHz.: 3700  
 Nominal: 3400  
 Enabled: 12 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 19.25 MB I+D on chip per chip  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx4 PC4-2666V-R)  
 Storage: 1 x 1200 GB SAS, 10000 RPM  
 Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 3.10.0-693.11.6.el7.x86\_64  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: Version 0.59 Released Feb-2018  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc: jemalloc memory allocator library V5.0.1;



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECrate2017\_int\_base = 83.8

## Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei

Test Date: May-2018  
Hardware Availability: Jul-2017  
Software Availability: Jan-2018

### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	24	620	61.6	630	60.6	<b>627</b>	<b>60.9</b>	24	495	77.3	<b>495</b>	<b>77.2</b>	495	77.1
502.gcc_r	24	457	74.3	<b>455</b>	<b>74.6</b>	455	74.7	24	<b>391</b>	<b>87.0</b>	392	86.8	390	87.0
505.mcf_r	24	375	104	<b>376</b>	<b>103</b>	377	103	24	379	102	<b>368</b>	<b>105</b>	368	105
520.omnetpp_r	24	621	50.7	613	51.4	<b>613</b>	<b>51.4</b>	24	<b>609</b>	<b>51.7</b>	609	51.7	609	51.7
523.xalancbmk_r	24	<b>272</b>	<b>93.2</b>	272	93.3	273	92.9	24	235	108	<b>234</b>	<b>108</b>	233	109
525.x264_r	24	246	171	243	173	<b>246</b>	<b>171</b>	24	<b>234</b>	<b>180</b>	234	179	233	180
531.deepsjeng_r	24	388	71.0	387	71.0	<b>387</b>	<b>71.0</b>	24	<b>386</b>	<b>71.2</b>	387	71.1	386	71.2
541.leela_r	24	<b>600</b>	<b>66.2</b>	596	66.7	601	66.2	24	<b>597</b>	<b>66.6</b>	598	66.5	596	66.6
548.exchange2_r	24	402	156	<b>402</b>	<b>156</b>	402	156	24	402	156	<b>402</b>	<b>156</b>	402	157
557.xz_r	24	<b>426</b>	<b>60.8</b>	426	60.8	426	60.8	24	427	60.7	426	60.9	<b>426</b>	<b>60.8</b>

SPECrate2017\_int\_base = 83.8

SPECrate2017\_int\_peak = 89.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### General Notes

Environment variables set by runcpu before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/lib/ia32:/spec/lib/intel64:/spec/je5.0.1-32:/spec/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>  
jemalloc: configured and built at default for  
32bit (i686) and 64bit (x86\_64) targets;  
jemalloc: built with the RedHat Enterprise 7.4,  
and the system compiler gcc 4.8.5;  
jemalloc: sources available from jemalloc.net or  
<https://github.com/jemalloc/jemalloc/releases>

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECrate2017\_int\_base = 83.8

## Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** May-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Jan-2018

### General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

### Platform Notes

BIOS configuration:  
Power Policy Set to Performance  
SNC Set to Enabled  
IMC Interleaving Set to 1-way Interleave  
XPT Prefetch Set to Enabled  
ADDDC Sparing Set to Disabled  
Sysinfo program /spec/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on localhost.localdomain Fri May 4 11:26:39 2018

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) Gold 6128 CPU @ 3.40GHz  
2 "physical id"s (chips)  
24 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6  
siblings : 12  
physical 0: cores 0 6 9 10 11 13  
physical 1: cores 0 6 9 10 11 13

From lscpu:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 24  
On-line CPU(s) list: 0-23  
Thread(s) per core: 2  
Core(s) per socket: 6  
Socket(s): 2  
NUMA node(s): 4  
Vendor ID: GenuineIntel  
CPU family: 6

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECrate2017\_int\_base = 83.8

## Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** May-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Jan-2018

### Platform Notes (Continued)

```

Model: 85
Model name: Intel(R) Xeon(R) Gold 6128 CPU @ 3.40GHz
Stepping: 4
CPU MHz: 3400.000
BogoMIPS: 6806.04
Virtualization: VT-x
L1d cache: 32K
L1i cache: 32K
L2 cache: 1024K
L3 cache: 19712K
NUMA node0 CPU(s): 0,2,3,12,14,15
NUMA node1 CPU(s): 1,4,5,13,16,17
NUMA node2 CPU(s): 6,8,9,18,20,21
NUMA node3 CPU(s): 7,10,11,19,22,23

```

```

/proc/cpuinfo cache data
cache size : 19712 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 2 3 12 14 15
node 0 size: 96437 MB
node 0 free: 93895 MB
node 1 cpus: 1 4 5 13 16 17
node 1 size: 98304 MB
node 1 free: 95808 MB
node 2 cpus: 6 8 9 18 20 21
node 2 size: 98304 MB
node 2 free: 96046 MB
node 3 cpus: 7 10 11 19 22 23
node 3 size: 98304 MB
node 3 free: 96035 MB
node distances:
node  0  1  2  3
0:  10  11  21  21
1:  11  10  21  21
2:  21  21  10  11
3:  21  21  11  10

```

```

From /proc/meminfo
MemTotal: 394174484 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:

```

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

## Huawei

SPECrate2017\_int\_base = 83.8

## Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

**CPU2017 License:** 3175  
**Test Sponsor:** Huawei  
**Tested by:** Huawei

**Test Date:** May-2018  
**Hardware Availability:** Jul-2017  
**Software Availability:** Jan-2018

### Platform Notes (Continued)

```
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-693.11.6.el7.x86_64 #1 SMP Thu Dec 28 14:23:39 EST
2017 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 4 11:19
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda8        xfs   325G   81G  245G  25% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS INSYDE Corp. 0.59 02/24/2018
Memory:
4x NO DIMM NO DIMM
12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666
```

(End of data from sysinfo program)

### Compiler Version Notes

```
=====  
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)  
525.x264_r(base, peak) 557.xz_r(base, peak)  
-----
```

```
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----
```

```
=====  
CC 500.perlbench_r(peak) 502.gcc_r(peak)  
-----
```

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017\_int\_base = 83.8

Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei

Test Date: May-2018  
Hardware Availability: Jul-2017  
Software Availability: Jan-2018

## Compiler Version Notes (Continued)

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
CXXC 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base)  
541.leela\_r(base)

=====  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
CXXC 520.omnetpp\_r(peak) 523.xalancbmk\_r(peak) 531.deepsjeng\_r(peak)  
541.leela\_r(peak)

=====  
icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

=====  
FC 548.exchange2\_r(base, peak)

=====  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

Fortran benchmarks:  
ifort

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017\_int\_base = 83.8

Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

CPU2017 License: 3175  
Test Sponsor: Huawei  
Tested by: Huawei

Test Date: May-2018  
Hardware Availability: Jul-2017  
Software Availability: Jan-2018

## Base Portability Flags (Continued)

520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

C++ benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc

## Base Other Flags

C benchmarks:

-m64 -std=c11

C++ benchmarks:

-m64

Fortran benchmarks:

-m64

## Peak Compiler Invocation

C benchmarks:

icc

(Continued on next page)



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017\_int\_base = 83.8

Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018

## Peak Compiler Invocation (Continued)

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc

502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc

505.mcf_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib
-ljemalloc

525.x264_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -fno-alias
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

(Continued on next page)





# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017\_int\_base = 83.8

Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: May-2018

Hardware Availability: Jul-2017

Software Availability: Jan-2018

## Peak Optimization Flags (Continued)

557.xz\_r: Same as 505.mcf\_r

C++ benchmarks:

```
520.omnetpp_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

```
523.xalancbmk_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

531.deepsjeng\_r: Same as 520.omnetpp\_r

541.leela\_r: Same as 520.omnetpp\_r

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Peak Other Flags

C benchmarks (except as noted below):

```
-m64 -std=c11
```

502.gcc\_r: -m32 -std=c11

C++ benchmarks (except as noted below):

```
-m64
```

523.xalancbmk\_r: -m32

Fortran benchmarks:

```
-m64
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.html>



# SPEC CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

SPECrate2017\_int\_base = 83.8

Huawei XH321 V5 (Intel Xeon Gold 6128)

SPECrate2017\_int\_peak = 89.3

**CPU2017 License:** 3175

**Test Sponsor:** Huawei

**Tested by:** Huawei

**Test Date:** May-2018

**Hardware Availability:** Jul-2017

**Software Availability:** Jan-2018

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.9-revC.xml>

SPEC is a registered trademark of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU2017 v1.0.2 on 2018-05-04 11:26:38-0400.

Report generated on 2018-10-31 18:06:41 by CPU2017 PDF formatter v6067.

Originally published on 2018-06-13.