



# SPEC® CPU2017 Integer Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Epsilon Sp. z o.o. Sp. Komandytowa

SPECrate2017\_int\_base = 43.8

eterio 210 RE1 (Intel Xeon Silver 4112, 2.60 GHz)

SPECrate2017\_int\_peak = 45.4

CPU2017 License: 9081

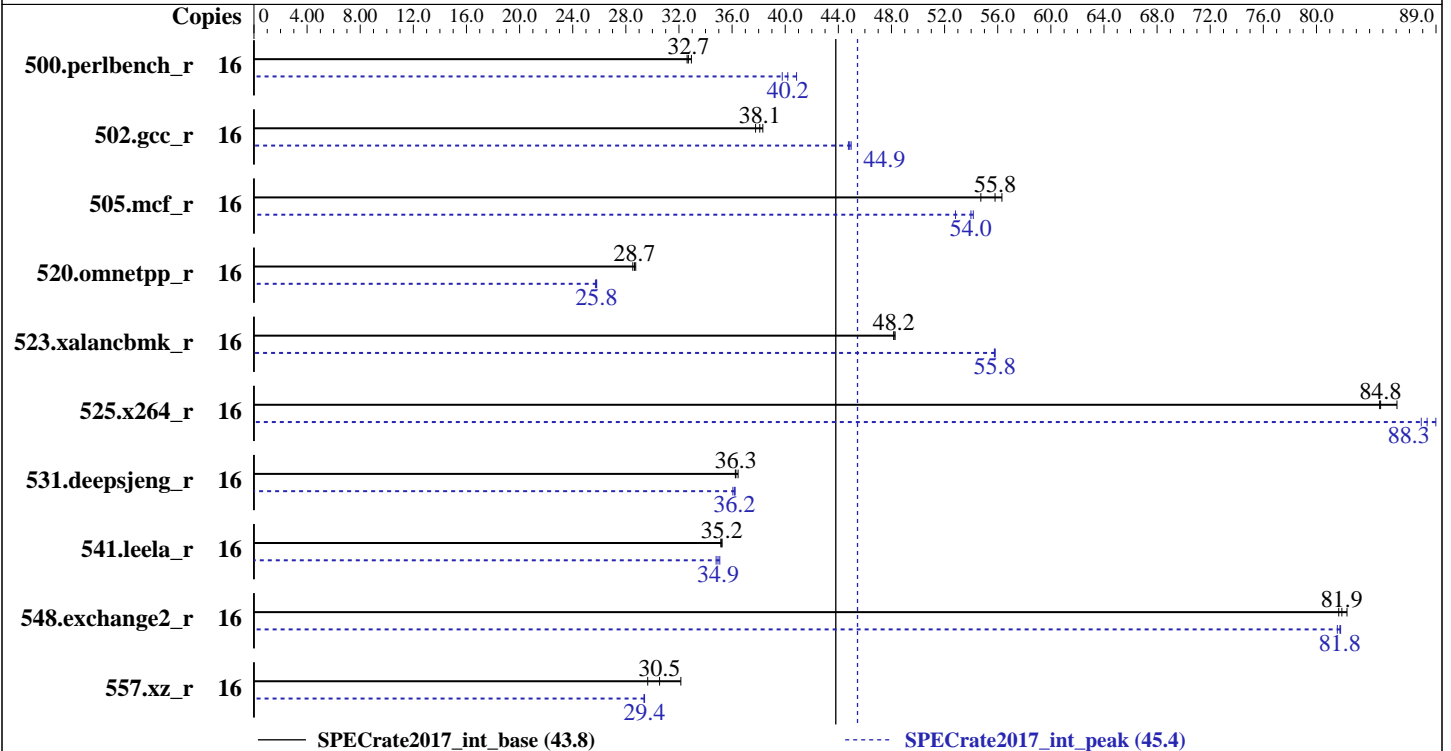
Test Date: Apr-2018

Test Sponsor: Epsilon Sp. z o.o. Sp. Komandytowa

Hardware Availability: Sep-2017

Tested by: Epsilon Sp. z o.o. Sp. Komandytowa

Software Availability: Mar-2018



## Hardware

CPU Name: Intel Xeon Silver 4112  
 Max MHz.: 3000  
 Nominal: 2600  
 Enabled: 8 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 8.25 MB I+D on chip per chip  
 Other: None  
 Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2666V-R, running at 2400)  
 Storage: 1 x 960 GB SSD SATA III  
 Other: None

## Software

OS: Red Hat Enterprise Linux Server release 7.4 (Maipo)  
 3.10.0-693.21.1.el7.x86\_64  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
 Parallel: No  
 Firmware: Version BIOS 2.0b released Mar-2018  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other: jemalloc memory allocator library V5.0.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	16	773	32.9	<b><u>779</u></b>	<b><u>32.7</u></b>	781	32.6	16	623	40.9	<b><u>634</u></b>	<b><u>40.2</u></b>	640	39.8
502.gcc_r	16	591	38.3	<b><u>595</u></b>	<b><u>38.1</u></b>	600	37.8	16	504	45.0	<b><u>505</u></b>	<b><u>44.9</u></b>	506	44.8
505.mcf_r	16	459	56.3	<b><u>463</u></b>	<b><u>55.8</u></b>	472	54.7	16	<b><u>479</u></b>	<b><u>54.0</u></b>	477	54.2	489	52.8
520.omnetpp_r	16	<b><u>733</u></b>	<b><u>28.7</u></b>	736	28.5	730	28.7	16	813	25.8	<b><u>814</u></b>	<b><u>25.8</u></b>	817	25.7
523.xalancbmk_r	16	<b><u>351</u></b>	<b><u>48.2</u></b>	350	48.3	351	48.2	16	<b><u>303</u></b>	<b><u>55.8</u></b>	303	55.7	303	55.8
525.x264_r	16	<b><u>330</u></b>	<b><u>84.8</u></b>	326	86.1	331	84.8	16	319	87.9	315	89.0	<b><u>317</u></b>	<b><u>88.3</u></b>
531.deepsjeng_r	16	<b><u>505</u></b>	<b><u>36.3</u></b>	503	36.5	506	36.2	16	<b><u>507</u></b>	<b><u>36.2</u></b>	508	36.1	506	36.2
541.leela_r	16	754	35.1	752	35.2	<b><u>752</u></b>	<b><u>35.2</u></b>	16	761	34.8	<b><u>758</u></b>	<b><u>34.9</u></b>	755	35.1
548.exchange2_r	16	<b><u>512</u></b>	<b><u>81.9</u></b>	509	82.3	513	81.7	16	514	81.6	512	81.8	<b><u>513</u></b>	<b><u>81.8</u></b>
557.xz_r	16	538	32.1	<b><u>566</u></b>	<b><u>30.5</u></b>	583	29.6	16	<b><u>588</u></b>	<b><u>29.4</u></b>	588	29.4	588	29.4

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2017.1.0/lib/ia32:/cpu2017.1.0/lib/intel64:/cpu2017.1.0/je5.0.1-32:/cpu2017.1.0/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32 GB RAM memory using Redhat Enterprise Linux 7.4

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

(Continued on next page)



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## General Notes (Continued)

```
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
```

```
jemalloc:
configured and built at default for 32bit (i686) and 64bit (x86_64) targets;
built with the RedHat Enterprise 7.4, and the system compiler gcc 4.8.5;
sources available via jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```

## Platform Notes

```
BIOS Settings:
Power Technology = Custom
Turbo Mode = Enable
Enhanced Halt State (C1E) = Disable
CPU C6 report = Disabled
Package C State = No limit
Software Controlled T-States = Disable
Hyper-Threading (All) = Enable
Enforce POR = Disable
Memory Frequency = Auto
Patrol Scrub = Disabled
IMC Interleaving = Auto
SNC = Disabled
```

```
Sysinfo program /cpu2017.1.0/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on SUT Wed Apr 11 13:47:27 2018
```

```
SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
https://www.spec.org/cpu2017/Docs/config.html#sysinfo
```

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores : 4
siblings : 8
physical 0: cores 1 2 4 5
physical 1: cores 1 2 4 5
```

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## Platform Notes (Continued)

From lscpu:

```

Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Byte Order:           Little Endian
CPU(s):               16
On-line CPU(s) list:  0-15
Thread(s) per core:   2
Core(s) per socket:   4
Socket(s):            2
NUMA node(s):        2
Vendor ID:            GenuineIntel
CPU family:           6
Model:                85
Model name:           Intel(R) Xeon(R) Silver 4112 CPU @ 2.60GHz
Stepping:             4
CPU MHz:              2601.000
CPU max MHz:          2601.0000
CPU min MHz:          800.0000
BogoMIPS:             5200.00
Virtualization:       VT-x
L1d cache:            32K
L1i cache:            32K
L2 cache:             1024K
L3 cache:             8448K
NUMA node0 CPU(s):   0-3,8-11
NUMA node1 CPU(s):   4-7,12-15
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc
aperfmpperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 fma
cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes
xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 invpcid_single
intel_pt spec_ctrl ibpb_support tpr_shadow vnmi flexpriority ept vpid fsgsbase
tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqm mpx rdt_a avx512f avx512dq
rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavec xgetbv1
cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local dtherm ida arat pln pts

```

```

/proc/cpuinfo cache data
cache size : 8448 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 8 9 10 11
node 0 size: 65196 MB
node 0 free: 63266 MB
node 1 cpus: 4 5 6 7 12 13 14 15

```

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## Platform Notes (Continued)

```
node 1 size: 65536 MB
node 1 free: 63746 MB
node distances:
node 0 1
  0: 10 21
  1: 21 10
```

From /proc/meminfo

```
MemTotal:      131460556 kB
HugePages_Total:      0
Hugepagesize:      2048 kB
```

From /etc/\*release\* /etc/\*version\*

os-release:

```
NAME="Red Hat Enterprise Linux Server"
VERSION="7.4 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VARIANT="Server"
VARIANT_ID="server"
VERSION_ID="7.4"
```

PRETTY\_NAME="Red Hat Enterprise Linux"

redhat-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.4 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.4:ga:server

uname -a:

```
Linux SUT 3.10.0-693.21.1.el7.x86_64 #1 SMP Fri Feb 23 18:54:16 UTC 2018 x86_64 x86_64
x86_64 GNU/Linux
```

run-level 3 Apr 11 13:44

SPEC is set to: /cpu2017.1.0

```
Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda1        ext4      825G      68G  716G   9% /
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 2.0b 03/06/2018

Memory:

8x Samsung M393A2G40EB2-CTD 16 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)



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## Compiler Version Notes

```
=====
CC 500.perlbench_r(base) 502.gcc_r(base) 505.mcf_r(base, peak)
    525.x264_r(base, peak) 557.xz_r(base, peak)
-----
```

```
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

```
=====
CC 500.perlbench_r(peak) 502.gcc_r(peak)
-----
```

```
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

```
=====
CXXC 520.omnetpp_r(base) 523.xalancbmk_r(base) 531.deepsjeng_r(base)
    541.leela_r(base)
-----
```

```
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

```
=====
CXXC 520.omnetpp_r(peak) 523.xalancbmk_r(peak) 531.deepsjeng_r(peak)
    541.leela_r(peak)
-----
```

```
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

```
=====
FC 548.exchange2_r(base, peak)
-----
```

```
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.
-----
```

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

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## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

C++ benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib -ljemalloc
```

Fortran benchmarks:

```
-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
-L/usr/local/je5.0.1-64/lib -ljemalloc
```

## Base Other Flags

C benchmarks:

```
-m64 -std=c11
```

C++ benchmarks:

```
-m64
```

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## Base Other Flags (Continued)

Fortran benchmarks:

-m64

## Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -D_FILE_OFFSET_BITS=64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -D_FILE_OFFSET_BITS=64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-fno-strict-overflow -L/usr/local/je5.0.1-64/lib
-ljemalloc
```

```
502.gcc_r: -L/opt/intel/compilers_and_libraries_2018/linux/lib/ia32
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3
-L/usr/local/je5.0.1-32/lib -ljemalloc
```

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## Peak Optimization Flags (Continued)

505.mcf\_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -L/usr/local/je5.0.1-64/lib  
-ljemalloc

525.x264\_r: -Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -fno-alias  
-L/usr/local/je5.0.1-64/lib -ljemalloc

557.xz\_r: Same as 505.mcf\_r

C++ benchmarks:

520.omnetpp\_r: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-L/usr/local/je5.0.1-64/lib -ljemalloc

523.xalancbmk\_r: -L/opt/intel/compilers\_and\_libraries\_2018/linux/lib/ia32  
-Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2) -ipo  
-xCORE-AVX512 -O3 -no-prec-div -qopt-mem-layout-trans=3  
-L/usr/local/je5.0.1-32/lib -ljemalloc

531.deepsjeng\_r: Same as 520.omnetpp\_r

541.leela\_r: Same as 520.omnetpp\_r

Fortran benchmarks:

-Wl,-z,muldefs -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte  
-L/usr/local/je5.0.1-64/lib -ljemalloc

## Peak Other Flags

C benchmarks (except as noted below):

-m64 -std=c11

502.gcc\_r: -m32 -std=c11

C++ benchmarks (except as noted below):

-m64

523.xalancbmk\_r: -m32

(Continued on next page)



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## Peak Other Flags (Continued)

Fortran benchmarks:

-m64

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/Epsilon-Platform-Flags-RevA-Mar-2018-For-Supermicro-Platform.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/Epsilon-Platform-Flags-RevA-Mar-2018-For-Supermicro-Platform.xml>

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Tested with SPEC CPU2017 v1.0.2 on 2018-04-11 07:47:26-0400.

Report generated on 2018-10-31 17:43:13 by CPU2017 PDF formatter v6067.

Originally published on 2018-05-01.