



# SPEC® CINT2006 Result

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## Fujitsu

PRIMEQUEST 3800B, Intel Xeon Platinum 8180, 2.50GHz

SPECint®\_rate2006 = 11800

SPECint\_rate\_base2006 = 11400

CPU2006 license: 19

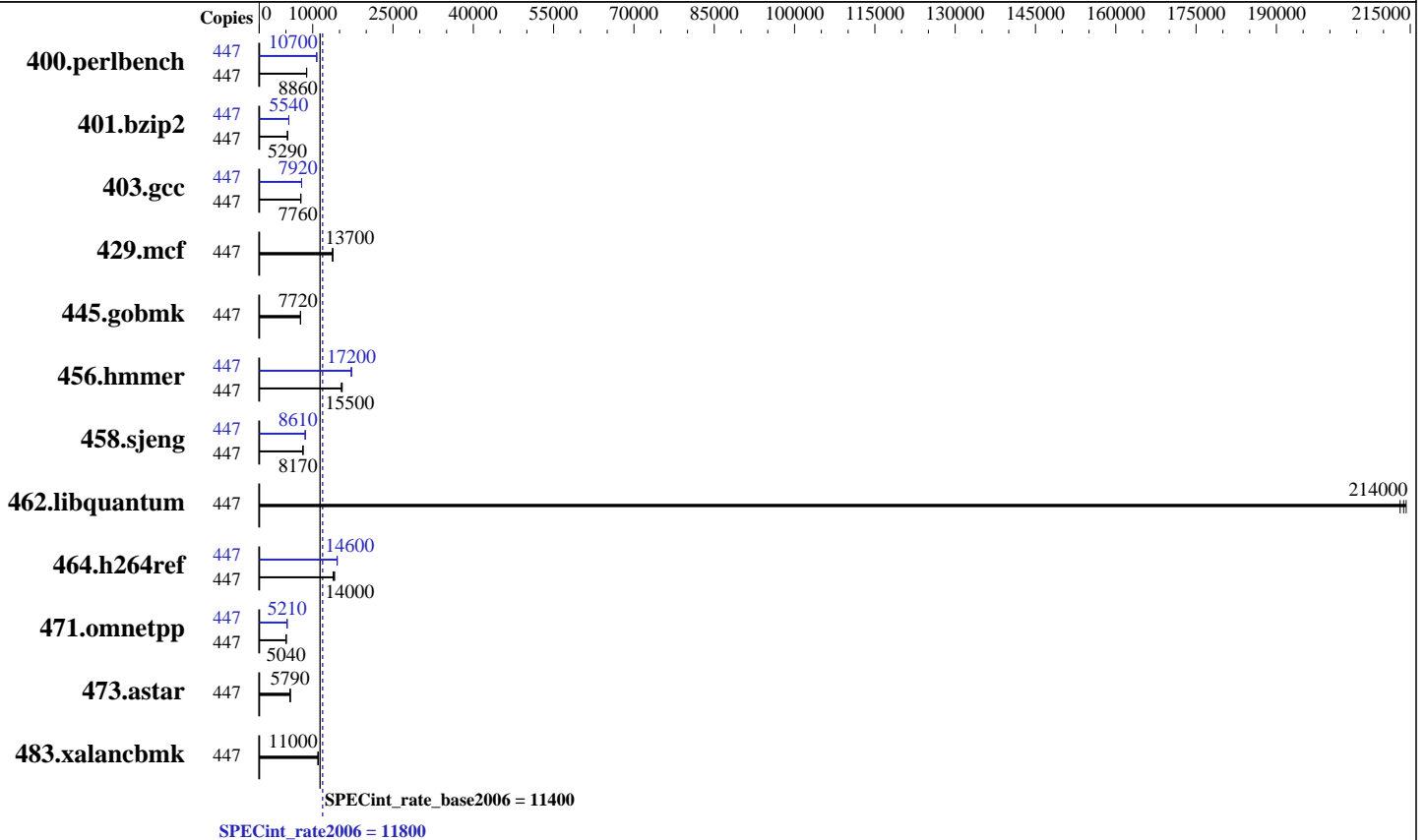
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2017

Hardware Availability: Jul-2017

Software Availability: Sep-2017



### Hardware

CPU Name: Intel Xeon Platinum 8180  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 224 cores, 8 chips, 28 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4,6,8 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 38.5 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 1536 GB (96 x 16 GB 2Rx4 PC4-2666V-R)  
 Disk Subsystem: 3020 GB tmpfs  
 Other Hardware: 1 x SAS HDD, 600 GB, 10.5K RPM, used for swap

### Software

Operating System: SUSE Linux Enterprise Server 12 SP2  
 4.4.21-69-default  
 Compiler: C/C++: Version 18.0.0.128 of Intel C/C++  
 Compiler for Linux  
 Auto Parallel: Yes  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	447	493	8860	<b><u>493</u></b>	<b><u>8860</u></b>	493	8860	447	407	10700	<b><u>407</u></b>	<b><u>10700</u></b>	407	10700
401.bzip2	447	814	5300	816	5290	<b><u>815</u></b>	<b><u>5290</u></b>	447	781	5520	<b><u>779</u></b>	<b><u>5540</u></b>	779	5540
403.gcc	447	465	7740	461	7800	<b><u>464</u></b>	<b><u>7760</u></b>	447	<b><u>454</u></b>	<b><u>7920</u></b>	454	7920	454	7920
429.mcf	447	298	13700	<b><u>297</u></b>	<b><u>13700</u></b>	297	13700	447	298	13700	<b><u>297</u></b>	<b><u>13700</u></b>	297	13700
445.gobmk	447	608	7720	607	7720	<b><u>607</u></b>	<b><u>7720</u></b>	447	608	7720	607	7720	<b><u>607</u></b>	<b><u>7720</u></b>
456.hammer	447	272	15300	<b><u>269</u></b>	<b><u>15500</u></b>	269	15500	447	242	17200	<b><u>242</u></b>	<b><u>17200</u></b>	242	17200
458.sjeng	447	662	8170	<b><u>662</u></b>	<b><u>8170</u></b>	662	8170	447	629	8600	<b><u>629</u></b>	<b><u>8610</u></b>	628	8610
462.libquantum	447	43.2	214000	<b><u>43.3</u></b>	<b><u>214000</u></b>	43.5	213000	447	43.2	214000	<b><u>43.3</u></b>	<b><u>214000</u></b>	43.5	213000
464.h264ref	447	702	14100	<b><u>709</u></b>	<b><u>14000</u></b>	716	13800	447	<b><u>680</u></b>	<b><u>14600</u></b>	680	14600	678	14600
471.omnetpp	447	556	5030	<b><u>555</u></b>	<b><u>5040</u></b>	554	5040	447	536	5210	536	5210	<b><u>536</u></b>	<b><u>5210</u></b>
473.astar	447	542	5790	<b><u>542</u></b>	<b><u>5790</u></b>	541	5800	447	542	5790	<b><u>542</u></b>	<b><u>5790</u></b>	541	5800
483.xalancbmk	447	280	11000	<b><u>280</u></b>	<b><u>11000</u></b>	281	11000	447	280	11000	<b><u>280</u></b>	<b><u>11000</u></b>	281	11000

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with : nohz_full=1-447 isolcpus=1-447
Turbo mode set with:
cpupower -c all frequency-set -g performance
Tmpfs filesystem can be set with:
mkdir /home/memory
mount -t tmpfs -o size=3020g,rw tmpfs /home/memory
Process tuning setting:
echo 1000000 > /proc/sys/kernel/sched_min_granularity_ns
echo 1500000 > /proc/sys/kernel/sched_wakeup_granularity_ns
echo 1 > /proc/sys/kernel/numa_balancing
echo always > /sys/kernel/mm/transparent_hugepage/enabled
cpu idle state set with:
cpupower idle-set -d 2
cpupower idle-set -d 3
set affinity of rcu threads to the cpu0:
for i in `pgrep rcu` ; do taskset -pc 0 $i ; done
```



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### Platform Notes

BIOS configuration:  
Intel Virtualization Technology = Disabled  
HWPM Support = Disabled  
DCU Streamer Prefetcher = Disabled  
Stale AtoS = Enabled  
LLC dead line alloc = Disabled  
Sub NUMA Clustering = Enabled  
Fan Control = Full  
Sysinfo program /home/memory/speccpu/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-k55j Tue Sep 19 01:17:36 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8180 CPU @ 2.50GHz
 8 "physical id"s (chips)
448 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 28
siblings : 56
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 4: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 5: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 6: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
physical 7: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
25 26 27 28 29 30
cache size : 39424 KB
```

```
From /proc/meminfo
MemTotal: 1583801256 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
```

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### Platform Notes (Continued)

```

PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
# release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
Linux linux-k55j 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 19 01:07

SPEC is set to: /home/memory/speccpu
Filesystem      Type      Size  Used Avail Use% Mounted on
tmpfs           tmpfs    3.0T  9.7G  3.0T   1% /home/memory
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU V1.0.0.0 R1.21.0 for D3858-A1x                09/15/2017
Memory:
48x Hynix HMA42GR7BJR4N-VK 16 GB 2 rank 2666 MHz
48x Samsung M393A2G40EB2-CTD 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

```

### General Notes

```

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/memory/speccpu/icc2018lib/ia32:/home/memory/speccpu/icc2018lib/intel64:/home/memory/speccpu/sh10.2"
Binaries compiled on a system with 2x Intel Xeon Platinum 8180 CPU + 384GB RAM
memory using SUSE Linux Enterprise Server 12 SP2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

```



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## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2018.0.128/linux/compiler/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2018.0.128/linux/compiler/lib/ia32

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2018.0.128/linux/compiler/lib/ia32

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## Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32 -L/opt/intel/compilers_and_libraries_2018.0.128/linux/compiler/lib/ia32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-D_FILE_OFFSET_BITS=64`

429.mcf: `-D_FILE_OFFSET_BITS=64`

445.gobmk: `-D_FILE_OFFSET_BITS=64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

464.h264ref: `-D_FILE_OFFSET_BITS=64`

471.omnetpp: `-D_FILE_OFFSET_BITS=64`

473.astar: `-D_FILE_OFFSET_BITS=64`

483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3`

403.gcc: `-xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

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## Peak Optimization Flags (Continued)

456.hmmr: -xCORE-AVX512 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto-ilp32  
-qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2)  
-qopt-ra-region-strategy=block  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs  
-L/sh10.2 -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-SKL-RevB.xml>



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