



SPEC® CINT2006 Result

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Supermicro

(Test Sponsor: Advanced Micro Devices)

Supermicro AS-1123US-TR4,
AMD EPYC 7601

SPECint®_rate2006 = 2360

SPECint_rate_base2006 = 2100

CPU2006 license: 49

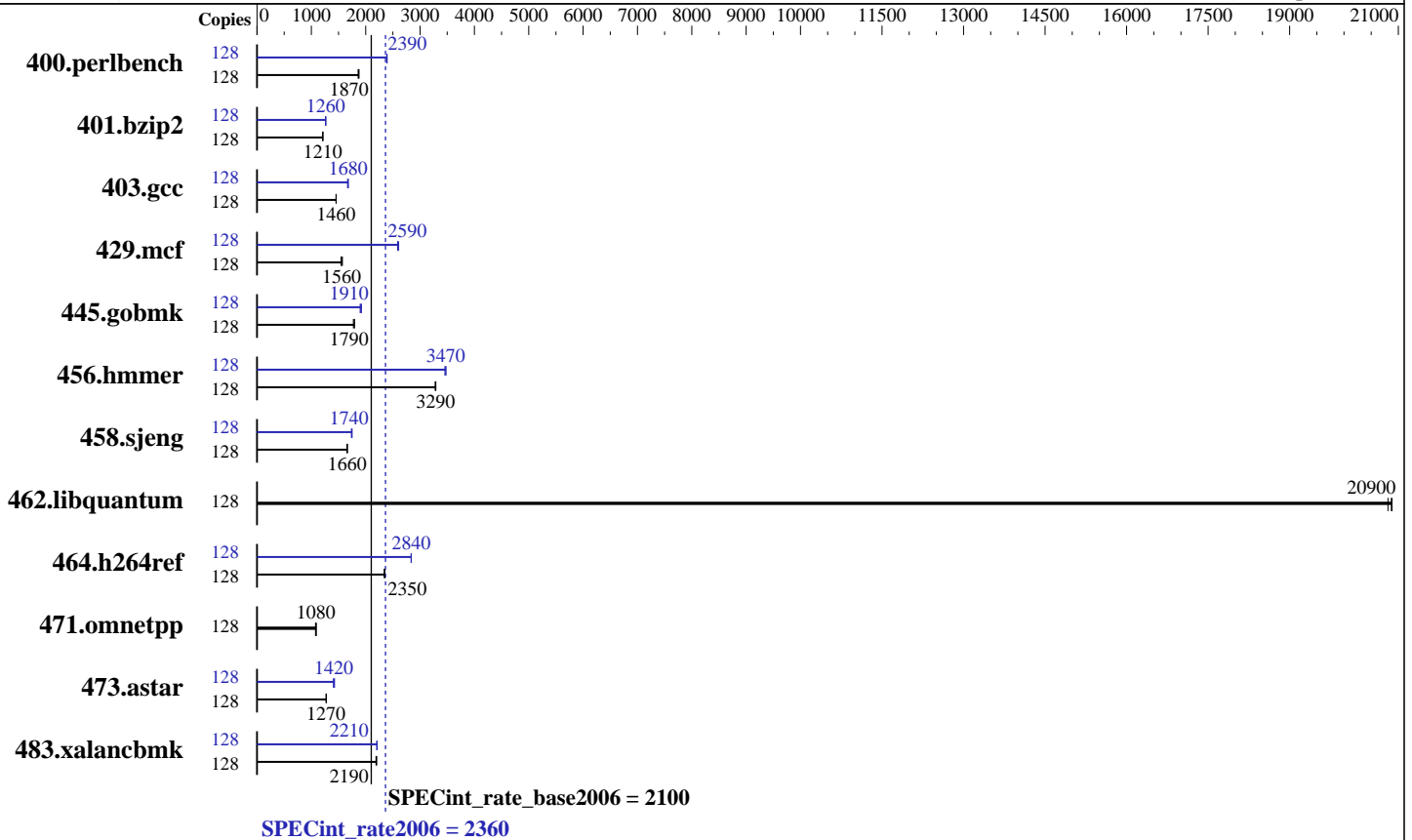
Test sponsor: Advanced Micro Devices

Tested by: Advanced Micro Devices

Test date: May-2017

Hardware Availability: Jun-2017

Software Availability: Apr-2016



Hardware

CPU Name: AMD EPYC 7601
 CPU Characteristics: AMD Turbo CORE technology up to 3.20 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 64 cores, 2 chips, 32 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 64 KB I + 32 KB D on chip per core
 Secondary Cache: 512 KB I+D on chip per core
 L3 Cache: 64 MB I+D on chip per chip, 8 MB shared / 4 cores
 Other Cache: None
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2667V-R, running at 2400)
 Disk Subsystem: 1 x 500 GB SSD
 Other Hardware: None

Software

Operating System: Ubuntu 16.04 LTS,
Kernel 4.4.0-71.generic
 Compiler: C/C++: Version 4.5.2.1 of x86 Open64 Compiler Suite (from AMD)
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (Full multiuser with network)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap 10.0 32-bit Library for Linux



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	128	666	1880	671	1860	670	1870	128	522	2390	524	2390	523	2390
401.bzip2	128	1019	1210	1024	1210	1018	1210	128	977	1260	977	1260	979	1260
403.gcc	128	707	1460	709	1450	707	1460	128	618	1670	614	1680	615	1680
429.mcf	128	748	1560	755	1550	743	1570	128	448	2610	451	2590	451	2590
445.gobmk	128	751	1790	749	1790	757	1770	128	707	1900	703	1910	699	1920
456.hammer	128	364	3280	363	3290	363	3290	128	344	3480	345	3460	344	3470
458.sjeng	128	931	1660	934	1660	936	1660	128	891	1740	889	1740	885	1750
462.libquantum	128	127	20800	127	20900	127	20900	128	127	20800	127	20900	127	20900
464.h264ref	128	1210	2340	1206	2350	1205	2350	128	998	2840	998	2840	999	2840
471.omnetpp	128	734	1090	738	1080	743	1080	128	734	1090	738	1080	743	1080
473.astar	128	705	1270	706	1270	706	1270	128	639	1410	630	1430	634	1420
483.xalancbmk	128	401	2200	403	2190	402	2190	128	400	2210	401	2200	400	2210

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary
Set zone_reclaim_mode=1 to free local node memory and avoid remote memory
sync then drop_caches=3 to reset caches before invoking runspec
Transparent huge pages were enabled for this run (OS default)

Platform Notes

The Linux run level was 3; sysinfo run-level is incorrect.
The dmidecode memory speed information is incorrect.

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/work/cpu2006/amd1603-rate-libs-revA/32:/root/work/cpu2006/amd1603-rate-libs-revA/64"
The binaries were built with the AMD supported x86 Open64 Compiler Suite,

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General Notes (Continued)

which is only available from AMD at
<http://developer.amd.com/tools-and-sdks/cpu-development/x86-open64-compiler-suite/>
Binaries were compiled on a system with 2x AMD Opteron 6378 chips + 128GB Memory using RHEL 6.3
Submitted_by: "Smith, Van" <Van.Smith@amd.com>
Submitted: Mon Jun 5 12:36:49 EDT 2017
Submission: cpu2006-20170529-47127.sub
Submitted_by: "Smith, Van" <Van.Smith@amd.com>
Submitted: Thu Jun 8 15:43:36 EDT 2017
Submission: cpu2006-20170529-47127.sub

Base Compiler Invocation

C benchmarks:

opencc

C++ benchmarks:

openCC

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-Ofast -CG:local_sched_alg=1 -INLINE:aggressive=ON -IPA:plimit=8000
-IPA:small_pu=100 -HP:bd=2m:heap=2m -mso -LNO:prefetch=2
-march=bdver1 -mno-fma4 -mno-xop -mno-tbm

C++ benchmarks:

-Ofast -m32 -INLINE:aggressive=on -CG:cmp_peep=on -D__OPEN64_FAST_SET
-march=bdver1 -mno-fma4 -mno-xop -mno-tbm
-L/root/work/libraries/SmartHeap-10/lib -lsmartheap



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Peak Compiler Invocation

C benchmarks:
opencc

C++ benchmarks:
openCC

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -LNO:opt=0 -IPA:plimit=20000
-OPT:unroll_times_max=8 -OPT:unroll_size=256
-OPT:unroll_level=2 -OPT:keep_ext=on -WOPT:if_conv=0
-WOPT:sib=on -CG:local_sched_alg=1 -CG:unroll_fb_req=on
-CG:movext_icmp=off -HP:bd=2m:heap=2m -march=bdver1
-mno-fma4 -GRA:aggr_loop_splitting=off
-GRA:loop_splitting=off

401.bzip2: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -LNO:pf2=0 -OPT:alias=disjoint
-OPT:goto=off -CG:local_sched_alg=1 -HP:bd=2m:heap=2m
-march=bdver2 -WB, -mno-fma4 -mno-tbm -mno-xop

403.gcc: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:trip_count=256 -CG:cmp_peep=on -CG:pre_minreg_level=2
-m32 -HP:bd=2m:heap=2m -GRA:unspill=on -IPA:small_pu=200
-WOPT:sib=on -march=bdver2 -mno-fma4 -WB, -mno-tbm
-mno-xop

429.mcf: -O3 -OPT:unroll_times_max=5 -ipa -INLINE:aggressive=on
-CG:gcm=off -CG:dsched=on -GRA:prioritize_by_density=on
-m32 -HP:bd=2m:heap=2m -mso -march=bdver1 -mno-fma4
```

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Peak Optimization Flags (Continued)

445.gobmk: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-OPT:unroll_size=256 -OPT:unroll_times_max=8
-OPT:keep_ext=on -IPA:plimit=750 -IPA:min_hotness=300
-IPA:pu_reorder=1 -LNO:ignore_feedback=off -WOPT:if_conv=2
-HP:bd=2m:heap=2m -march=bdver1 -mno-fma4

456.hmmr: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-LNO:prefetch=2 -OPT:alias=disjoint
-OPT:unroll_times_max=16 -OPT:unroll_size=512
-OPT:unroll_level=2 -OPT:keep_ext=on -CG:cflow=0
-CG:cmp_peep=on -CG:pre_local_sched=off -HP:bd=2m:heap=2m
-CG:p2align=0 -CG:load_exe=3 -CG:dsched=on -march=bdver1
-mno-fma4

458.sjeng: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-CG:ptr_load_use=0 -CG:divrem_opt=on -CG:movext_icmp=off
-CG:locs_best=on -LNO:full_unroll=10 -IPA:pu_reorder=2
-HP:heap=2m:bd=2m -WOPT:sib=on -march=bdver1 -mno-fma4

462.libquantum: basepeak = yes

464.h264ref: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -O3
-OPT:unroll_size=256 -OPT:unroll_times_max=2
-IPA:plimit=20000 -OPT:alias=disjoint -CG:ptr_load_use=0
-CG:local_sched_alg=1 -HP:bd=2m:heap=2m -march=bdver1
-mno-fma4

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -fb_create fbdata(pass 1) -fb_opt fbdata(pass 2) -Ofast
-WOPT:if_conv=0 -WOPT:sib=on -CG:divrem_opt=on
-CG:p2align=1 -CG:dsched=on -GRA:optimize_boundary=on
-OPT:alias=disjoint -INLINE:aggressive=on
-IPA:small_pu=3000 -IPA:plimit=3000 -HP:bd=2m:heap=2m
-march=bdver1 -mno-fma4

483.xalancbmk: -Ofast -LNO:prefetch=2 -OPT:unroll_size=512
-OPT:unroll_times_max=8 -D__OPEN64_FAST_SET
-INLINE:aggressive=on -m32 -CG:cmp_peep=on
-CG:local_sched=off -CG:p2align=1 -GRA:unspill=on
-TENV:frame_pointer=off -fno-emit-exceptions -march=bdver2
-mno-fma4
-L/root/work/libraries/SmartHeap-10/lib -lsmarheap

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/x86-openflags-rate-revA-I.xml>

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Tested with SPEC CPU2006 v1.2.
Report generated on Tue Sep 5 18:32:07 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 June 2017.