



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 3430

PowerEdge R930 (Intel Xeon E7-8880 v4, 2.20 GHz)

SPECint\_rate\_base2006 = 3310

CPU2006 license: 55

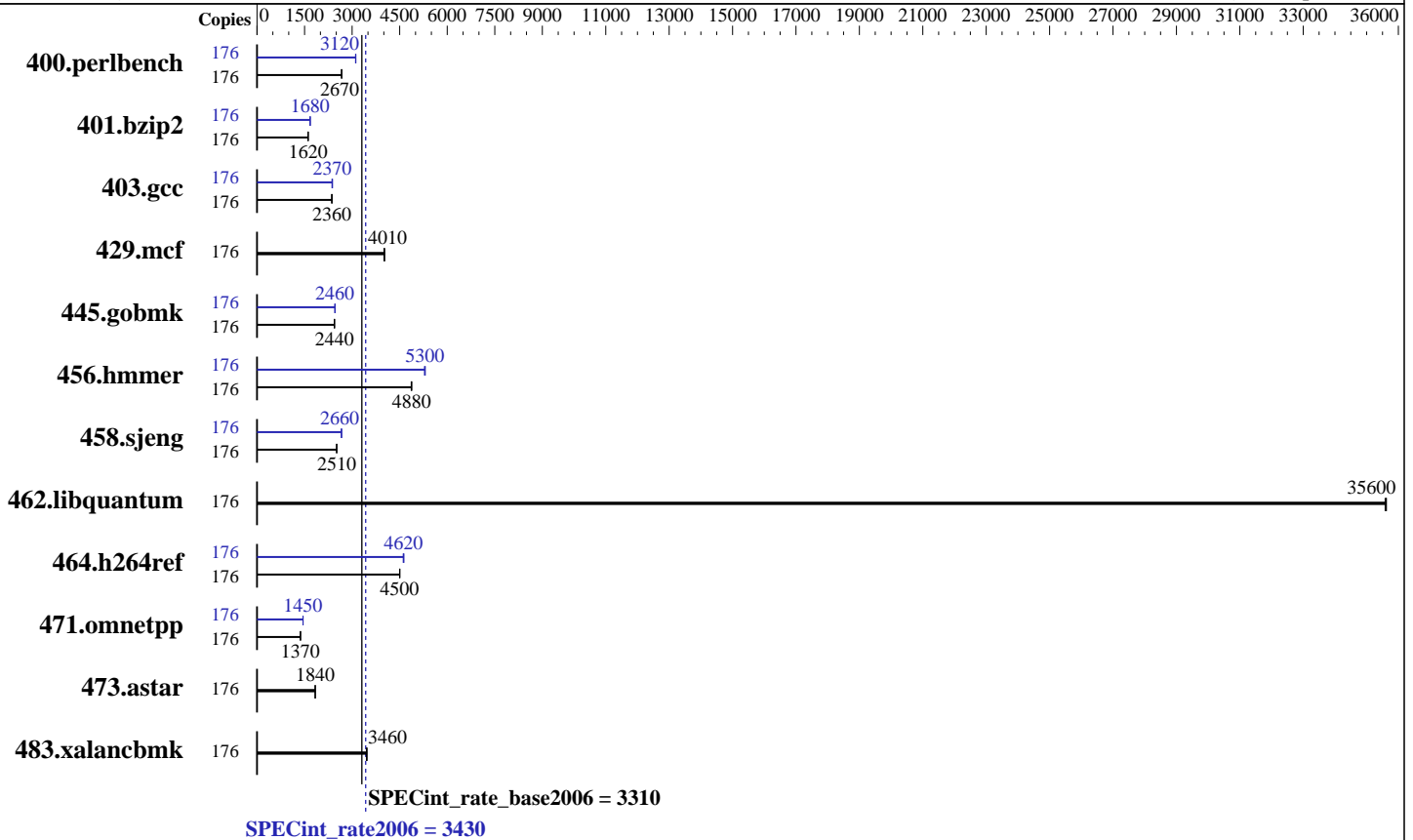
Test date: Nov-2016

Test sponsor: Dell Inc.

Hardware Availability: Oct-2016

Tested by: Dell Inc.

Software Availability: Sep-2016



## Hardware

CPU Name: Intel Xeon E7-8880 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 88 cores, 4 chips, 22 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 55 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133T-R, running at 1600 MHz)  
 Disk Subsystem: 1 x 480 GB SATA SSD  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

| Benchmark      | Base   |            |              |             |             |         |       | Peak   |            |              |             |             |            |             |
|----------------|--------|------------|--------------|-------------|-------------|---------|-------|--------|------------|--------------|-------------|-------------|------------|-------------|
|                | Copies | Seconds    | Ratio        | Seconds     | Ratio       | Seconds | Ratio | Copies | Seconds    | Ratio        | Seconds     | Ratio       | Seconds    | Ratio       |
| 400.perlbench  | 176    | <b>644</b> | <b>2670</b>  | 644         | 2670        | 645     | 2670  | 176    | 553        | 3110         | <b>552</b>  | <b>3120</b> | 552        | 3120        |
| 401.bzip2      | 176    | 1050       | 1620         | <b>1051</b> | <b>1620</b> | 1059    | 1600  | 176    | 1013       | 1680         | <b>1013</b> | <b>1680</b> | 1014       | 1670        |
| 403.gcc        | 176    | <b>599</b> | <b>2360</b>  | 598         | 2370        | 602     | 2350  | 176    | 595        | 2380         | <b>598</b>  | <b>2370</b> | 599        | 2370        |
| 429.mcf        | 176    | 398        | 4030         | <b>400</b>  | <b>4010</b> | 401     | 4010  | 176    | 398        | 4030         | <b>400</b>  | <b>4010</b> | 401        | 4010        |
| 445.gobmk      | 176    | <b>756</b> | <b>2440</b>  | 755         | 2440        | 756     | 2440  | 176    | 751        | 2460         | 750         | 2460        | <b>751</b> | <b>2460</b> |
| 456.hammer     | 176    | 338        | 4860         | <b>337</b>  | <b>4880</b> | 336     | 4880  | 176    | 311        | 5280         | <b>310</b>  | <b>5300</b> | 310        | 5310        |
| 458.sjeng      | 176    | <b>847</b> | <b>2510</b>  | 848         | 2510        | 847     | 2510  | 176    | <b>799</b> | <b>2660</b>  | 799         | 2660        | 799        | 2660        |
| 462.libquantum | 176    | <b>102</b> | <b>35600</b> | 102         | 35600       | 102     | 35600 | 176    | <b>102</b> | <b>35600</b> | 102         | 35600       | 102        | 35600       |
| 464.h264ref    | 176    | <b>865</b> | <b>4500</b>  | 864         | 4510        | 866     | 4500  | 176    | 841        | 4630         | <b>842</b>  | <b>4620</b> | 845        | 4610        |
| 471.omnetpp    | 176    | 802        | 1370         | <b>803</b>  | <b>1370</b> | 803     | 1370  | 176    | 757        | 1450         | 759         | 1450        | <b>757</b> | <b>1450</b> |
| 473.astar      | 176    | 672        | 1840         | <b>673</b>  | <b>1840</b> | 674     | 1830  | 176    | 672        | 1840         | <b>673</b>  | <b>1840</b> | 674        | 1830        |
| 483.xalancbmk  | 176    | <b>351</b> | <b>3460</b>  | 352         | 3450        | 350     | 3470  | 176    | <b>351</b> | <b>3460</b>  | 352         | 3450        | 350        | 3470        |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
 Snoop Mode set to Cluster on Die  
 Virtualization Technology disabled  
 System Profile set to Custom  
 CPU Performance set to Maximum Performance  
 C States set to disabled  
 C1E disabled  
 Energy Efficient Turbo enabled  
 Uncore Frequency set to Dynamic  
 Energy Efficiency Policy set to Balanced Performance  
 Memory Patrol Scrub disabled  
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on linux-lbo5 Sun Nov 20 00:09:06 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

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## Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E7-8880 v4 @ 2.20GHz

4 "physical id"s (chips)

176 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 22

siblings : 44

physical 0: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

physical 1: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

physical 2: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

physical 3: cores 0 1 2 3 4 5 8 9 10 11 12 16 17 18 19 20 21 24 25 26 27 28

cache size : 28160 KB

From /proc/meminfo

MemTotal: 529059184 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

SUSE Linux Enterprise Server 12 SP1

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 1

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP1"

VERSION\_ID="12.1"

PRETTY\_NAME="SUSE Linux Enterprise Server 12 SP1"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12:sp1"

uname -a:

Linux linux-lbo5 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015

(8d714a0) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Nov 18 22:15

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## Platform Notes (Continued)

SPEC is set to: /root/cpu2006-1.2

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|------------|------|------|------|-------|------|------------|
| /dev/sda2  | xfs  | 439G | 29G  | 411G  | 7%   | /          |

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.2.0 09/29/2016

Memory:

32x 00AD00B300AD HMA42GR7MFR4N-TF 16 GB 2 rank 2133 MHz, configured at 1600 MHz

64x Not Specified Not Specified

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -D\_FILE\_OFFSET\_BITS=64

403.gcc: -D\_FILE\_OFFSET\_BITS=64

429.mcf: -D\_FILE\_OFFSET\_BITS=64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

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## Base Portability Flags (Continued)

```

456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

```

C++ benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

```

## Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

## Peak Compiler Invocation

C benchmarks (except as noted below):

```

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

```

```

400.perlbench: icc -m64

```

```

401.bzip2: icc -m64

```

```

456.hmmer: icc -m64

```

```

458.sjeng: icc -m64

```

C++ benchmarks:

```

icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

```



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -D\_FILE\_OFFSET\_BITS=64  
 429.mcf: -D\_FILE\_OFFSET\_BITS=64  
 445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
 471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
 473.astar: -D\_FILE\_OFFSET\_BITS=64  
 483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -qopt-prefetch -auto-ilp32  
 -qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div  
 -qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -qopt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -unroll4 -auto-ilp32  
 -qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
             -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2)
             -qopt-ra-region-strategy=block
             -qopt-mem-layout-trans=3 -Wl,-z,muldefs
             -L/sh10.2 -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

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