



# SPEC® CINT2006 Result

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## Fujitsu

PRIMERGY RX4770 M2, Intel Xeon E7-8891 v3, 2.80 GHz

SPECint®\_rate2006 = 1950

SPECint\_rate\_base2006 = 1890

CPU2006 license: 19

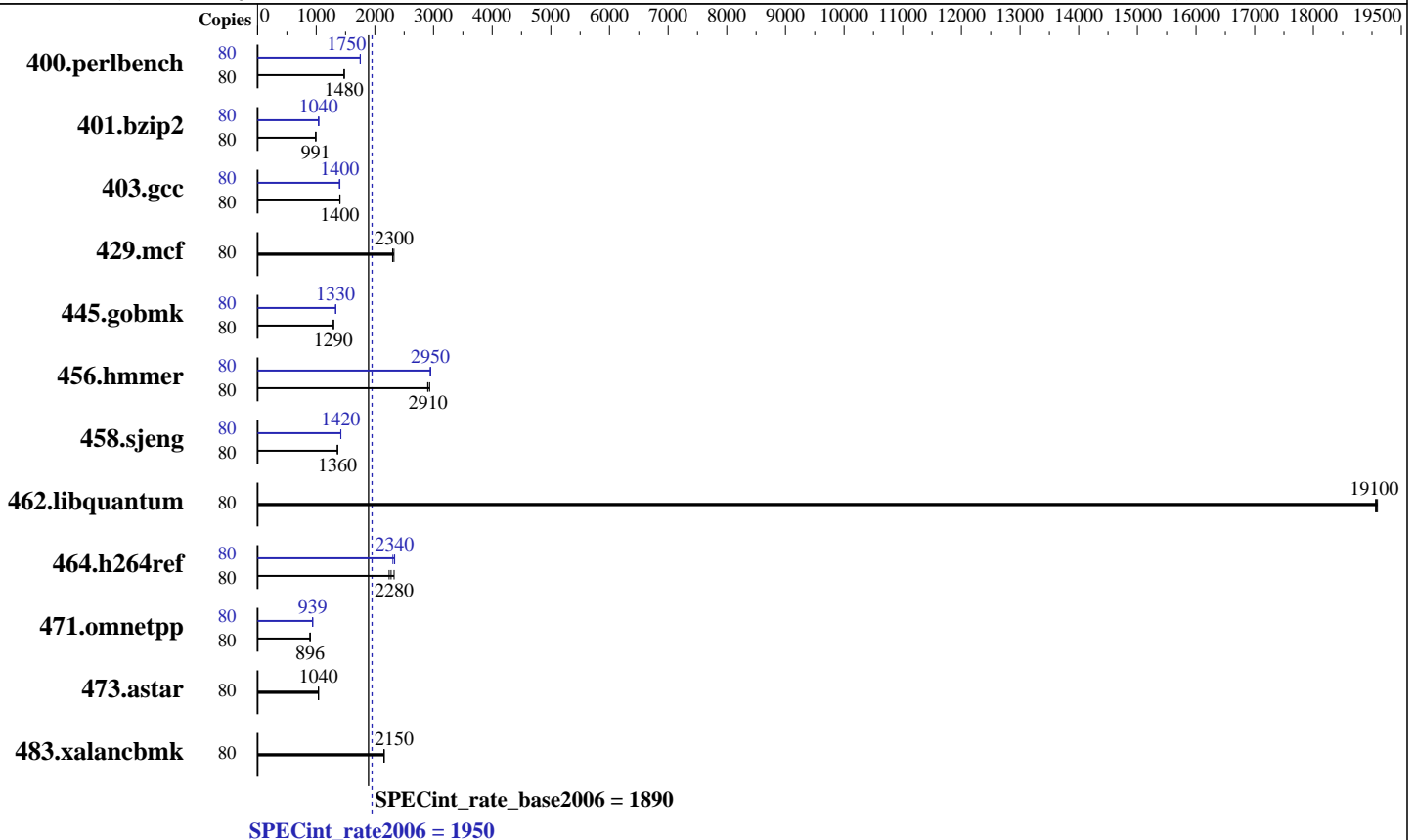
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2015

Hardware Availability: May-2015

Software Availability: Oct-2014



### Hardware

CPU Name: Intel Xeon E7-8891 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 45 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
 Disk Subsystem: 1 x SATA, 600 GB, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.6 (Santiago)  
 2.6.32-504.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	80	530	1480	529	1480	<u>529</u>	<u>1480</u>	80	<u>447</u>	<u>1750</u>	447	1750	446	1750
401.bzip2	80	776	995	779	991	<u>779</u>	<u>991</u>	80	<u>742</u>	<u>1040</u>	741	1040	742	1040
403.gcc	80	458	1410	460	1400	<u>459</u>	<u>1400</u>	80	459	1400	462	1390	<u>459</u>	<u>1400</u>
429.mcf	80	314	2330	<u>317</u>	<u>2300</u>	317	2300	80	314	2330	<u>317</u>	<u>2300</u>	317	2300
445.gobmk	80	648	1300	<u>649</u>	<u>1290</u>	649	1290	80	<u>630</u>	<u>1330</u>	630	1330	630	1330
456.hammer	80	257	2900	<u>257</u>	<u>2910</u>	254	2930	80	254	2940	<u>253</u>	<u>2950</u>	253	2950
458.sjeng	80	<u>710</u>	<u>1360</u>	711	1360	710	1360	80	681	1420	<u>682</u>	<u>1420</u>	682	1420
462.libquantum	80	87.0	19100	86.8	19100	<u>86.9</u>	<u>19100</u>	80	87.0	19100	86.8	19100	<u>86.9</u>	<u>19100</u>
464.h264ref	80	789	2240	761	2330	<u>778</u>	<u>2280</u>	80	767	2310	<u>758</u>	<u>2340</u>	758	2340
471.omnetpp	80	<u>558</u>	<u>896</u>	556	899	560	893	80	534	937	532	940	<u>532</u>	<u>939</u>
473.astar	80	<u>541</u>	<u>1040</u>	540	1040	542	1040	80	<u>541</u>	<u>1040</u>	540	1040	542	1040
483.xalancbmk	80	255	2160	256	2150	<u>256</u>	<u>2150</u>	80	255	2160	256	2150	<u>256</u>	<u>2150</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Energy Performance = Performance

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

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## General Notes (Continued)

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3  
  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
  
400.perlbench: icc -m64  
  
401.bzip2: icc -m64

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## Peak Compiler Invocation (Continued)

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:  
`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`  
401.bzip2: `-DSPEC_CPU_LP64`  
456.hmmer: `-DSPEC_CPU_LP64`  
458.sjeng: `-DSPEC_CPU_LP64`  
462.libquantum: `-DSPEC_CPU_LINUX`  
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

464.h264ref: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2 -ansi-alias`

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## Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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