



SPEC® CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint®_rate2006 = 472

PRIMERGY TX2560 M1, Intel Xeon E5-2637 v3, 3.5 GHz

SPECint_rate_base2006 = 455

CPU2006 license: 19

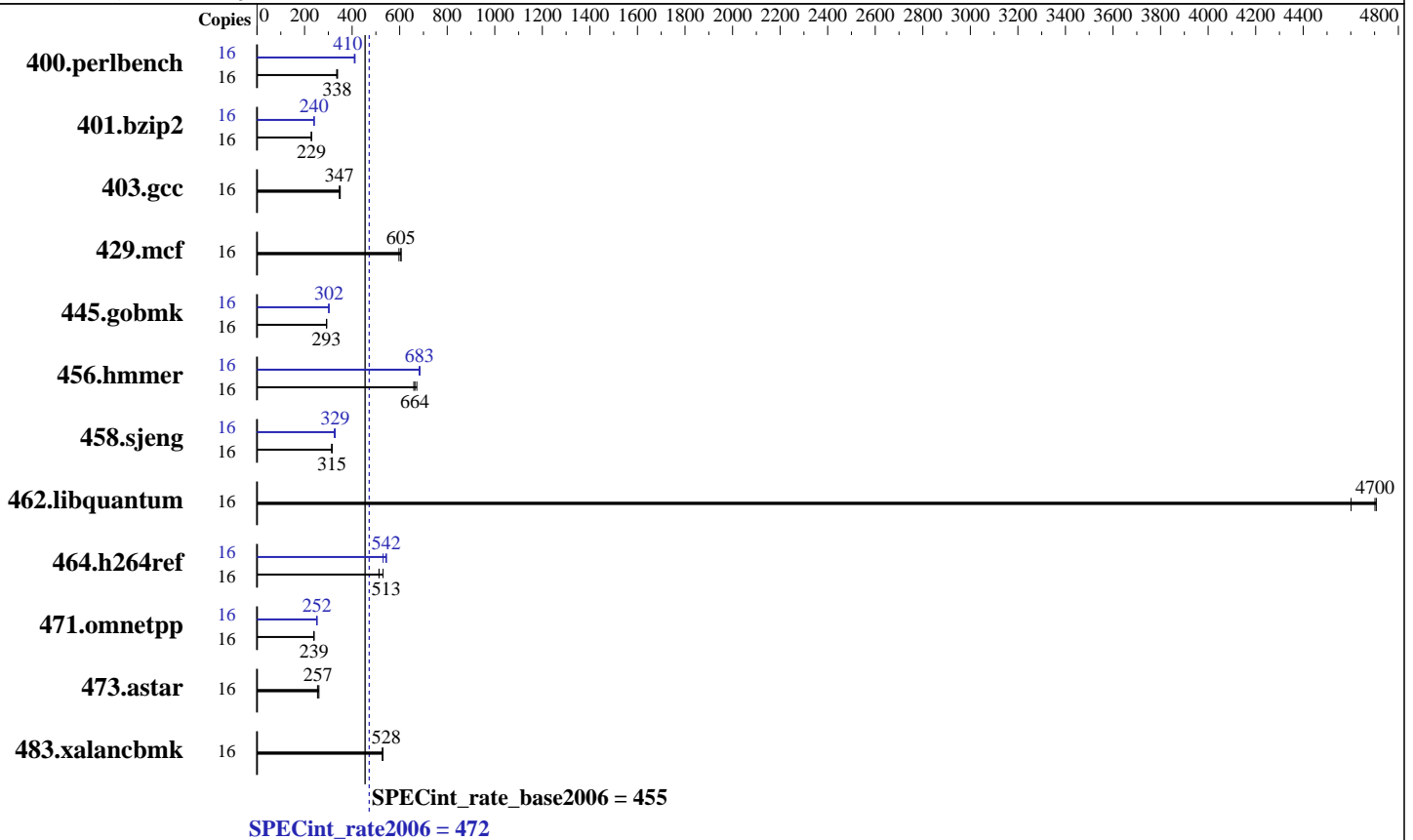
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2015

Hardware Availability: Apr-2015

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2637 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.6 (Santiago)
 2.6.32-504.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: None



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 472

PRIMERGY TX2560 M1, Intel Xeon E5-2637 v3, 3.5 GHz

SPECint_rate_base2006 = 455

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2015
Hardware Availability: Apr-2015
Software Availability: Nov-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>463</u>	<u>338</u>	467	335	462	338	16	379	412	<u>381</u>	<u>410</u>	381	410
401.bzip2	16	676	228	<u>675</u>	<u>229</u>	673	229	16	643	240	<u>643</u>	<u>240</u>	643	240
403.gcc	16	368	350	371	347	<u>371</u>	<u>347</u>	16	368	350	371	347	<u>371</u>	<u>347</u>
429.mcf	16	241	606	244	597	<u>241</u>	<u>605</u>	16	241	606	244	597	<u>241</u>	<u>605</u>
445.gobmk	16	<u>574</u>	<u>293</u>	574	292	574	293	16	557	301	554	303	<u>555</u>	<u>302</u>
456.hammer	16	<u>225</u>	<u>664</u>	222	672	226	659	16	219	682	218	685	<u>218</u>	<u>683</u>
458.sjeng	16	612	316	617	314	<u>614</u>	<u>315</u>	16	596	325	589	329	<u>589</u>	<u>329</u>
462.libquantum	16	72.0	4600	<u>70.5</u>	<u>4700</u>	70.4	4710	16	72.0	4600	<u>70.5</u>	<u>4700</u>	70.4	4710
464.h264ref	16	690	513	668	530	<u>690</u>	<u>513</u>	16	<u>653</u>	<u>542</u>	650	545	668	530
471.omnetpp	16	417	240	<u>418</u>	<u>239</u>	418	239	16	398	251	<u>397</u>	<u>252</u>	397	252
473.astar	16	<u>438</u>	<u>257</u>	431	260	441	254	16	<u>438</u>	<u>257</u>	431	260	441	254
483.xalancbmk	16	209	529	<u>209</u>	<u>528</u>	209	527	16	209	529	<u>209</u>	<u>528</u>	209	527

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Energy Performance = Performance
Utilization Profile = Unbalanced
QPI snoop mode: Early Snoop
COD Enable = Disabled, Early Snoop = Enabled
CPU C1E Support = Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 472

PRIMERGY TX2560 M1, Intel Xeon E5-2637 v3, 3.5 GHz

SPECint_rate_base2006 = 455

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2015

Hardware Availability: Apr-2015

Software Availability: Nov-2013

General Notes (Continued)

```
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>
```

This result was measured on the PRIMERGY RX2560 M1. The PRIMERGY RX2560 M1 and the PRIMERGY TX2560 M1 are electronically equivalent.
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32  
462.libquantum: -DSPEC_CPU_LINUX  
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmarheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 472

PRIMERGY TX2560 M1, Intel Xeon E5-2637 v3, 3.5 GHz

SPECint_rate_base2006 = 455

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2015
Hardware Availability: Apr-2015
Software Availability: Nov-2013

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2015 Standard Performance Evaluation Corporation

Fujitsu

SPECint_rate2006 = 472

PRIMERGY TX2560 M1, Intel Xeon E5-2637 v3, 3.5 GHz

SPECint_rate_base2006 = 455

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2015

Hardware Availability: Apr-2015

Software Availability: Nov-2013

Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Jun 2 13:48:14 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 2 June 2015.