



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 1240

PowerEdge M830 (Intel Xeon E5-4655 v3, 2.90 GHz)

SPECint\_rate\_base2006 = 1190

CPU2006 license: 55

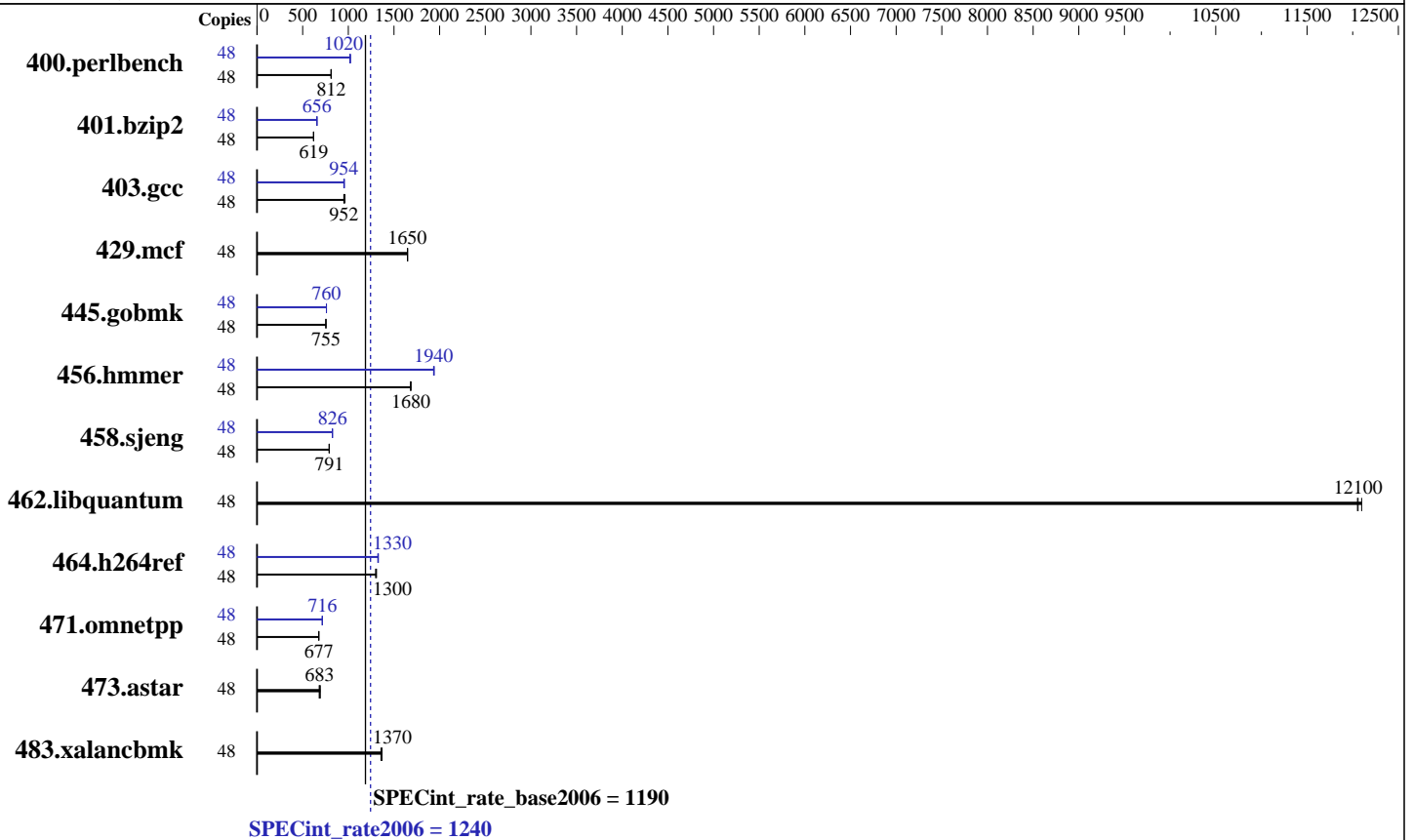
Test date: Feb-2015

Test sponsor: Dell Inc.

Hardware Availability: Jun-2015

Tested by: Dell Inc.

Software Availability: Jun-2015



## Hardware

CPU Name: Intel Xeon E5-4655 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2900  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 4 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (32 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 400 GB 7200 RPM SATA  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12  
 3.12.28-4-default  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	577	813	579	810	<u>577</u>	<u>812</u>	48	461	1020	457	1030	<u>461</u>	<u>1020</u>
401.bzip2	48	<u>748</u>	<u>619</u>	748	619	747	620	48	706	656	<u>706</u>	<u>656</u>	706	656
403.gcc	48	<u>406</u>	<u>952</u>	406	951	401	963	48	<u>405</u>	<u>954</u>	404	957	406	951
429.mcf	48	265	1650	<u>266</u>	<u>1650</u>	266	1650	48	265	1650	<u>266</u>	<u>1650</u>	266	1650
445.gobmk	48	667	755	<u>667</u>	<u>755</u>	668	754	48	<u>662</u>	<u>760</u>	663	760	662	761
456.hammer	48	267	1680	266	1690	<u>266</u>	<u>1680</u>	48	232	1930	231	1940	<u>231</u>	<u>1940</u>
458.sjeng	48	735	791	<u>735</u>	<u>791</u>	734	791	48	<u>703</u>	<u>826</u>	703	826	703	826
462.libquantum	48	<u>82.5</u>	<u>12100</u>	82.2	12100	82.5	12100	48	<u>82.5</u>	<u>12100</u>	82.2	12100	82.5	12100
464.h264ref	48	817	1300	<u>816</u>	<u>1300</u>	811	1310	48	800	1330	<u>800</u>	<u>1330</u>	803	1320
471.omnetpp	48	443	677	<u>443</u>	<u>677</u>	444	676	48	420	714	419	716	<u>419</u>	<u>716</u>
473.astar	48	486	694	494	682	<u>493</u>	<u>683</u>	48	486	694	494	682	<u>493</u>	<u>683</u>
483.xalancbmk	48	<u>243</u>	<u>1370</u>	242	1370	244	1360	48	<u>243</u>	<u>1370</u>	242	1370	244	1360

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
Virtualization Technology disabled  
Execute Disable disabled  
System Profile set to Performance  
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on linux-71ly Fri Feb 6 09:17:27 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-4655 v3 @ 2.90GHz  
4 "physical id"s (chips)  
48 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The  
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## Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```

cpu cores : 6
siblings  : 12
physical 0: cores 1 3 5 9 11 12
physical 1: cores 1 3 5 9 11 12
physical 2: cores 1 3 5 9 11 12
physical 3: cores 1 3 5 9 11 12
cache size : 30720 KB

```

From /proc/meminfo

```

MemTotal:      529334380 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

/usr/bin/lsb\_release -d

SUSE Linux Enterprise Server 12

From /etc/\*release\* /etc/\*version\*

SuSE-release:

```

SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 0

```

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

```

NAME="SLES"
VERSION="12"
VERSION_ID="12"
PRETTY_NAME="SUSE Linux Enterprise Server 12"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12"

```

uname -a:

```

Linux linux-71ly 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014
(9879bd4) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Feb 6 09:13

SPEC is set to: /root/cpu2006-1.2

```

Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  359G   18G  341G   5% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 0.3.8 12/18/2014

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## Platform Notes (Continued)

Memory:

32x 00CE00B300CE M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz  
16x Not Specified Not Specified

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

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## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.20150421.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.20150421.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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