SPEC® CINT2006 Result
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Fujitsu

PRIMERGY RX2530 M1, Intel Xeon E5-2660 v3, 2.6 GHz

$SPECint^{®}\_rate2006 =$ 918

$SPECint\_rate\_base2006 =$ 888

CPU2006 license: 19	**Test date:** Mar-2015
Test sponsor: Fujitsu	**Hardware Availability:** Feb-2015
Tested by: Fujitsu	**Software Availability:** Nov-2013

Copies

- 400.perlbench: 40 / 833, 40 / 684
- 401.bzip2: 40 / 454, 40 / 434
- 403.gcc: 40 / 675
- 429.mcf: 40 / 1210
- 445.gobmk: 40 / 615, 40 / 600
- 456.hmmer: 40 / 1290, 40 / 1270
- 458.sjeng: 40 / 658, 40 / 637
- 462.libquantum: 40 / 8660
- 464.h264ref: 40 / 1070, 40 / 1040
- 471.omnetpp: 40 / 508, 40 / 486
- 473.astar: 40 / 483
- 483.xalancbmk: 40 / 976

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Hardware

CPU Name:	Intel Xeon E5-2660 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.30 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.23.3.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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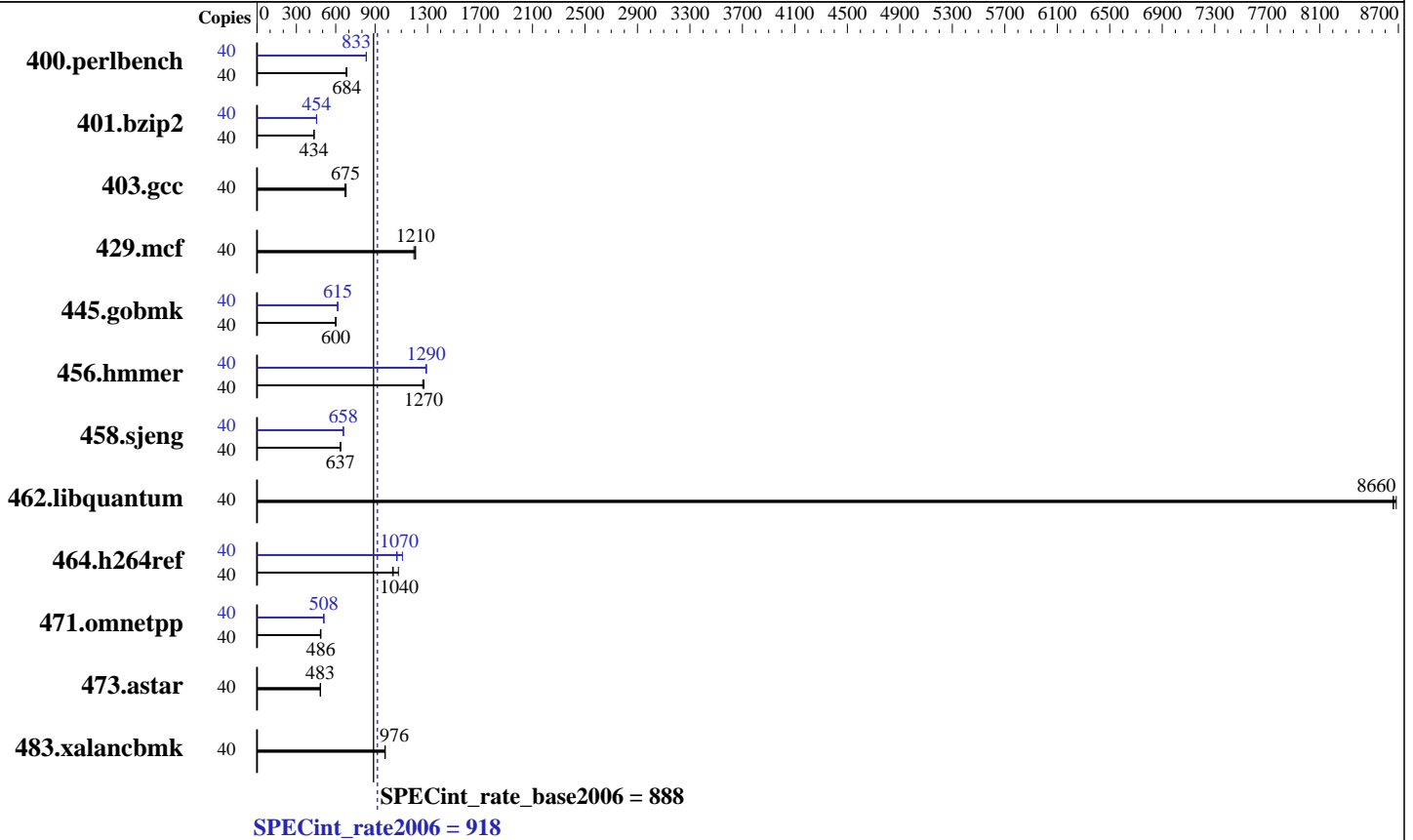
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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	575	679	571	685	**571**	**684**	40	**469**	**833**	469	833	469	833
401.bzip2	40	**889**	**434**	890	434	887	435	40	**850**	**454**	851	454	849	454
403.gcc	40	**477**	**675**	480	671	474	679	40	**477**	**675**	480	671	474	679
429.mcf	40	**302**	**1210**	305	1200	302	1210	40	**302**	**1210**	305	1200	302	1210
445.gobmk	40	**699**	**600**	699	600	699	600	40	683	614	682	615	**682**	**615**
456.hmmer	40	293	1270	**294**	**1270**	295	1260	40	**289**	**1290**	290	1290	289	1290
458.sjeng	40	760	637	**760**	**637**	761	636	40	**735**	**658**	735	658	736	658
462.libquantum	40	**95.7**	**8660**	95.4	8680	95.7	8660	40	**95.7**	**8660**	95.4	8680	95.7	8660
464.h264ref	40	821	1080	**852**	**1040**	856	1030	40	831	1060	799	1110	**829**	**1070**
471.omnetpp	40	517	483	514	486	**515**	**486**	40	492	508	**492**	**508**	490	510
473.astar	40	**581**	**483**	580	484	582	483	40	**581**	**483**	580	484	582	483
483.xalancbmk	40	282	978	**283**	**976**	283	975	40	282	978	**283**	**976**	283	975

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The numactl mechanism was used to bind copies to processors. The config file option 'submit'
    was used to generate numactl commands to bind each copy to a specific processor.
    For details, please see the config file.

Operating System Notes

    Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

    BIOS configuration:
    Energy Performance = Performance
    QPI snoop mode: Cluster on Die
        COD Enable = Enabled, Early Snoop = Disabled
    CPU C1E Support = Disabled

General Notes

    Environment variables set by runspec before the start of the run:
    LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

    Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
    memory using RedHat EL 6.4
    Transparent Huge Pages enabled with:
    echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
    Filesystem page cache cleared with:

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General Notes (Continued)

```
echo 1>         /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

  For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

     400.perlbench: -DSPEC_CPU_LINUX_IA32
     462.libquantum: -DSPEC_CPU_LINUX
     483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
    `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
              `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
              `-auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
          `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
          `-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
          `-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
          `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
          `-unroll4 -auto-ilp32`

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Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
`-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
`-L/sh -lsmartheap`

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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