



SPEC[®] CINT2006 Result

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NEC Corporation

SPECint[®]_rate2006 = 169

Express5800/T120f (Intel Xeon E5-2609 v3)

SPECint_rate_base2006 = 163

CPU2006 license: 9006

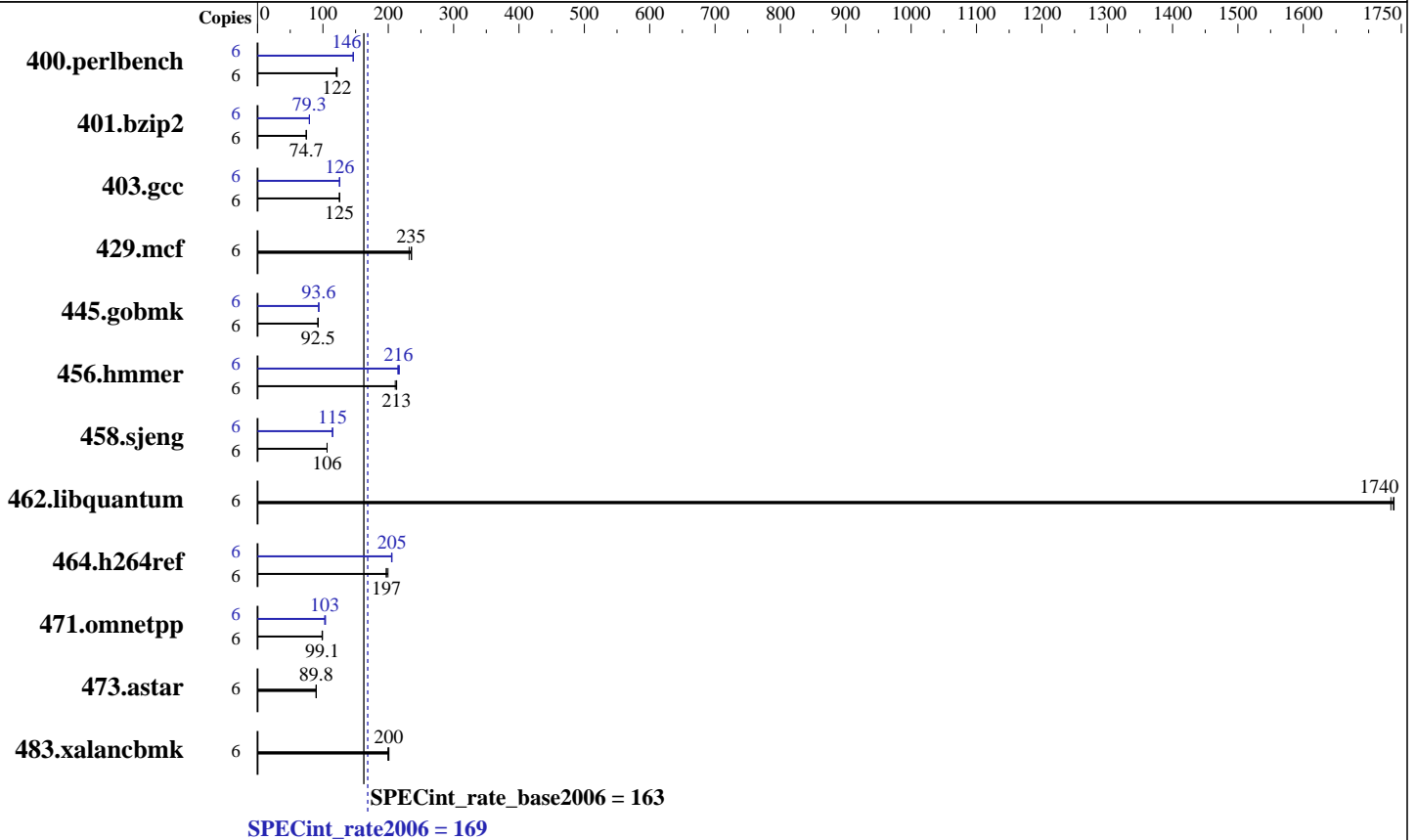
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Mar-2015

Hardware Availability: Jan-2015

Software Availability: Jul-2014



Hardware

CPU Name: Intel Xeon E5-2609 v3
 CPU Characteristics:
 CPU MHz: 1900
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)
 Disk Subsystem: 1 x 250 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 Kernel 2.6.32-431.20.3.el6.x86_64
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|------------|-------------|-------------|------------|-------------|--------|------------|-------------|-------------|-------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 6 | 482 | 122 | 482 | 122 | 487 | 120 | 6 | 400 | 146 | 400 | 146 | 400 | 146 |
| 401.bzip2 | 6 | 778 | 74.5 | 775 | 74.7 | 775 | 74.7 | 6 | 730 | 79.3 | 730 | 79.3 | 730 | 79.4 |
| 403.gcc | 6 | 385 | 126 | 385 | 125 | 385 | 125 | 6 | 384 | 126 | 384 | 126 | 386 | 125 |
| 429.mcf | 6 | 236 | 232 | 233 | 235 | 232 | 236 | 6 | 236 | 232 | 233 | 235 | 232 | 236 |
| 445.gobmk | 6 | 680 | 92.6 | 680 | 92.5 | 681 | 92.4 | 6 | 672 | 93.6 | 672 | 93.6 | 672 | 93.6 |
| 456.hammer | 6 | 263 | 213 | 266 | 211 | 263 | 213 | 6 | 261 | 215 | 260 | 216 | 258 | 217 |
| 458.sjeng | 6 | 682 | 106 | 683 | 106 | 682 | 106 | 6 | 633 | 115 | 633 | 115 | 633 | 115 |
| 462.libquantum | 6 | 71.7 | 1730 | 71.5 | 1740 | 71.5 | 1740 | 6 | 71.7 | 1730 | 71.5 | 1740 | 71.5 | 1740 |
| 464.h264ref | 6 | 674 | 197 | 665 | 200 | 674 | 197 | 6 | 646 | 206 | 646 | 205 | 648 | 205 |
| 471.omnetpp | 6 | 380 | 98.7 | 379 | 99.1 | 378 | 99.3 | 6 | 361 | 104 | 364 | 103 | 364 | 103 |
| 473.astar | 6 | 471 | 89.5 | 469 | 89.8 | 469 | 89.9 | 6 | 471 | 89.5 | 469 | 89.8 | 469 | 89.9 |
| 483.xalancbmk | 6 | 207 | 200 | 206 | 201 | 208 | 199 | 6 | 207 | 200 | 206 | 201 | 208 | 199 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Power Management Policy: Custom
Energy Performance: Performance
Patrol Scrub: Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



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Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32 -L/opt/intel/composer_xe_2015/lib/ia32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

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Peak Compiler Invocation (Continued)

C++ benchmarks:

icpc -m32 -L/opt/intel/composer_xe_2015/lib/ia32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
429.mcf: basepeak = yes
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

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Peak Optimization Flags (Continued)

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-120f-RevC.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

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