



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

SPECint®\_rate2006 = 917

ProLiant DL60 Gen9  
(2.60 GHz, Intel Xeon E5-2660 v3)

SPECint\_rate\_base2006 = 884

CPU2006 license: 3

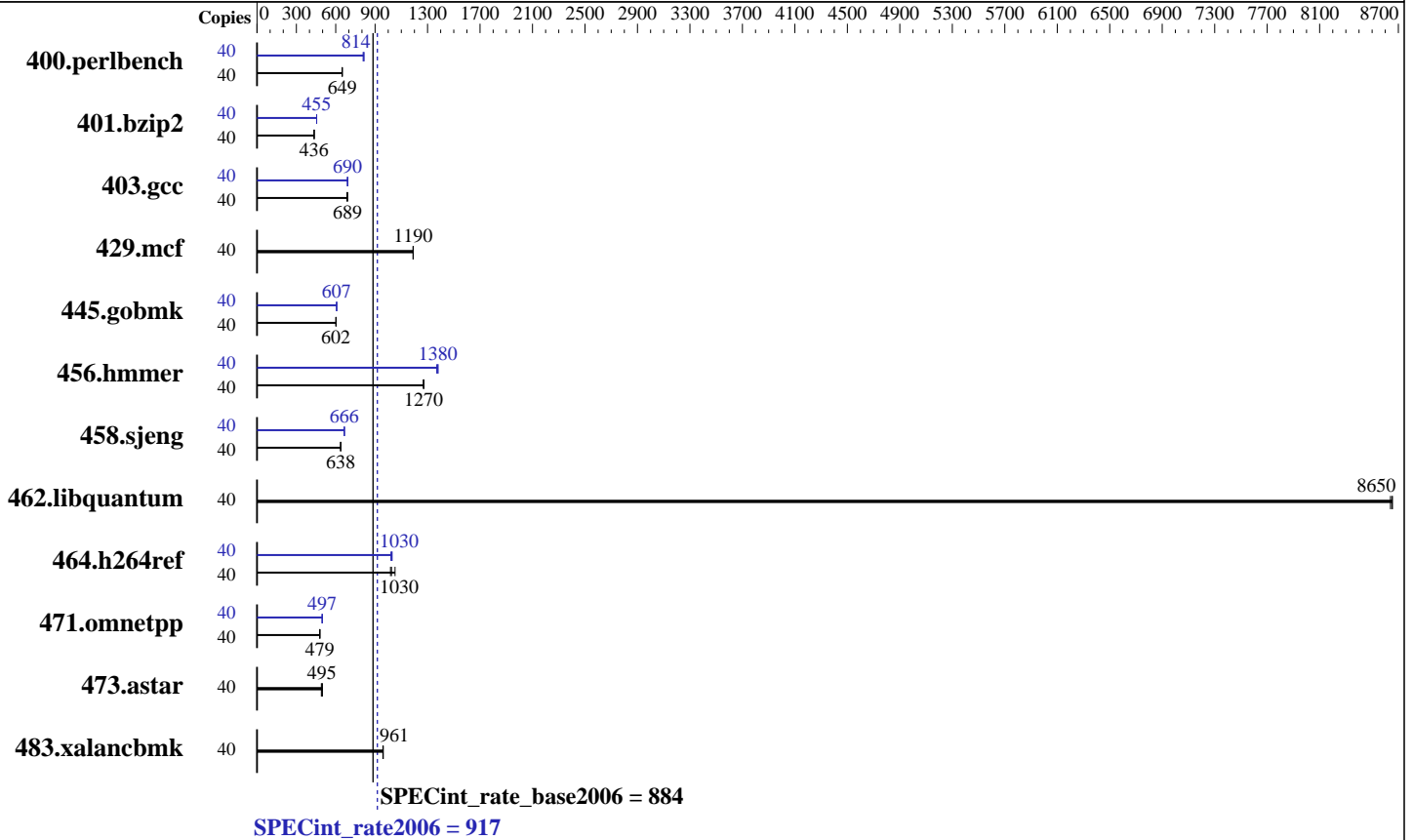
Test date: Nov-2014

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2660 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (8 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 400 GB SATA SSD, RAID 0  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 Kernel 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

SPECint\_rate2006 = 917

ProLiant DL60 Gen9  
(2.60 GHz, Intel Xeon E5-2660 v3)

SPECint\_rate\_base2006 = 884

CPU2006 license: 3

Test date: Nov-2014

Test sponsor: Hewlett-Packard Company

Hardware Availability: Sep-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2014

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	<b>602</b>	<b>649</b>	602	649	601	650	40	479	815	483	809	<b>480</b>	<b>814</b>
401.bzip2	40	888	435	885	436	<b>886</b>	<b>436</b>	40	849	455	<b>849</b>	<b>455</b>	849	454
403.gcc	40	470	685	467	690	<b>467</b>	<b>689</b>	40	465	692	<b>467</b>	<b>690</b>	467	689
429.mcf	40	307	1190	306	1190	<b>306</b>	<b>1190</b>	40	307	1190	306	1190	<b>306</b>	<b>1190</b>
445.gobmk	40	697	602	<b>697</b>	<b>602</b>	697	602	40	692	606	690	608	<b>692</b>	<b>607</b>
456.hammer	40	293	1270	295	1270	<b>294</b>	<b>1270</b>	40	<b>271</b>	<b>1380</b>	272	1370	270	1380
458.sjeng	40	<b>759</b>	<b>638</b>	759	638	759	638	40	<b>727</b>	<b>666</b>	727	665	727	666
462.libquantum	40	95.8	8660	<b>95.8</b>	<b>8650</b>	95.9	8640	40	95.8	8660	<b>95.8</b>	<b>8650</b>	95.9	8640
464.h264ref	40	870	1020	<b>863</b>	<b>1030</b>	841	1050	40	860	1030	<b>861</b>	<b>1030</b>	867	1020
471.omnetpp	40	<b>522</b>	<b>479</b>	521	480	524	477	40	<b>503</b>	<b>497</b>	504	496	502	498
473.astar	40	<b>567</b>	<b>495</b>	572	490	563	499	40	<b>567</b>	<b>495</b>	572	490	563	499
483.xalancbmk	40	287	963	<b>287</b>	<b>961</b>	288	959	40	287	963	<b>287</b>	<b>961</b>	288	959

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Platform Notes

BIOS Configuration:  
HP Power Profile set to Custom  
HP Power Regulator set to HP Static High Performance Mode  
Minimum Processor Idle Power Package C-State set to No Package State  
Energy/Performance Bias set to Maximum Performance  
QPI Snoop Configuration set to Cluster on Die  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh

Sysinfo program /cpu2006/config/sysinfo.rev6914  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

SPECint\_rate2006 = 917

ProLiant DL60 Gen9  
(2.60 GHz, Intel Xeon E5-2660 v3)

SPECint\_rate\_base2006 = 884

**CPU2006 license:** 3  
**Test sponsor:** Hewlett-Packard Company  
**Tested by:** Hewlett-Packard Company

**Test date:** Nov-2014  
**Hardware Availability:** Sep-2014  
**Software Availability:** Sep-2014

### Platform Notes (Continued)

\$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1  
running on DL80-Gen9 Tue Nov 11 17:34:12 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v3 @ 2.60GHz
 2 "physical id"s (chips)
 40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 5
  siblings  : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 12800 KB
```

```
From /proc/meminfo
MemTotal:      131600496 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

```
uname -a:
Linux DL80-Gen9 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 11 17:33

```
SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   368G  15G  354G   4% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECint\_rate2006 = 917**

ProLiant DL60 Gen9  
(2.60 GHz, Intel Xeon E5-2660 v3)

**SPECint\_rate\_base2006 = 884**

**CPU2006 license:** 3

**Test date:** Nov-2014

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Sep-2014

**Tested by:** Hewlett-Packard Company

**Software Availability:** Sep-2014

## Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U15 08/26/2014

Memory:

8x HP 752369-081 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECint\_rate2006 = 917**

ProLiant DL60 Gen9  
(2.60 GHz, Intel Xeon E5-2660 v3)

**SPECint\_rate\_base2006 = 884**

**CPU2006 license:** 3

**Test date:** Nov-2014

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Sep-2014

**Tested by:** Hewlett-Packard Company

**Software Availability:** Sep-2014

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

**Hewlett-Packard Company**

**SPECint\_rate2006 = 917**

ProLiant DL60 Gen9  
(2.60 GHz, Intel Xeon E5-2660 v3)

**SPECint\_rate\_base2006 = 884**

**CPU2006 license:** 3

**Test date:** Nov-2014

**Test sponsor:** Hewlett-Packard Company

**Hardware Availability:** Sep-2014

**Tested by:** Hewlett-Packard Company

**Software Availability:** Sep-2014

## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant DL60 Gen9  
(2.60 GHz, Intel Xeon E5-2660 v3)

SPECint\_rate2006 = 917

SPECint\_rate\_base2006 = 884

**CPU2006 license:** 3  
**Test sponsor:** Hewlett-Packard Company  
**Tested by:** Hewlett-Packard Company

**Test date:** Nov-2014  
**Hardware Availability:** Sep-2014  
**Software Availability:** Sep-2014

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Dec 3 10:36:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 2 December 2014.