



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = 578

PowerEdge M630 (Intel Xeon E5-2630L v3, 1.80 GHz)

SPECint\_rate\_base2006 = 558

CPU2006 license: 55

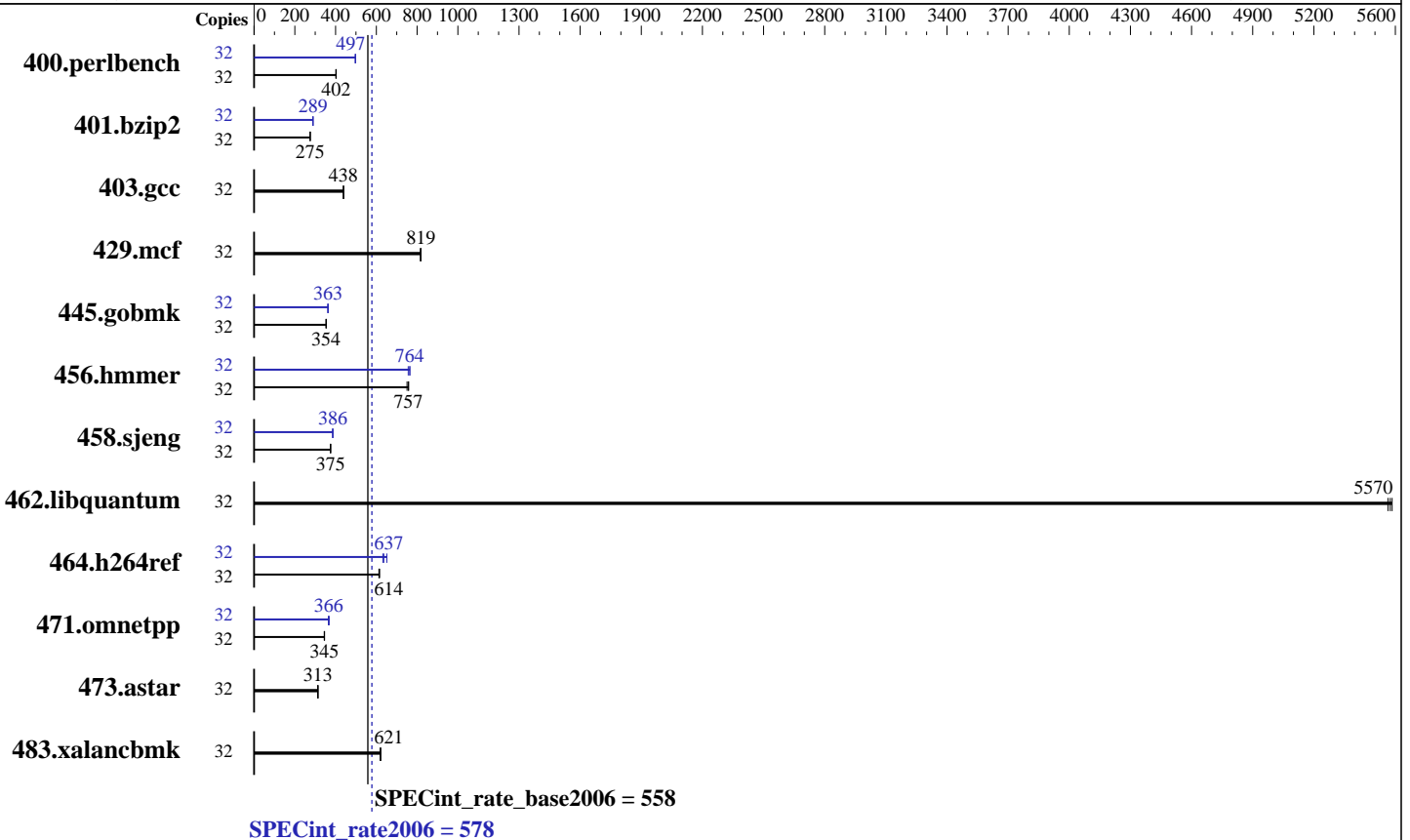
Test date: Oct-2014

Test sponsor: Dell Inc.

Hardware Availability: Dec-2014

Tested by: Dell Inc.

Software Availability: Jan-2014



## Hardware

CPU Name: Intel Xeon E5-2630L v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 1800  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1866 MHz)  
 Disk Subsystem: 1 x 1.6 TB SAS SSD  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	775	403	<b><u>778</u></b>	<b><u>402</u></b>	779	402	32	<b><u>629</u></b>	<b><u>497</u></b>	629	497	628	498
401.bzip2	32	1122	275	<b><u>1122</u></b>	<b><u>275</u></b>	1121	276	32	1069	289	1067	289	<b><u>1068</u></b>	<b><u>289</u></b>
403.gcc	32	586	439	<b><u>588</u></b>	<b><u>438</u></b>	588	438	32	586	439	<b><u>588</u></b>	<b><u>438</u></b>	588	438
429.mcf	32	<b><u>357</u></b>	<b><u>819</u></b>	358	816	357	819	32	<b><u>357</u></b>	<b><u>819</u></b>	358	816	357	819
445.gobmk	32	950	354	<b><u>949</u></b>	<b><u>354</u></b>	949	354	32	<b><u>925</u></b>	<b><u>363</u></b>	927	362	924	363
456.hammer	32	394	759	398	750	<b><u>395</u></b>	<b><u>757</u></b>	32	<b><u>391</u></b>	<b><u>764</u></b>	395	756	390	765
458.sjeng	32	1030	376	<b><u>1031</u></b>	<b><u>375</u></b>	1033	375	32	<b><u>1003</u></b>	<b><u>386</u></b>	1004	386	1001	387
462.libquantum	32	119	5560	<b><u>119</u></b>	<b><u>5570</u></b>	119	5580	32	119	5560	<b><u>119</u></b>	<b><u>5570</u></b>	119	5580
464.h264ref	32	<b><u>1154</u></b>	<b><u>614</u></b>	1149	616	1156	613	32	1087	651	1120	632	<b><u>1113</u></b>	<b><u>637</u></b>
471.omnetpp	32	578	346	<b><u>580</u></b>	<b><u>345</u></b>	580	345	32	<b><u>546</u></b>	<b><u>366</u></b>	548	365	543	368
473.astar	32	<b><u>717</u></b>	<b><u>313</u></b>	715	314	721	312	32	<b><u>717</u></b>	<b><u>313</u></b>	715	314	721	312
483.xalancbmk	32	355	621	357	619	<b><u>355</u></b>	<b><u>621</u></b>	32	355	621	357	619	<b><u>355</u></b>	<b><u>621</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
Snoop Mode set to Cluster on Die  
Virtualization Technology disabled  
Execute Disable disabled  
System Profile set to Custom  
Memory Patrol Scrub set to Disabled  
Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on localhost.localdomain Mon Oct 20 22:05:55 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2630L v3 @ 1.80GHz  
2 "physical id"s (chips)

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## Platform Notes (Continued)

32 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 8
siblings  : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      264437524 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
EST 2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Oct 20 21:54
```

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       ext4  1.5T  9.7G  1.4T   1% /
```

Additional information from dmidecode:

```
BIOS Dell Inc. 1.0.0 10/13/2014
Memory:
8x 000000000000 Not Specified 1867 MHz 1 rank
16x 00AD00B300AD HMA42GR7MFR4N-TFTD 16 GB 1867 MHz 2 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Filesystem page cache cleared with:

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## General Notes (Continued)

```
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:  
icc -m32  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32  
400.perlbench: icc -m64

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## Peak Compiler Invocation (Continued)

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`  
401.bzip2: `-DSPEC_CPU_LP64`  
456.hmmer: `-DSPEC_CPU_LP64`  
458.sjeng: `-DSPEC_CPU_LP64`  
462.libquantum: `-DSPEC_CPU_LINUX`  
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-auto-ilp32`

401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`  
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

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## Peak Optimization Flags (Continued)

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>  
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revE.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Dec 3 10:33:10 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 2 December 2014.