



# SPEC® CINT2006 Result

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## Huawei

## SPECint®\_rate2006 = 308

### Huawei CH121 V3 (Intel Xeon E5-2609 v3)

## SPECint\_rate\_base2006 = 298

CPU2006 license: 3175

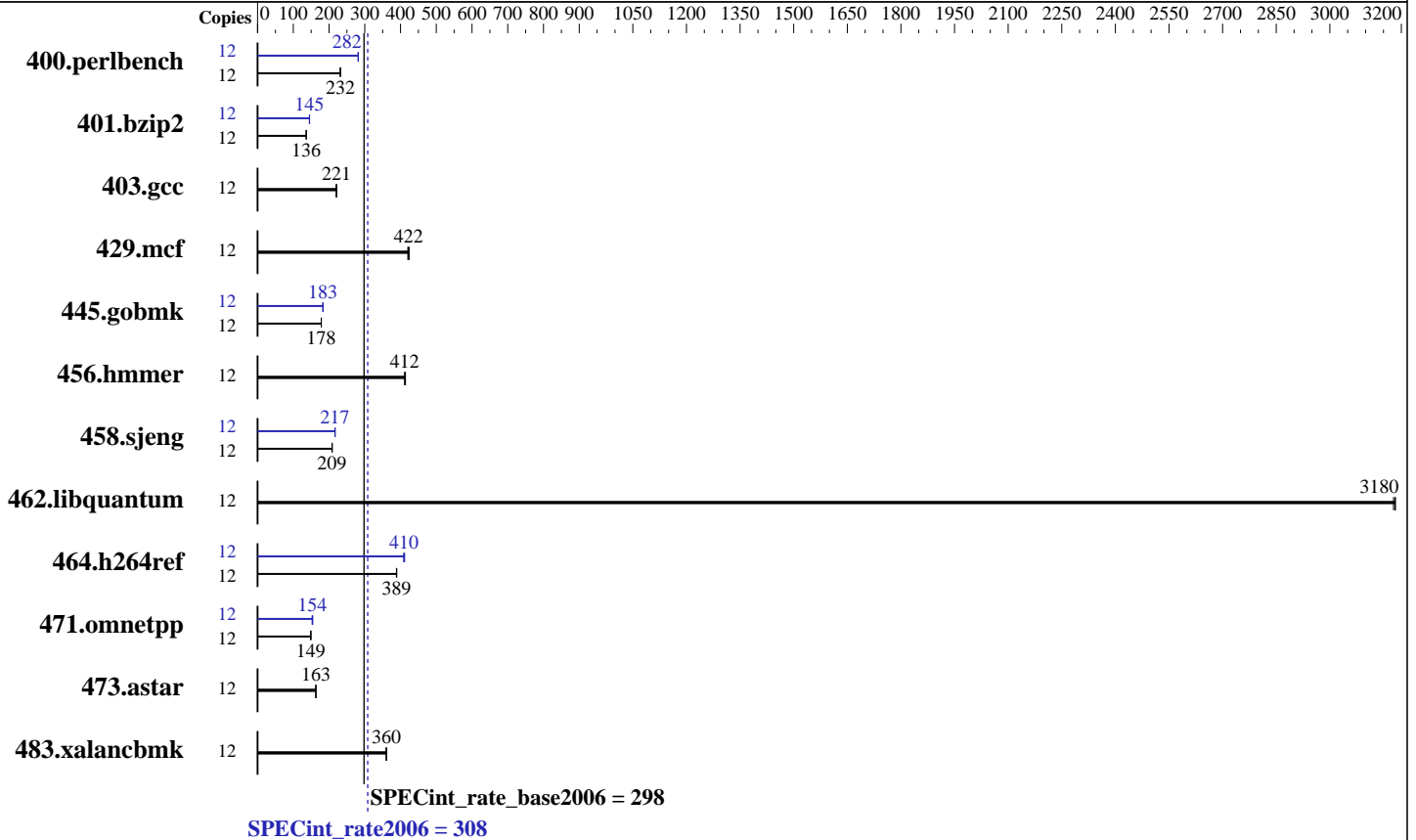
Test sponsor: Huawei

Tested by: Huawei

Test date: Jul-2014

Hardware Availability: Sep-2014

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E5-2609 v3  
 CPU Characteristics:  
 CPU MHz: 1900  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
 Disk Subsystem: 1 x 256 GB SATA, SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	<b><u>506</u></b>	<b><u>232</u></b>	505	232	507	231	12	<b><u>416</u></b>	<b><u>282</u></b>	416	282	416	282
401.bzip2	12	849	136	<b><u>850</u></b>	<b><u>136</u></b>	852	136	12	798	145	795	146	<b><u>798</u></b>	<b><u>145</u></b>
403.gcc	12	438	221	439	220	<b><u>438</u></b>	<b><u>221</u></b>	12	438	221	439	220	<b><u>438</u></b>	<b><u>221</u></b>
429.mcf	12	<b><u>259</u></b>	<b><u>422</u></b>	258	424	260	420	12	<b><u>259</u></b>	<b><u>422</u></b>	258	424	260	420
445.gobmk	12	705	178	<b><u>706</u></b>	<b><u>178</u></b>	707	178	12	688	183	688	183	<b><u>688</u></b>	<b><u>183</u></b>
456.hammer	12	<b><u>272</u></b>	<b><u>412</u></b>	271	414	272	412	12	<b><u>272</u></b>	<b><u>412</u></b>	271	414	272	412
458.sjeng	12	695	209	<b><u>696</u></b>	<b><u>209</u></b>	696	209	12	669	217	670	217	<b><u>669</u></b>	<b><u>217</u></b>
462.libquantum	12	78.1	3180	78.2	3180	<b><u>78.1</u></b>	<b><u>3180</u></b>	12	78.1	3180	78.2	3180	<b><u>78.1</u></b>	<b><u>3180</u></b>
464.h264ref	12	682	390	683	389	<b><u>683</u></b>	<b><u>389</u></b>	12	<b><u>648</u></b>	<b><u>410</u></b>	645	412	650	409
471.omnetpp	12	<b><u>504</u></b>	<b><u>149</u></b>	501	150	506	148	12	486	154	<b><u>487</u></b>	<b><u>154</u></b>	492	153
473.astar	12	516	163	517	163	<b><u>516</u></b>	<b><u>163</u></b>	12	516	163	517	163	<b><u>516</u></b>	<b><u>163</u></b>
483.xalancbmk	12	230	360	230	360	<b><u>230</u></b>	<b><u>360</u></b>	12	230	360	230	360	<b><u>230</u></b>	<b><u>360</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:  
Set Power Efficiency Mode to Custom  
Set Snoop Mode to HS  
Set Hyper-Threading to Disabled  
Baseboard Management Controller used to adjust the fan speed to 100%  
Sysinfo program /spec/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on xjt Tue Jul 22 14:24:23 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2609 v3 @ 1.90GHz  
2 "physical id"s (chips)  
12 "processors"

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## Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 6
siblings  : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

```
From /proc/meminfo
MemTotal:      131928996 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux xjt 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 22 13:22
```

```
SPEC is set to: /spec
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sdal       ext4  222G  7.7G  203G   4% /
```

Additional information from dmidecode:

```
BIOS Insyde Corp. 1.06 06/17/2014
Memory:
8x Micron 18ASF1G72PDZ-2G1A1 8 GB 1600 MHz 1 rank
8x Micron 18ASF1G72PDZ-2G1A1 8 GB 1600 MHz 2 rank
8x NO DIMM NO DIMM      3 rank
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:

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## General Notes (Continued)

```
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
The Huawei CH121 V3 and Huawei CH222 V3
are electronically equivalent.
The results have been measured on a Huawei CH121 V3 model
```

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

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## Peak Compiler Invocation (Continued)

400.perlbench: icc -m64

401.bzip2: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/sh -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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