SPEC® CINT2006 Result

Fujitsu

PRIMERGY RX1330 M1, Intel Xeon E3-1231 v3, 3.40 GHz

$SPECint®\_rate2006 =$ 214

$SPECint\_rate\_base2006 =$ 207

Benchmark results (Copies = 8 for all):

Benchmark	Peak	Base
400.perlbench	206	171
401.bzip2	108	104
403.gcc		162
429.mcf		249
445.gobmk	148	145
456.hmmer	305	298
458.sjeng	163	160
462.libquantum		1810
464.h264ref	284	258
471.omnetpp	109	106
473.astar		112
483.xalancbmk		226

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Hardware

CPU Name:	Intel Xeon E3-1231 v3
CPU Characteristics:	Intel Turbo Boost Technology up to 3.80 GHz
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11, ECC)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 5 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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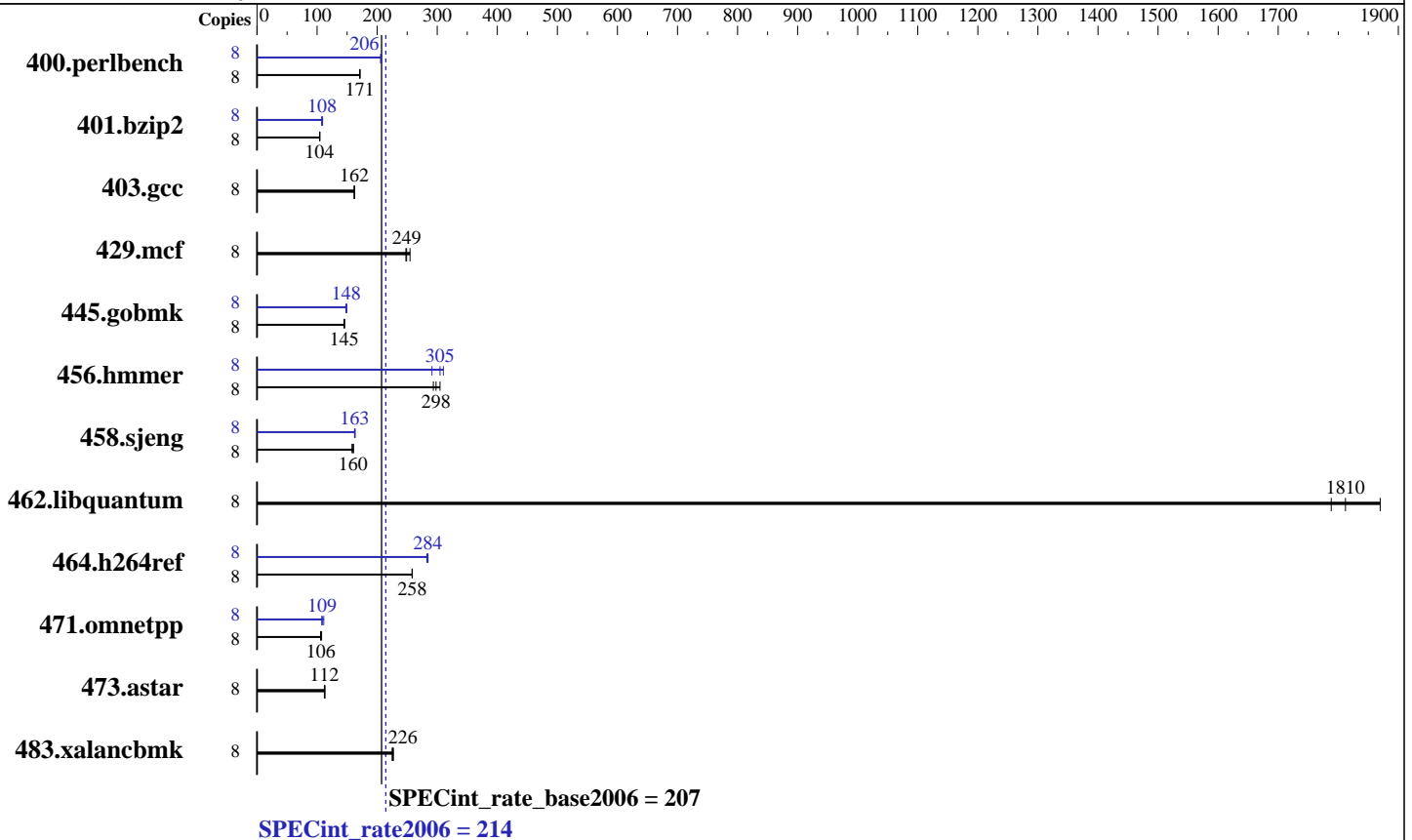
SPECint_rate_base2006 = 207

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jun-2014
Hardware Availability: Jul-2014
Software Availability: Nov-2013

Results Table

Benchmark	Base Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Peak Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	458	171	456	172	**456**	**171**	8	**379**	**206**	380	206	378	207
401.bzip2	8	741	104	**740**	**104**	738	105	8	**713**	**108**	707	109	717	108
403.gcc	8	398	162	397	162	**397**	**162**	8	398	162	397	162	**397**	**162**
429.mcf	8	294	248	**293**	**249**	286	255	8	294	248	**293**	**249**	286	255
445.gobmk	8	579	145	**577**	**145**	576	146	8	**566**	**148**	566	148	561	150
456.hmmer	8	**250**	**298**	254	294	245	305	8	256	291	241	310	**245**	**305**
458.sjeng	8	**606**	**160**	612	158	602	161	8	596	162	592	163	**595**	**163**
462.libquantum	8	**91.5**	**1810**	92.7	1790	88.7	1870	8	**91.5**	**1810**	92.7	1790	88.7	1870
464.h264ref	8	685	259	**685**	**258**	686	258	8	**623**	**284**	622	285	626	283
471.omnetpp	8	**471**	**106**	465	108	473	106	8	450	111	464	108	**460**	**109**
473.astar	8	501	112	**499**	**112**	496	113	8	501	112	**499**	**112**	496	113
483.xalancbmk	8	**244**	**226**	243	227	245	225	8	**244**	**226**	243	227	245	225

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
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Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    456.hmmer: icc -m64

    458.sjeng: icc -m64

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Peak Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Peak Portability Flags

        400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
          401.bzip2: -DSPEC_CPU_LP64
          456.hmmer: -DSPEC_CPU_LP64
          458.sjeng: -DSPEC_CPU_LP64
      462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

        400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                       -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                       -auto-ilp32

          401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                     -opt-prefetch -auto-ilp32 -ansi-alias

            403.gcc: basepeak = yes

            429.mcf: basepeak = yes

          445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                     -ansi-alias -opt-mem-layout-trans=3

          456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

          458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                     -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                     -unroll4 -auto-ilp32

      462.libquantum: basepeak = yes

          464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                       -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                       -unroll2 -ansi-alias

C++ benchmarks:

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Peak Optimization Flags (Continued)

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Oct 9 13:57:54 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 9 October 2014.