



SPEC[®] CFP2006 Result

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Huawei

SPECfp[®]2006 = **93.2**

Huawei BH622 V2 (Intel Xeon E5-2658 v2)

SPECfp_base2006 = **89.3**

CPU2006 license: 3175

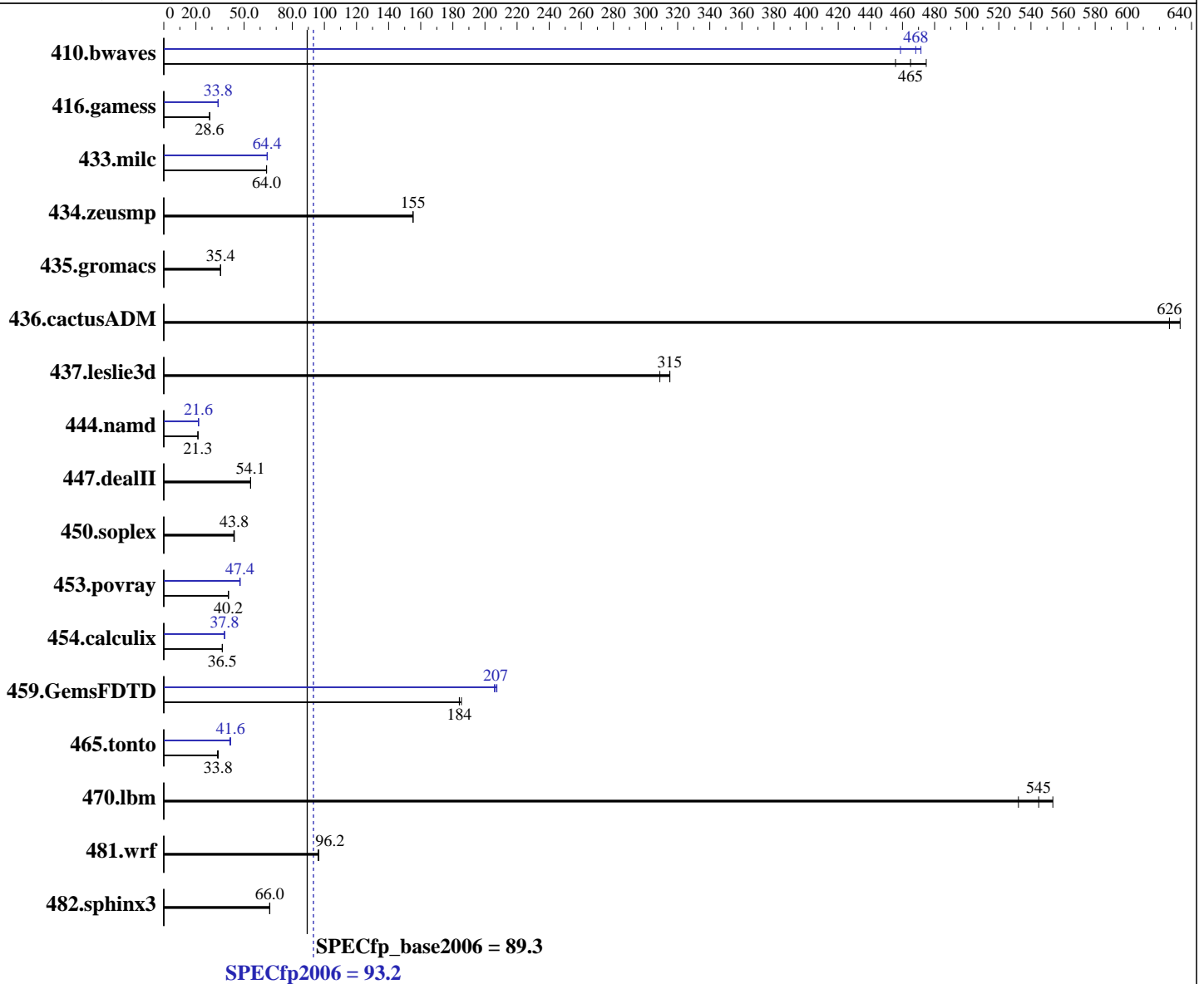
Test sponsor: Huawei

Tested by: Huawei

Test date: Jul-2014

Hardware Availability: Sep-2013

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-2658 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC)
 Disk Subsystem: 1 X 300 GB SAS 10000 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	<u>29.2</u>	<u>465</u>	28.6	475	29.8	456	29.6	459	28.8	472	<u>29.0</u>	<u>468</u>
416.gamess	<u>686</u>	<u>28.6</u>	687	28.5	684	28.6	579	33.8	<u>579</u>	<u>33.8</u>	579	33.8
433.milc	144	63.9	<u>144</u>	<u>64.0</u>	143	64.0	<u>143</u>	<u>64.4</u>	142	64.5	143	64.4
434.zeusmp	58.6	155	58.6	155	<u>58.6</u>	<u>155</u>	58.6	155	58.6	155	<u>58.6</u>	<u>155</u>
435.gromacs	202	35.4	<u>202</u>	<u>35.4</u>	202	35.4	202	35.4	<u>202</u>	<u>35.4</u>	202	35.4
436.cactusADM	19.1	626	<u>19.1</u>	<u>626</u>	18.9	633	19.1	626	<u>19.1</u>	<u>626</u>	18.9	633
437.leslie3d	30.4	309	29.8	315	<u>29.8</u>	<u>315</u>	30.4	309	29.8	315	<u>29.8</u>	<u>315</u>
444.namd	<u>377</u>	<u>21.3</u>	377	21.3	377	21.3	371	21.6	<u>371</u>	<u>21.6</u>	371	21.6
447.dealII	212	54.0	211	54.1	<u>212</u>	<u>54.1</u>	212	54.0	211	54.1	<u>212</u>	<u>54.1</u>
450.soplex	190	43.9	<u>191</u>	<u>43.8</u>	191	43.6	190	43.9	<u>191</u>	<u>43.8</u>	191	43.6
453.povray	132	40.2	<u>132</u>	<u>40.2</u>	132	40.3	112	47.6	112	47.3	<u>112</u>	<u>47.4</u>
454.calculix	<u>226</u>	<u>36.5</u>	226	36.5	227	36.4	218	37.8	<u>218</u>	<u>37.8</u>	218	37.8
459.GemsFDTD	57.7	184	<u>57.6</u>	<u>184</u>	57.2	185	51.5	206	51.1	207	<u>51.3</u>	<u>207</u>
465.tonto	291	33.9	295	33.4	<u>291</u>	<u>33.8</u>	236	41.6	<u>237</u>	<u>41.6</u>	239	41.1
470.lbm	25.8	532	24.8	554	<u>25.2</u>	<u>545</u>	25.8	532	24.8	554	<u>25.2</u>	<u>545</u>
481.wrf	116	96.1	116	96.6	<u>116</u>	<u>96.2</u>	116	96.1	116	96.6	<u>116</u>	<u>96.2</u>
482.sphinx3	295	66.0	296	65.9	<u>295</u>	<u>66.0</u>	295	66.0	296	65.9	<u>295</u>	<u>66.0</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
 Set HT to Disable
 Sysinfo program /spec/config/sysinfo.rev6800
 \$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
 running on RHEL Fri Jul 18 16:34:51 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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Platform Notes (Continued)

```

From /proc/cpuinfo
  model name : Intel(R) Xeon(R) CPU E5-2658 v2 @ 2.40GHz
    2 "physical id"s (chips)
    20 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 10
  siblings  : 10
  physical 0: cores 0 1 2 3 4 8 9 10 11 12
  physical 1: cores 0 1 2 3 4 8 9 10 11 12
  cache size : 25600 KB

From /proc/meminfo
MemTotal:      264478184 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
  Linux RHEL 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
  x86_64 x86_64 GNU/Linux

run-level 3 Jul 17 10:15

SPEC is set to: /spec
  Filesystem      Type      Size      Used Avail Use% Mounted on
  /dev/sda2       ext4     265G      21G   231G   9% /

Additional information from dmidecode:
Memory:
  2x Hynix HMT42GR7AFR4C-RD 16 GB 1867 MHz 2 rank
  8x Samsung M393B2G70DB0-CMA 16 GB 1867 MHz 2 rank
  6x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank

(End of data from sysinfo program)

```

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "20"

```

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General Notes (Continued)

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

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Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>



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