



SPEC® CINT2006 Result

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Huawei

SPECint®2006 = 43.4

Huawei CH240 (Intel Xeon E5-4620)

SPECint_base2006 = 40.3

CPU2006 license: 3175

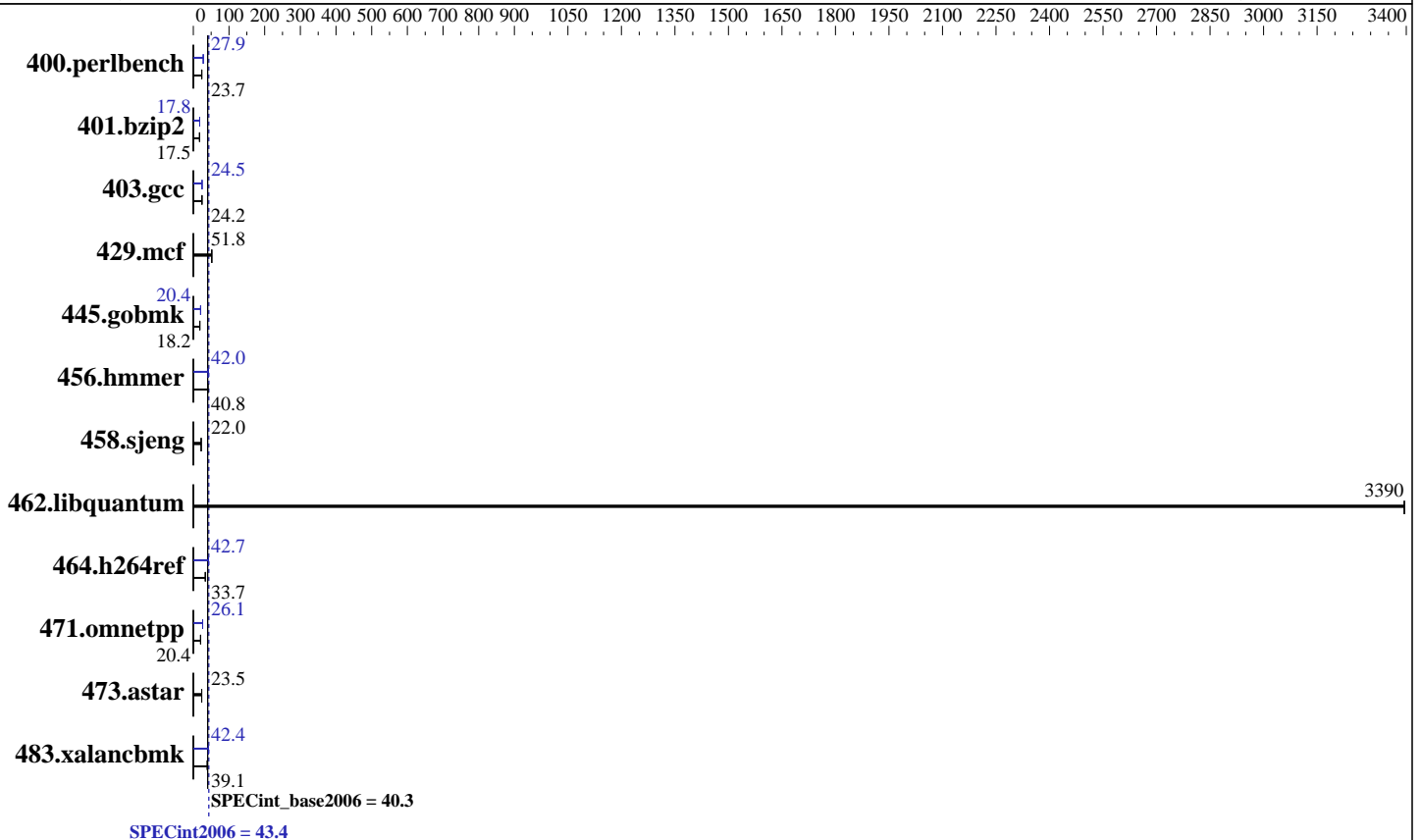
Test sponsor: Huawei

Tested by: Huawei

Test date: Jul-2014

Hardware Availability: May-2012

Software Availability: Nov-2013



Hardware

CPU Name: Intel Xeon E5-4620
 CPU Characteristics: Intel Turbo Boost Technology up to 2.60 GHz
 CPU MHz: 2200
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip
 CPU(s) orderable: 2,4 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (32 x 16 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
 2.6.32-431.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	413	23.7	412	23.7	412	23.7	351	27.9	350	27.9	350	27.9
401.bzip2	553	17.5	552	17.5	553	17.4	541	17.8	541	17.8	540	17.9
403.gcc	332	24.2	332	24.2	333	24.2	329	24.5	329	24.5	329	24.5
429.mcf	177	51.6	176	51.9	176	51.8	177	51.6	176	51.9	176	51.8
445.gobmk	576	18.2	576	18.2	576	18.2	514	20.4	514	20.4	514	20.4
456.hammer	229	40.8	228	40.9	229	40.8	222	42.0	223	41.9	222	42.0
458.sjeng	550	22.0	550	22.0	550	22.0	550	22.0	550	22.0	550	22.0
462.libquantum	6.10	3400	6.10	3390	6.10	3390	6.10	3400	6.10	3390	6.10	3390
464.h264ref	657	33.7	658	33.6	657	33.7	519	42.7	519	42.7	524	42.3
471.omnetpp	308	20.3	307	20.4	306	20.4	239	26.1	239	26.2	239	26.1
473.astar	299	23.5	298	23.5	298	23.5	299	23.5	298	23.5	298	23.5
483.xalancbmk	175	39.5	177	39.0	177	39.1	163	42.4	163	42.4	163	42.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
Sysinfo program /spec/config/sysinfo.rev6800
$Rev: 6800 $ $Date:: 2011-10-11 #$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Thu Jul 17 06:44:02 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-4620 0 @ 2.20GHz
 4 "physical id"s (chips)
32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 8
siblings : 8
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
physical 2: cores 0 1 2 3 4 5 6 7
physical 3: cores 0 1 2 3 4 5 6 7
cache size : 16384 KB
```

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Platform Notes (Continued)

```

From /proc/meminfo
  MemTotal:      529228328 kB
  HugePages_Total:      0
  Hugepagesize:    2048 kB

/usr/bin/lsb_release -d
  Red Hat Enterprise Linux Server release 6.5 (Santiago)

From /etc/*release* /etc/*version*
  redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
  system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
  Linux localhost.localdomain 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54
  EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jul 17 05:30

SPEC is set to: /spec
  Filesystem      Type  Size  Used Avail Use% Mounted on
  /dev/sda2       ext4  411G   15G  375G   4% /

Additional information from dmidecode:
  Memory:
    32x Hynix HMT42GR7MFR4C-PB 16 GB 1600 MHz 2 rank

(End of data from sysinfo program)

```

General Notes

Environment variables set by runspec before the start of the run:

```

KMP_AFFINITY = "granularity=fine,compact,0,1"
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"
OMP_NUM_THREADS = "32"

```

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory using RHEL 6.1

Transparent Huge Pages enabled with:

```

echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

```

Base Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/smartheap -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: basepeak = yes

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>
<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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For other inquiries, please contact webmaster@spec.org.

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