SPEC® CINT2006 Result
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Supermicro

SuperServer 1018D-73MTF
(X10SL7-F , Intel Xeon E3-1281 v3)

SPECint®_rate2006 =   227

SPECint_rate_base2006 = 220
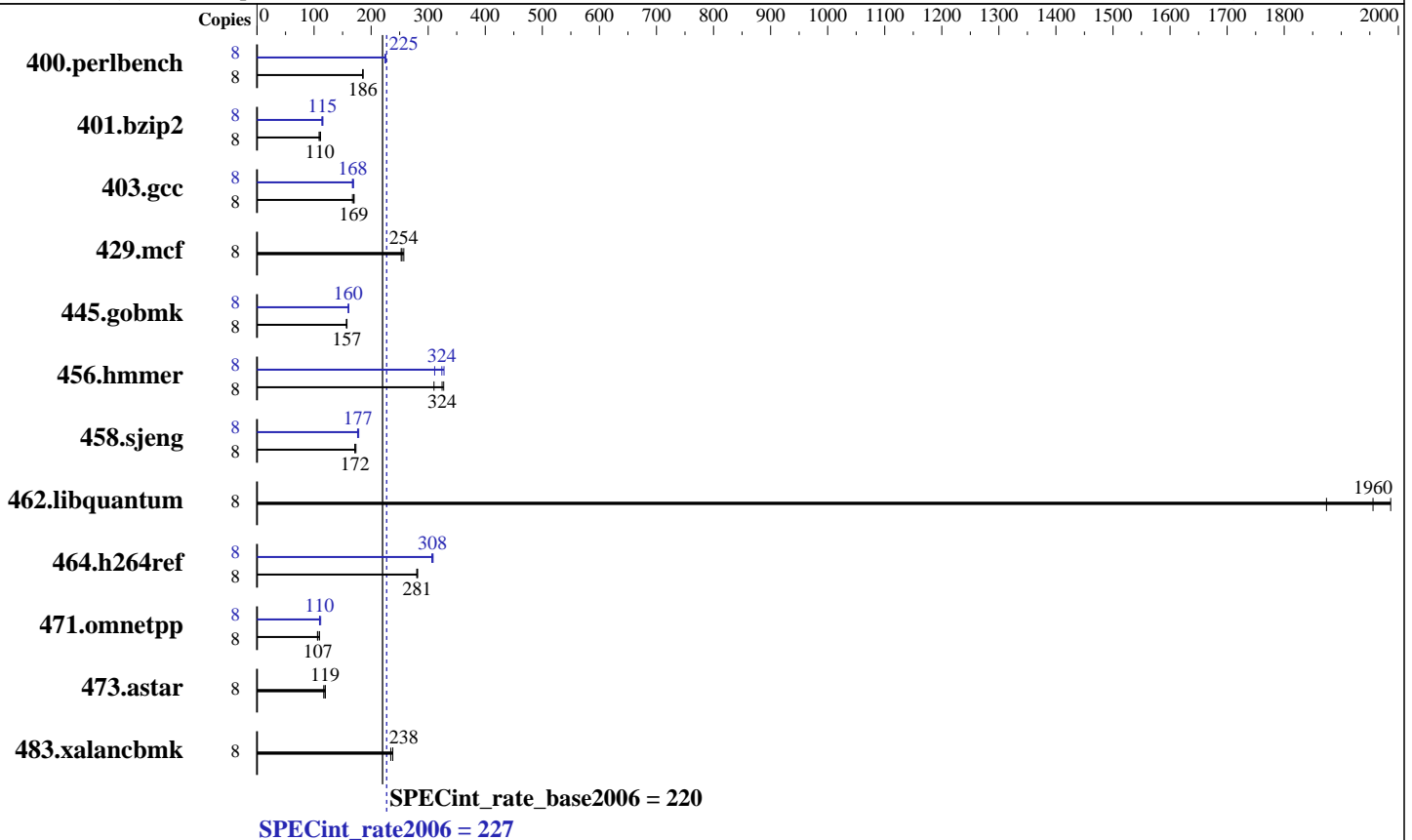
CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2014

Hardware Availability: May-2014

Software Availability: Nov-2013

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Hardware

CPU Name: Intel Xeon E3-1281 v3

CPU Characteristics: Intel Turbo Boost Technology up to 4.10 GHz

CPU MHz: 3700

FPU: Integrated

CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core

CPU(s) orderable: 1 chip

Primary Cache: 32 KB I + 32 KB D on chip per core

Secondary Cache: 256 KB I+D on chip per core

L3 Cache: 8 MB I+D on chip per chip

Other Cache: None

Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)

Disk Subsystem: 1 x 200 GB SATA III, SSD

Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.5, Kernel 2.6.32-431.el6.x86_64

Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux

Auto Parallel: No

File System: ext4

System State: Run level 3 (multi-user)

Base Pointers: 32-bit

Peak Pointers: 32/64-bit

Other Software: Microquill SmartHeap V10.0

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	**421**	**186**	421	186	422	185	8	348	224	**348**	**225**	347	226
401.bzip2	8	710	109	694	111	**700**	**110**	8	669	115	**672**	**115**	680	114
403.gcc	8	378	170	**380**	**169**	385	167	8	**384**	**168**	385	167	381	169
429.mcf	8	**287**	**254**	289	253	284	257	8	**287**	**254**	289	253	284	257
445.gobmk	8	536	157	**534**	**157**	532	158	8	522	161	524	160	**524**	**160**
456.hmmer	8	**230**	**324**	228	327	241	310	8	240	311	**230**	**324**	228	328
458.sjeng	8	565	171	**563**	**172**	558	173	8	549	176	**546**	**177**	543	178
462.libquantum	8	88.4	1870	83.4	1990	**84.8**	**1960**	8	88.4	1870	83.4	1990	**84.8**	**1960**
464.h264ref	8	632	280	629	282	**631**	**281**	8	578	306	574	309	**575**	**308**
471.omnetpp	8	472	106	456	110	**468**	**107**	8	448	112	**456**	**110**	456	110
473.astar	8	480	117	469	120	**471**	**119**	8	480	117	469	120	**471**	**119**
483.xalancbmk	8	232	238	236	234	**232**	**238**	8	232	238	236	234	**232**	**238**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
   The taskset mechanism was used to bind copies to processors. The config file option 'submit'
   was used to generate taskset commands to bind each copy to a specific processor.
   For details, please see the config file.
```

Operating System Notes

```
   Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
   Environment variables set by runspec before the start of the run:
   LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

   Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
   memory using RedHat EL 6.4
   Transparent Huge Pages enabled with:
   echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
      icc  -m32
```

C++ benchmarks:
```
      icpc  -m32
```

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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

        458.sjeng: `-DSPEC_CPU_LP64`
    462.libquantum: `-DSPEC_CPU_LINUX`
    483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                   `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                   `-auto-ilp32`

       401.bzip2: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                  `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                  `-opt-prefetch -auto-ilp32 -ansi-alias`

         403.gcc: `-xCORE-AVX2 -ipo -O3 -no-prec-div`

         429.mcf: basepeak = yes

       445.gobmk: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`
                  `-ansi-alias -opt-mem-layout-trans=3`

       456.hmmer: `-xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

       458.sjeng: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                  `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                  `-unroll4 -auto-ilp32`

    462.libquantum: basepeak = yes

      464.h264ref: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                   `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                   `-unroll2 -ansi-alias`

C++ benchmarks:

      471.omnetpp: `-xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
                   `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
                   `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
                   `-L/sh -lsmartheap`

       473.astar: basepeak = yes

    483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revD.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revD.xml

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