



SPEC® CINT2006 Result

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Fujitsu

PRIMERGY RX4770 M1, Intel Xeon E7-4850 v2, 2.30 GHz

SPECint®_rate2006 = NC

SPECint_rate_base2006 = NC

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2014
Hardware Availability: Jun-2014
Software Availability: Sep-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by Fujitsu or Intel.

	Copies
400.perlbench	
401.bzip2	
403.gcc	
429.mcf	
445.gobmk	
456.hmmer	
458.sjeng	
462.libquantum	
464.h264ref	
471.omnetpp	
473.astar	
483.xalancbmk	

Non-Compliant

Hardware

CPU Name: Intel Xeon E7-4850 v2
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2300
FPU: Integrated
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core
CPU(s) orderable: 2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 24 MB I+D on chip per chip
Other Cache: None

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)
2.6.32-431.el6.x86_64
Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel: No
File System: ext4
System State: Run level 5 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit

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Memory: 512 GB (32 x 16 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM
Other Hardware: None

Other Software: Mellanox SmartHeap V10.0

Results Table

Benchmark	Base						Peak								
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	
400.perlbench	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
401.bzip2	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
403.gcc	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
429.mcf	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
445.gobmk	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
456.hmmer	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
458.sjeng	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
462.libquantum	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
464.h264ref	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
471.omnetpp	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
473.astar	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC
483.xalancbmk	48	NC	NC	NC	NC	NC	NC	48	NC	NC	NC	NC	NC	NC	NC

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Platform Notes

BIOS configuration:
Energy Performance = Performance

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006-MONITOR/libs/64:/SPECcpu2006-MONITOR/libs/64:/SPECcpu2006-MONITOR/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m32
C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prec-etch -opt-mem-layout-trans=3

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prec-etch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmath-heap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32
400.perlbenc: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbenc: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-pre-bench -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

45.gobmm: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

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Peak Optimization Flags (Continued)

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -g -f-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap
473.astar: basepeak = yes
483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

The flags that were used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.html>

You can also download the XML flags sources by saving the following links:
<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20131009.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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