



# SPEC® CINT2006 Result

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## HITACHI

SPECint®\_rate2006 = 874

Compute Blade 520H (Intel Xeon E5-2690 v2)

SPECint\_rate\_base2006 = 846

CPU2006 license: 35

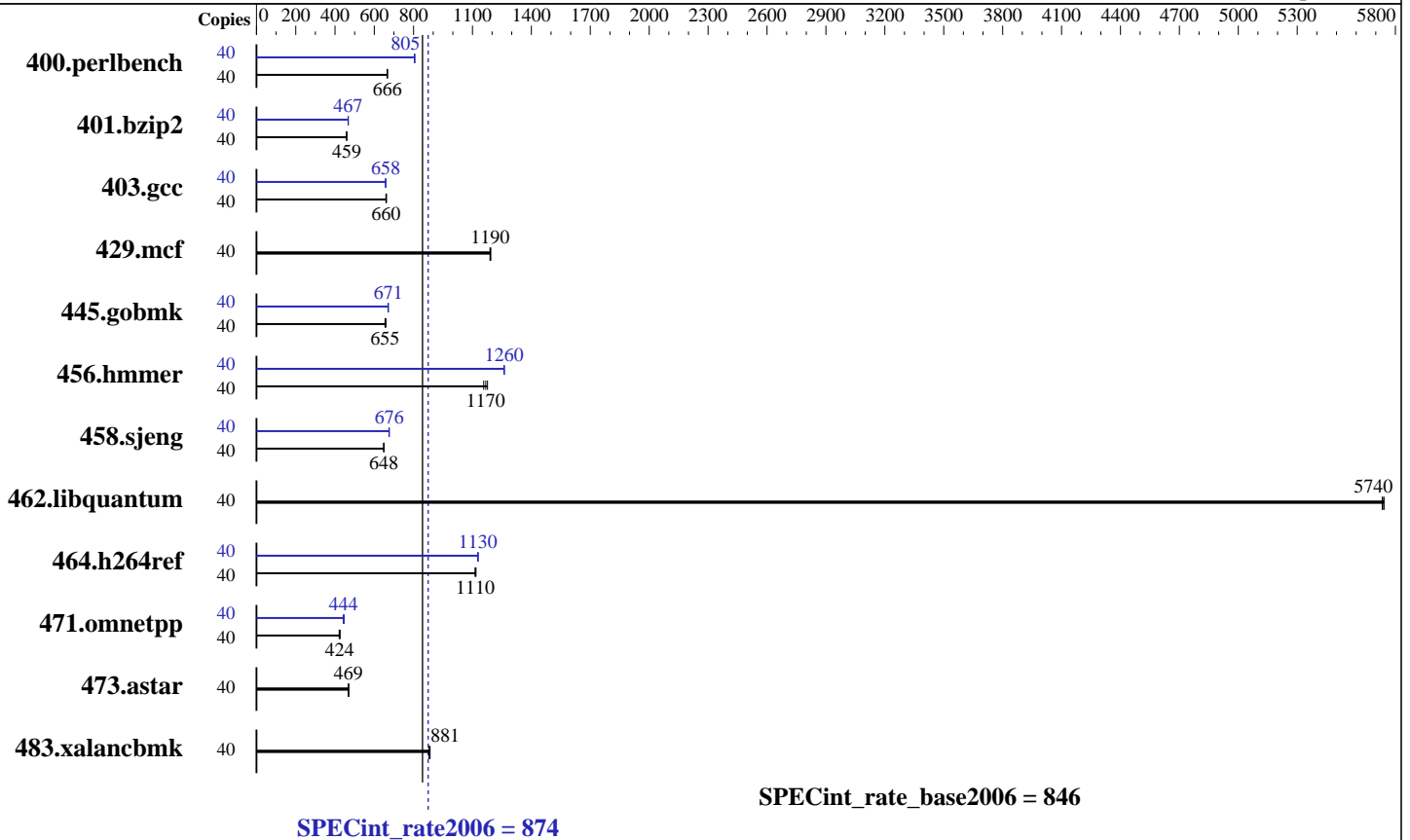
Test sponsor: HITACHI

Tested by: HITACHI

Test date: Mar-2014

Hardware Availability: Feb-2014

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2690 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (8 x 16 GB 2Rx4 PC3-14900R-13, ECC)

Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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### Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	586	667	<b><u>586</u></b>	<b><u>666</u></b>	586	666	40	486	804	<b><u>485</u></b>	<b><u>805</u></b>	484	807
401.bzip2	40	840	460	<b><u>840</u></b>	<b><u>459</u></b>	841	459	40	825	468	827	467	<b><u>826</u></b>	<b><u>467</u></b>
403.gcc	40	<b><u>488</u></b>	<b><u>660</u></b>	488	660	486	662	40	490	657	489	659	<b><u>490</u></b>	<b><u>658</u></b>
429.mcf	40	307	1190	306	1190	<b><u>307</u></b>	<b><u>1190</u></b>	40	307	1190	306	1190	<b><u>307</u></b>	<b><u>1190</u></b>
445.gobmk	40	641	655	636	660	<b><u>640</u></b>	<b><u>655</u></b>	40	<b><u>625</u></b>	<b><u>671</u></b>	626	671	625	671
456.hammer	40	<b><u>320</u></b>	<b><u>1170</u></b>	323	1160	317	1180	40	296	1260	<b><u>296</u></b>	<b><u>1260</u></b>	296	1260
458.sjeng	40	745	650	747	648	<b><u>747</u></b>	<b><u>648</u></b>	40	<b><u>716</u></b>	<b><u>676</u></b>	717	675	716	676
462.libquantum	40	144	5740	145	5730	<b><u>144</u></b>	<b><u>5740</u></b>	40	144	5740	145	5730	<b><u>144</u></b>	<b><u>5740</u></b>
464.h264ref	40	<b><u>794</u></b>	<b><u>1110</u></b>	795	1110	792	1120	40	786	1130	<b><u>785</u></b>	<b><u>1130</u></b>	784	1130
471.omnetpp	40	594	421	<b><u>590</u></b>	<b><u>424</u></b>	587	426	40	<b><u>563</u></b>	<b><u>444</u></b>	566	442	559	447
473.astar	40	<b><u>599</u></b>	<b><u>469</u></b>	597	470	601	467	40	<b><u>599</u></b>	<b><u>469</u></b>	597	470	601	467
483.xalancbmk	40	312	884	315	878	<b><u>313</u></b>	<b><u>881</u></b>	40	312	884	315	878	<b><u>313</u></b>	<b><u>881</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

### Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

### Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

### Platform Notes

Sysinfo program /home/cpu2006/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on localhost.localdomain Tue Mar 11 03:29:59 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2690 v2 @ 3.00GHz  
2 "physical id"s (chips)  
40 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 10  
siblings : 20

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### Platform Notes (Continued)

```
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 25600 KB
```

```
From /proc/meminfo
MemTotal:      132334256 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41
EST 2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 11 03:24
```

```
SPEC is set to: /home/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
ext4            133G    16G  110G  13% /
```

```
Additional information from dmidecode:
BIOS HITACHI EP1043 02/04/2014
Memory:
16x Not Specified Not Specified
8x Samsung M393B2G70QH0-CMA 16 GB 1867 MHz 2 rank
```

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
BladeSymphony BS520H and Hitachi Compute Blade 520H are electronically equivalent.  
The results have been measured on a BladeSymphony BS520H



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## Base Compiler Invocation

C benchmarks:

`icc -m32`

C++ benchmarks:

`icpc -m32`

## Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`  
462.libquantum: `-DSPEC_CPU_LINUX`  
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap`

## Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

## Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/sh -lsmartheap

473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.html>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revC.xml>

<http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.2.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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