



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint[®]_rate2006 = NC

Express5800/A2040b (Intel Xeon E7-4820 v2)

SPECint_rate_base2006 = NC

CPU2006 license: 9006

Test date: Mar-2014

Test sponsor: NEC Corporation

Hardware Availability: Mar-2014

Tested by: NEC Corporation

Software Availability: Oct-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by NEC or Intel.

- Copies
- 400.perlbench
- 401.bzip2
- 403.gcc
- 429.mcf
- 445.gobmk
- 456.hmmer
- 458.sjeng
- 462.libquantum
- 464.h264ref
- 471.omnetpp
- 473.astar
- 483.xalancbmk

Non-Compliant

Hardware

CPU Name: Intel Xeon E7-4820 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 2,3,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 16 MB I+D on chip per chip
 Other Cache: None

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Kernel 2.6.32-358.23.2.el6.x86_64
 Compiler: C/C++: Version 14.0.1.106 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = NC

Express5800/A2040b (Intel Xeon E7-4820 v2)

SPECint_rate_base2006 = NC

CPU2006 license: 9006

Test date: Mar-2014

Test sponsor: NEC Corporation

Hardware Availability: Mar-2014

Tested by: NEC Corporation

Software Availability: Oct-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by NEC or Intel.

Memory: 1 TB (64 x 16 GB 2Rx4 PC3L-12800R-11, ECC, running at 1333 MHz and CL9)
Disk Subsystem: 1 x 300 GB SAS, 10000 RPM
Other Hardware: None

Other Software: Mellinix SmartHeap Multi-Core V10.01

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
401.bzip2	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
403.gcc	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
429.mcf	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
445.gobmk	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
456.hmmer	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
458.sjeng	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
462.libquantum	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
464.h264ref	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
471.omnetpp	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
473.astar	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		
483.xalancbmk	64	NC	NC	NC	NC	NC	NC	64	NC	NC	NC	NC	NC	NC		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Zone reclaim mode enabled with:
echo 1 > /proc/sys/vm/zone_reclaim_mode



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = NC

Express5800/A2040b (Intel Xeon E7-4820 v2)

SPECint_rate_base2006 = NC

CPU2006 license: 9006

Test date: Mar-2014

Test sponsor: NEC Corporation

Hardware Availability: Mar-2014

Tested by: NEC Corporation

Software Availability: Oct-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by NEC or Intel.

Platform Notes

BIOS Settings:
Memory RAS Mode: Independent mode

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = */opt/SmartHeap_10mc/lib:/opt/SmartHeap_10mc/lib:/opt/intel/composer_xe_2013_sp1.1.106/compiler/lib/ia32:/opt/intel/composer_xe_2013_sp1.1.106/compiler/lib/intel64*

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec etc>

Base Compiler Invocation

C benchmarks:
icc -m32
C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
422.libquantum: -DSPEC_CPU_LINUX
483.galanchmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = **NC**

Express5800/A2040b (Intel Xeon E7-4820 v2)

SPECint_rate_base2006 = **NC**

CPU2006 license: 9006

Test date: Mar-2014

Test sponsor: NEC Corporation

Hardware Availability: Mar-2014

Tested by: NEC Corporation

Software Availability: Oct-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by NEC or Intel.

Base Optimization Flags (Continued)

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prec-etch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/opt/SmartHeap_10mc/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmcr: icc -m64
```

```
458.sjeng: icc -m64
```

```
462.libquantum: icc -m32
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmcr: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = NC

Express5800/A2040b (Intel Xeon E7-4820 v2)

SPECint_rate_base2006 = NC

CPU2006 license: 9006

Test date: Mar-2014

Test sponsor: NEC Corporation

Hardware Availability: Mar-2014

Tested by: NEC Corporation

Software Availability: Oct-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by NEC or Intel.

Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3

456.hmm: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll14 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/opt/SmartHeap_10mc/lib -lsmartheap
Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = NC

Express5800/A2040b (Intel Xeon E7-4820 v2)

SPECint_rate_base2006 = NC

CPU2006 license: 9006

Test date: Mar-2014

Test sponsor: NEC Corporation

Hardware Availability: Mar-2014

Tested by: NEC Corporation

Software Availability: Oct-2013

SPEC has determined that this result is not in compliance with the SPEC CPU2006 run and reporting rules. Specifically, the submitter has notified SPEC that the system was run with a BIOS which included a version of the Intel MRC (Memory Reference Code) that is not supported by NEC or Intel.

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-A2040b-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/NEC-platform-Settings-V1.2-A2040b-RevA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Fri Sep 19 15:47:37 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 April 2014.