



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint[®]_rate2006 = 291

Express5800/B120e-h (Intel Xeon E5-2650L v2)

SPECint_rate_base2006 = 279

CPU2006 license: 9006

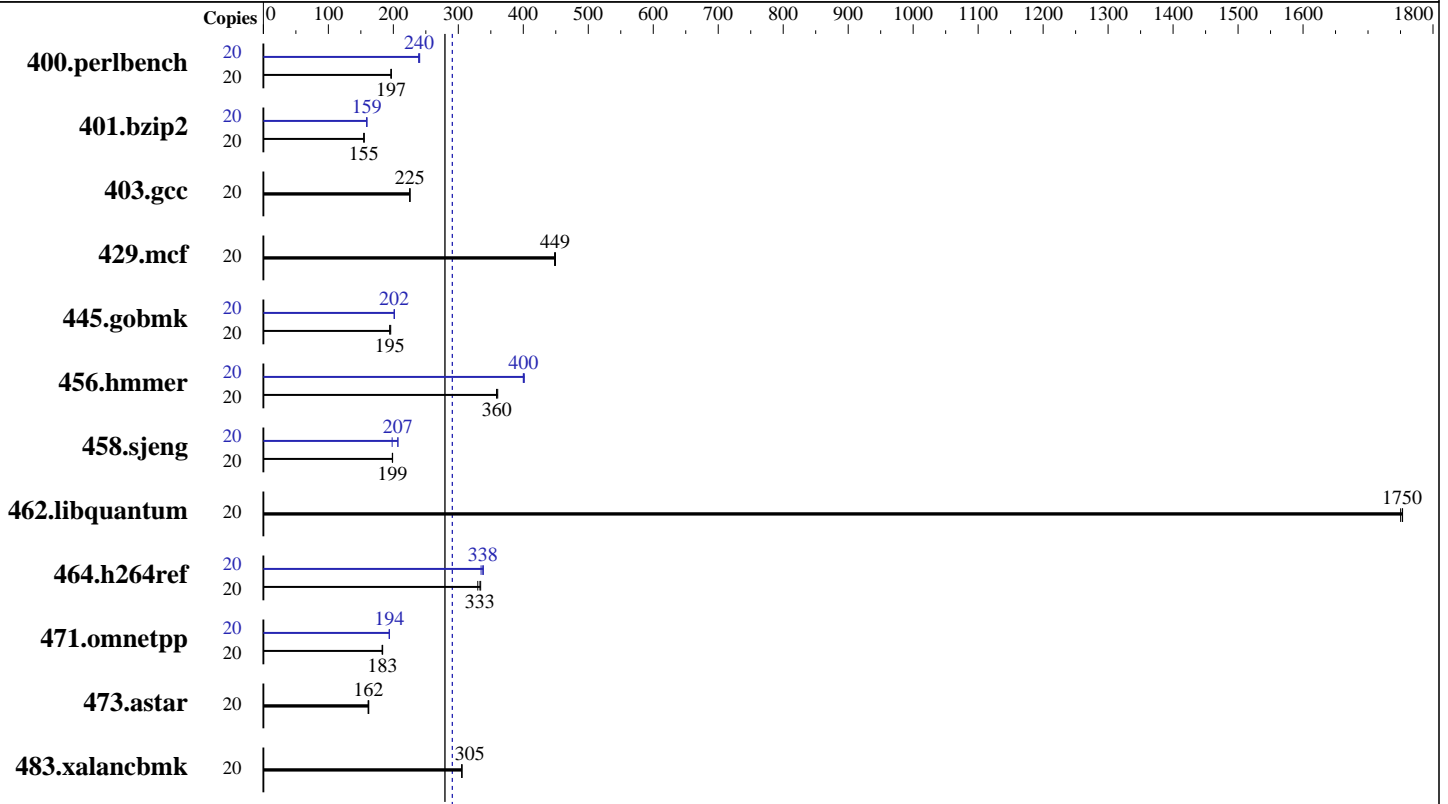
Test date: Dec-2013

Test sponsor: NEC Corporation

Hardware Availability: Sep-2013

Tested by: NEC Corporation

Software Availability: Sep-2013



SPECint_rate2006 = 291

SPECint_rate_base2006 = 279

Hardware

CPU Name: Intel Xeon E5-2650L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.10 GHz
 CPU MHz: 1700
 FPU: Integrated
 CPU(s) enabled: 10 cores, 1 chip, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 25 MB I+D on chip per chip
 Other Cache: None
 Memory: 128 GB (8 x 16 GB 2Rx4 PC3L-12800R-11, ECC)
 Disk Subsystem: 1 x 400 GB SATA, SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Kernel 2.6.32-358.18.1.el6.x86_64
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 291

Express5800/B120e-h (Intel Xeon E5-2650L v2)

SPECint_rate_base2006 = 279

CPU2006 license: 9006

Test date: Dec-2013

Test sponsor: NEC Corporation

Hardware Availability: Sep-2013

Tested by: NEC Corporation

Software Availability: Sep-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	20	995	196	994	197	992	197	20	812	241	818	239	815	240
401.bzip2	20	1247	155	1249	155	1244	155	20	1214	159	1212	159	1212	159
403.gcc	20	715	225	714	225	714	226	20	715	225	714	225	714	226
429.mcf	20	406	450	407	448	406	449	20	406	450	407	448	406	449
445.gobmk	20	1078	195	1071	196	1078	195	20	1041	202	1042	201	1041	202
456.hammer	20	519	360	517	361	520	359	20	465	402	466	400	467	400
458.sjeng	20	1218	199	1217	199	1219	198	20	1169	207	1169	207	1221	198
462.libquantum	20	236	1750	236	1750	237	1750	20	236	1750	236	1750	237	1750
464.h264ref	20	1324	334	1328	333	1341	330	20	1307	339	1321	335	1311	338
471.omnetpp	20	684	183	682	183	681	184	20	646	194	644	194	645	194
473.astar	20	866	162	868	162	871	161	20	866	162	868	162	871	161
483.xalancbmk	20	452	305	452	306	452	305	20	452	305	452	306	452	305

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Settings:
Energy Performance: Performance
Memory Voltage: 1.5 V

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 291

Express5800/B120e-h (Intel Xeon E5-2650L v2)

SPECint_rate_base2006 = 279

CPU2006 license: 9006

Test date: Dec-2013

Test sponsor: NEC Corporation

Hardware Availability: Sep-2013

Tested by: NEC Corporation

Software Availability: Sep-2013

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 291

Express5800/B120e-h (Intel Xeon E5-2650L v2)

SPECint_rate_base2006 = 279

CPU2006 license: 9006

Test date: Dec-2013

Test sponsor: NEC Corporation

Hardware Availability: Sep-2013

Tested by: NEC Corporation

Software Availability: Sep-2013

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/sh -lsmartheap

473.astar: basepeak = yes

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

SPECint_rate2006 = 291

Express5800/B120e-h (Intel Xeon E5-2650L v2)

SPECint_rate_base2006 = 279

CPU2006 license: 9006

Test date: Dec-2013

Test sponsor: NEC Corporation

Hardware Availability: Sep-2013

Tested by: NEC Corporation

Software Availability: Sep-2013

Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/NEC-Platform-Settings-V1.2-R120d-RevA.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 20:16:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 14 January 2014.