



SPEC® CINT2006 Result

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Dell Inc.

Dell Precision T3610 (Intel Xeon E5-1650 v2, 3.50 GHz)

SPECint®_rate2006 = 276

SPECint_rate_base2006 = 267

CPU2006 license: 55

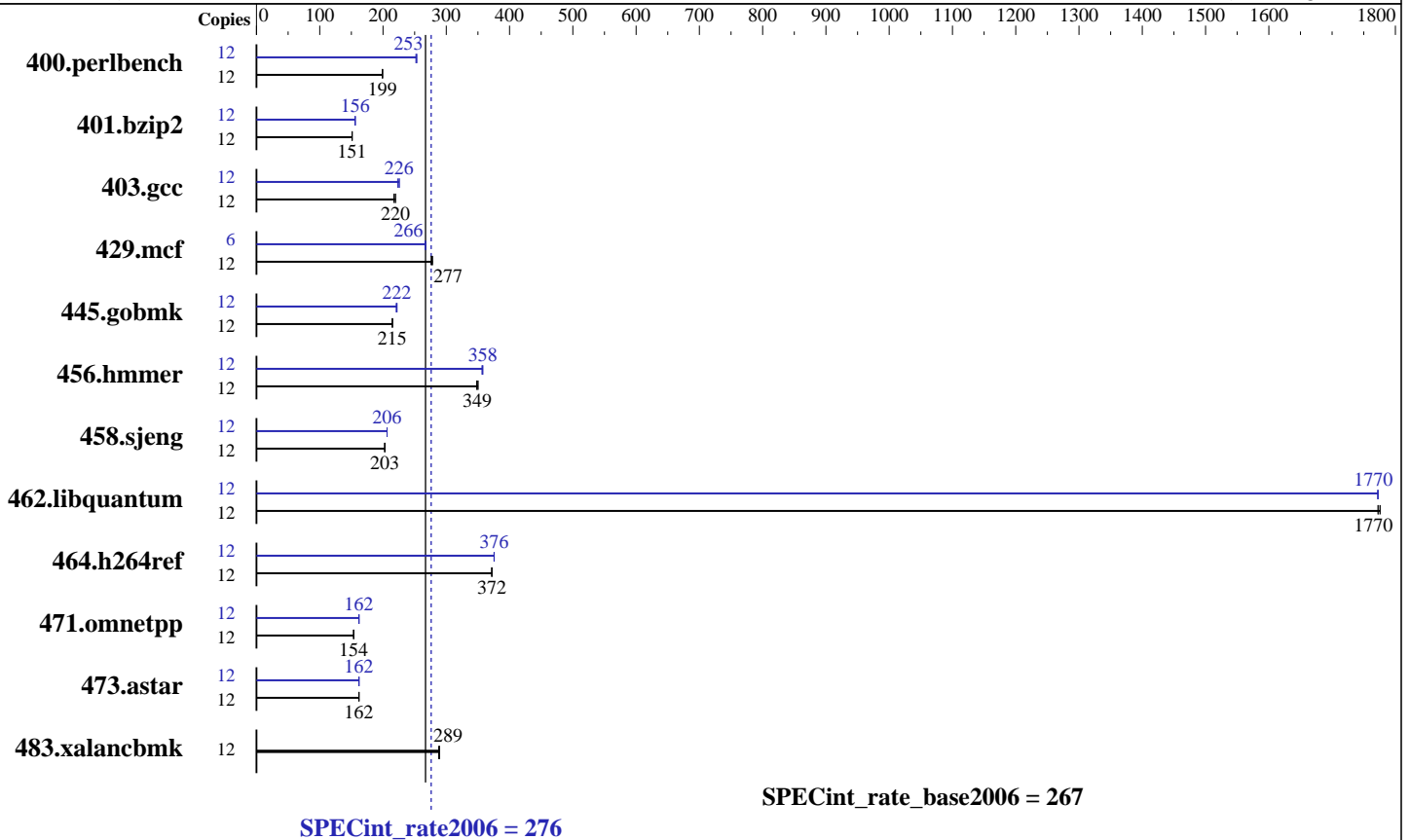
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Nov-2013

Hardware Availability: Sep-2013

Software Availability: Aug-2011



Hardware

CPU Name: Intel Xeon E5-1650 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 32 GB (8 x 4 GB 1Rx8 PC3-14900R-13, ECC)
 Disk Subsystem: 1 x 500 GB SATA III 7200 RPM
 Other Hardware: None

Software

Operating System: Windows 7 Professional (64-bit) SP1
 Compiler: C/C++: Version 12.1.0.233 of Intel C++ Studio XE for Windows
 C/C++: Version 2010 of Microsoft Visual Studio
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 10.0



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Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	12	588	199	588	199	588	199	12	465	252	464	253	463	253		
401.bzip2	12	763	151	763	151	765	151	12	743	156	743	156	743	156		
403.gcc	12	441	220	441	220	444	217	12	427	226	427	226	433	223		
429.mcf	12	393	278	394	277	397	276	6	204	268	206	266	206	266		
445.gobmk	12	586	215	586	215	587	215	12	569	221	568	222	568	222		
456.hammer	12	320	350	322	348	320	349	12	313	358	314	356	313	358		
458.sjeng	12	718	203	717	203	717	203	12	703	206	704	206	703	206		
462.libquantum	12	140	1780	140	1770	140	1770	12	140	1770	140	1770	140	1770		
464.h264ref	12	713	372	714	372	714	372	12	708	376	707	376	708	376		
471.omnetpp	12	488	154	487	154	488	154	12	463	162	462	162	462	162		
473.astar	12	519	162	519	162	520	162	12	519	162	519	162	519	162		
483.xalancbmk	12	286	289	286	289	287	288	12	286	289	286	289	287	288		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Set power configuration to High Performance and disabled monitor blanking and sleep timer

Platform Notes

```
Sysinfo program c:\cpu2006.v1.2/Docs/sysinfo
$Rev: 6775 $ $Date:: 2011-08-16 #$ \8787f7622badcf24e01c368b1db4377c
running on T3610-GLDN-PC Wed Nov 20 00:54:01 2013
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
Trying 'systeminfo'
OS Name       : Microsoft Windows 7 Professional
OS Version    : 6.1.7601 Service Pack 1 Build 7601
System Manufacturer: Dell Inc.
System Model   : Precision T3610
Processor(s)  : 1 Processor(s) Installed.
               [01]: Intel64 Family 6 Model 62 Stepping 4 GenuineIntel ~3501 Mhz
BIOS Version  : Dell Inc. A03, 9/5/2013
Total Physical Memory: 32,712 MB
```

Trying 'wmic cpu get /value'

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Platform Notes (Continued)

DeviceID : CPU0
L2CacheSize : 1536
L3CacheSize : 12288
MaxClockSpeed : 3501
Name : Intel(R) Xeon(R) CPU E5-1650 v2 @ 3.50GHz
NumberOfCores : 6
NumberOfLogicalProcessors : 12

(End of data from sysinfo program)

General Notes

Binaries were compiled on a Precision T1600 system with a single Xeon E3-1200 CPU and 16GB memory using Windows 7 Professional 64-bit

Base Compiler Invocation

C benchmarks:

icl -Qvc10 -Qstd=c99

C++ benchmarks:

icl -Qvc10

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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Peak Compiler Invocation

C benchmarks:
icl -Qvc10 -Qstd=c99

C++ benchmarks:
icl -Qvc10

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -QxAVX(pass 2) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias -Qopt-prefetch /F512000000
shlw32m.lib -link /FORCE:MULTIPLE

401.bzip2: -Qprof_gen(pass 1) -QxAVX(pass 2) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch -Qansi-alias /F512000000

403.gcc: -Qprof_gen(pass 1) -QxAVX(pass 2) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -Qprof_gen(pass 1) -QxAVX(pass 2) -Qprof_use(pass 2) -Qipo
-O2 -Qprec-div- -Qansi-alias /F512000000

456.hammer: Same as 403.gcc

458.sjeng: -Qprof_gen(pass 1) -QxAVX(pass 2) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll14 /F512000000

462.libquantum: Same as 429.mcf

464.h264ref: -Qprof_gen(pass 1) -QxAVX(pass 2) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qunroll12 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -QxAVX(pass 2) -Qprof_use(pass 2) -Qipo
-O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

```
473.astar: -QxAVX -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000
          shlw32m.lib -link /FORCE:MULTIPLE
```

```
483.xalancbmk: basepeak = yes
```

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Dell-ic12.1-Windows.html>

<http://www.spec.org/cpu2006/flags/Dell-platform-Precision.20131217.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Dell-ic12.1-Windows.xml>

<http://www.spec.org/cpu2006/flags/Dell-platform-Precision.20131217.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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