



# SPEC® CINT2006 Result

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## IBM Corporation

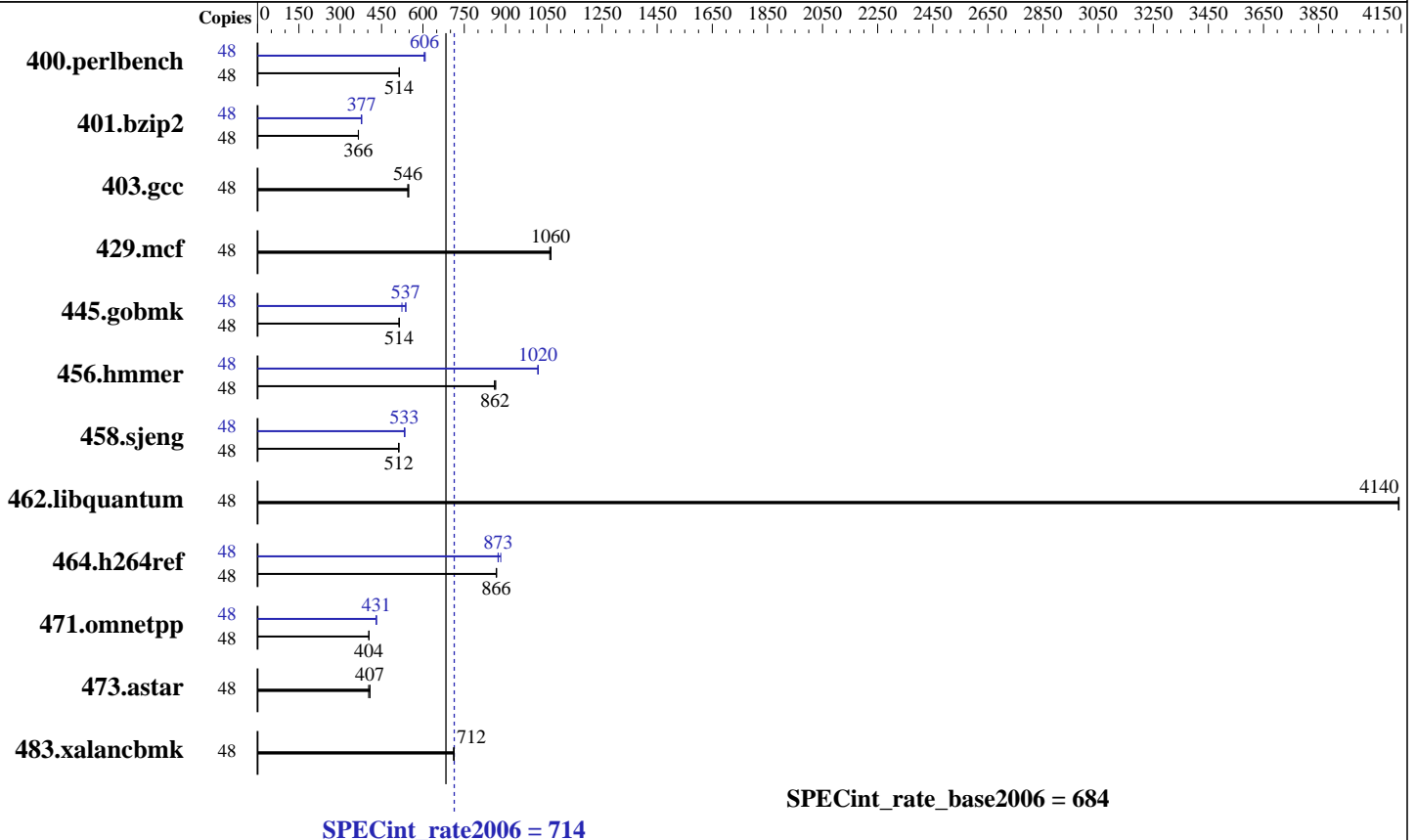
IBM Flex System x440  
(Intel Xeon E5-4607, 2.20 GHz)

**SPECint®\_rate2006 = 714**

**SPECint\_rate\_base2006 = 684**

**CPU2006 license:** 11  
**Test sponsor:** IBM Corporation  
**Tested by:** IBM Corporation

**Test date:** Aug-2012  
**Hardware Availability:** Aug-2012  
**Software Availability:** Dec-2011



### Hardware

CPU Name: Intel Xeon E5-4607  
 CPU Characteristics:  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,3,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1066 MHz)  
 Disk Subsystem: 1 x 300 GB SAS, 10000 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)  
 2.6.32-220.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	48	913	514	911	514	<u>912</u>	<u>514</u>	48	775	605	771	608	<u>774</u>	<u>606</u>
401.bzip2	48	<u>1266</u>	<u>366</u>	1265	366	1267	366	48	1230	376	<u>1227</u>	<u>377</u>	1227	378
403.gcc	48	<u>708</u>	<u>546</u>	704	549	708	545	48	<u>708</u>	<u>546</u>	704	549	708	545
429.mcf	48	411	1060	<u>412</u>	<u>1060</u>	413	1060	48	411	1060	<u>412</u>	<u>1060</u>	413	1060
445.gobmk	48	<u>979</u>	<u>514</u>	981	513	978	515	48	961	524	<u>937</u>	<u>537</u>	936	538
456.hammer	48	<u>520</u>	<u>862</u>	521	859	518	864	48	<u>440</u>	<u>1020</u>	440	1020	440	1020
458.sjeng	48	1131	513	1136	511	<u>1134</u>	<u>512</u>	48	<u>1090</u>	<u>533</u>	1086	535	1091	532
462.libquantum	48	240	4140	240	4140	<u>240</u>	<u>4140</u>	48	240	4140	240	4140	<u>240</u>	<u>4140</u>
464.h264ref	48	1227	866	<u>1226</u>	<u>866</u>	1224	868	48	1217	873	1204	882	<u>1217</u>	<u>873</u>
471.omnetpp	48	742	404	743	404	<u>742</u>	<u>404</u>	48	696	431	<u>696</u>	<u>431</u>	696	431
473.astar	48	826	408	<u>827</u>	<u>407</u>	836	403	48	826	408	<u>827</u>	<u>407</u>	836	403
483.xalancbmk	48	<u>465</u>	<u>712</u>	465	712	464	713	48	<u>465</u>	<u>712</u>	465	712	464	713

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Operating Mode set to Maximum Performance in BIOS  
Sysinfo program /cpu2006.1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on congo-pete Thu Aug 9 17:30:36 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-4607 0 @ 2.20GHz  
4 "physical id"s (chips)  
48 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 6

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## Platform Notes (Continued)

```

siblings      : 12
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
physical 2: cores 0 1 2 3 4 5
physical 3: cores 0 1 2 3 4 5
cache size   : 12288 KB

```

From /proc/meminfo

```

MemTotal:      264503712 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.2 (Santiago)

From /etc/\*release\* /etc/\*version\*

```

redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

```

uname -a:

```

Linux congo-pete 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Aug 9 17:25

SPEC is set to: /cpu2006.1.2

```

Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_congopete-lv_root
                ext4      264G  5.9G  245G   3% /

```

Additional information from dmidecode:

Memory:

32x Micron 36JSF1G72PZ-1G6M1 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006.1.2/libs/32:/cpu2006.1.2/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>



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## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>  
<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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