



SPEC® CINT2006 Result

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Supermicro

SPECint®_rate2006 = 174

Motherboard X9SCA-F (Intel Xeon E3-1265L v2)

SPECint_rate_base2006 = 167

CPU2006 license: 001176

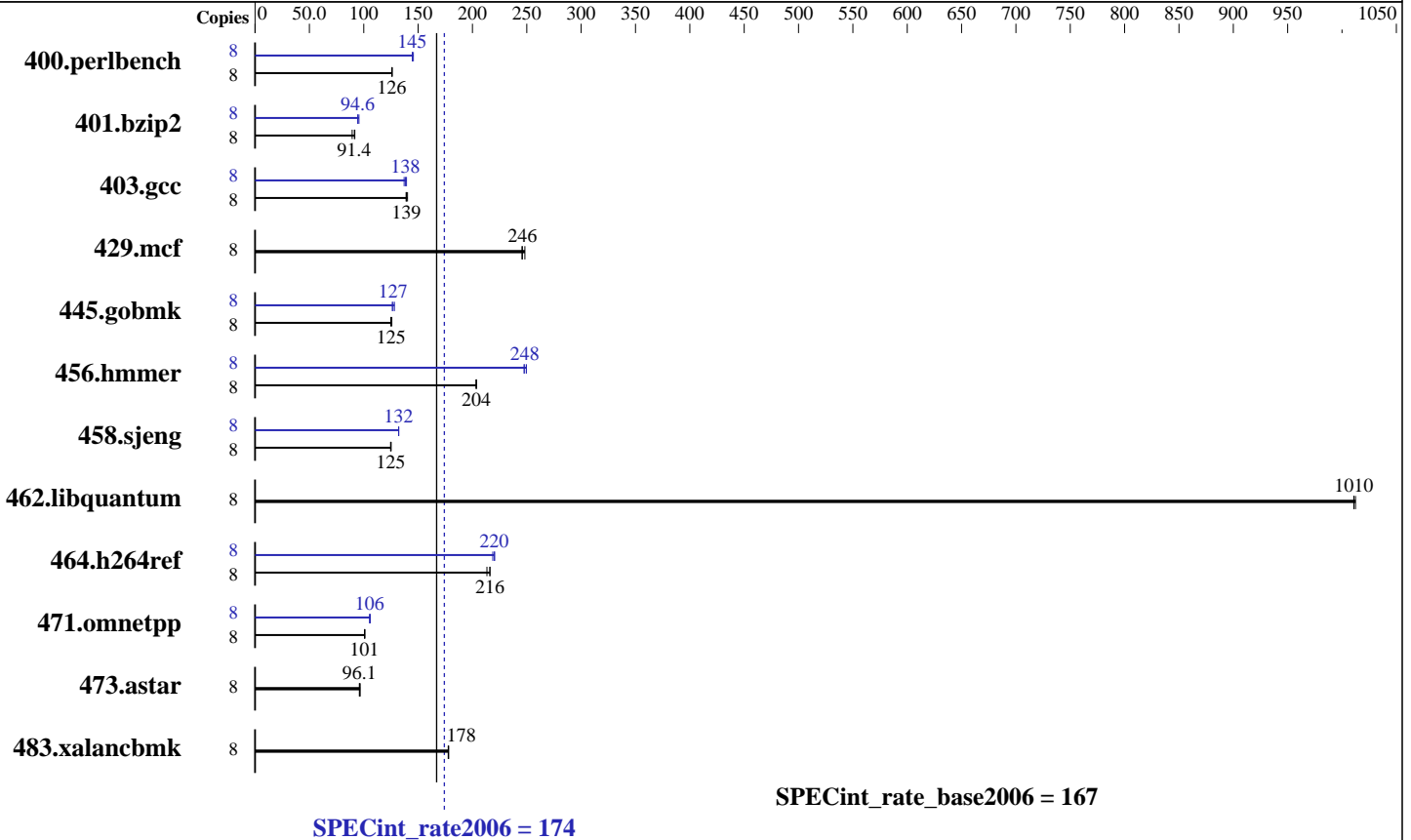
Test date: Jul-2012

Test sponsor: Supermicro

Hardware Availability: May-2012

Tested by: Supermicro

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E3-1265L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago), Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	<u>621</u>	<u>126</u>	621	126	619	126	8	<u>540</u>	<u>145</u>	540	145	537	146
401.bzip2	8	<u>845</u>	<u>91.4</u>	841	91.8	864	89.3	8	<u>816</u>	<u>94.6</u>	808	95.6	817	94.5
403.gcc	8	459	140	<u>463</u>	<u>139</u>	463	139	8	462	139	<u>466</u>	<u>138</u>	470	137
429.mcf	8	<u>297</u>	<u>246</u>	297	246	294	248	8	<u>297</u>	<u>246</u>	297	246	294	248
445.gobmk	8	672	125	<u>670</u>	<u>125</u>	667	126	8	665	126	<u>661</u>	<u>127</u>	654	128
456.hammer	8	367	203	367	204	<u>367</u>	<u>204</u>	8	299	250	<u>301</u>	<u>248</u>	302	247
458.sjeng	8	776	125	<u>775</u>	<u>125</u>	773	125	8	732	132	<u>733</u>	<u>132</u>	733	132
462.libquantum	8	164	1010	<u>164</u>	<u>1010</u>	164	1010	8	164	1010	<u>164</u>	<u>1010</u>	164	1010
464.h264ref	8	819	216	<u>819</u>	<u>216</u>	830	213	8	809	219	803	220	<u>804</u>	<u>220</u>
471.omnetpp	8	497	101	<u>496</u>	<u>101</u>	494	101	8	472	106	474	105	<u>474</u>	<u>106</u>
473.astar	8	581	96.7	585	96.0	<u>585</u>	<u>96.1</u>	8	581	96.7	585	96.0	<u>585</u>	<u>96.1</u>
483.xalancbmk	8	311	178	310	178	<u>310</u>	<u>178</u>	8	311	178	310	178	<u>310</u>	<u>178</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Platform Notes

As tested, the system used a Supermicro CSE-111T-560CB chassis.
The system includes a PWS-562-1H power supply, SNK-P0046P heatsink, and 3 FAN-0106L4 cooling fans.
Sysinfo program /home/cpu2006/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 # \$ 6f2ebdff5032aaa42e583f96b07f99d3
running on 103-81.inet Tue Jul 10 19:30:59 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1265L V2 @ 2.50GHz
1 "physical id"s (chips)
8 "processors"

Continued on next page



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Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings  : 8
physical 0: cores 0 1 2 3
cache size : 8192 KB
```

From /proc/meminfo

```
MemTotal:      16304916 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

/usr/bin/lsb_release -d

```
Red Hat Enterprise Linux Server release 6.2 (Santiago)
```

From /etc/*release* /etc/*version*

```
redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux 103-81.inet 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Jul 10 19:30

SPEC is set to: /home/cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_10381-lv_home
                ext4      392G  45G  328G  12% /home
```

Additional information from dmidecode:

```
Memory:
2x Hynix/Hyundai HMT41GU7MFR8C-PB 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
```

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5



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Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
           -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
           -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
             -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

```



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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