



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 153

Acer AT110 F2, (Intel Xeon E3-1220 v2, 3.10 GHz)

SPECint_rate_base2006 = 147

CPU2006 license: 97

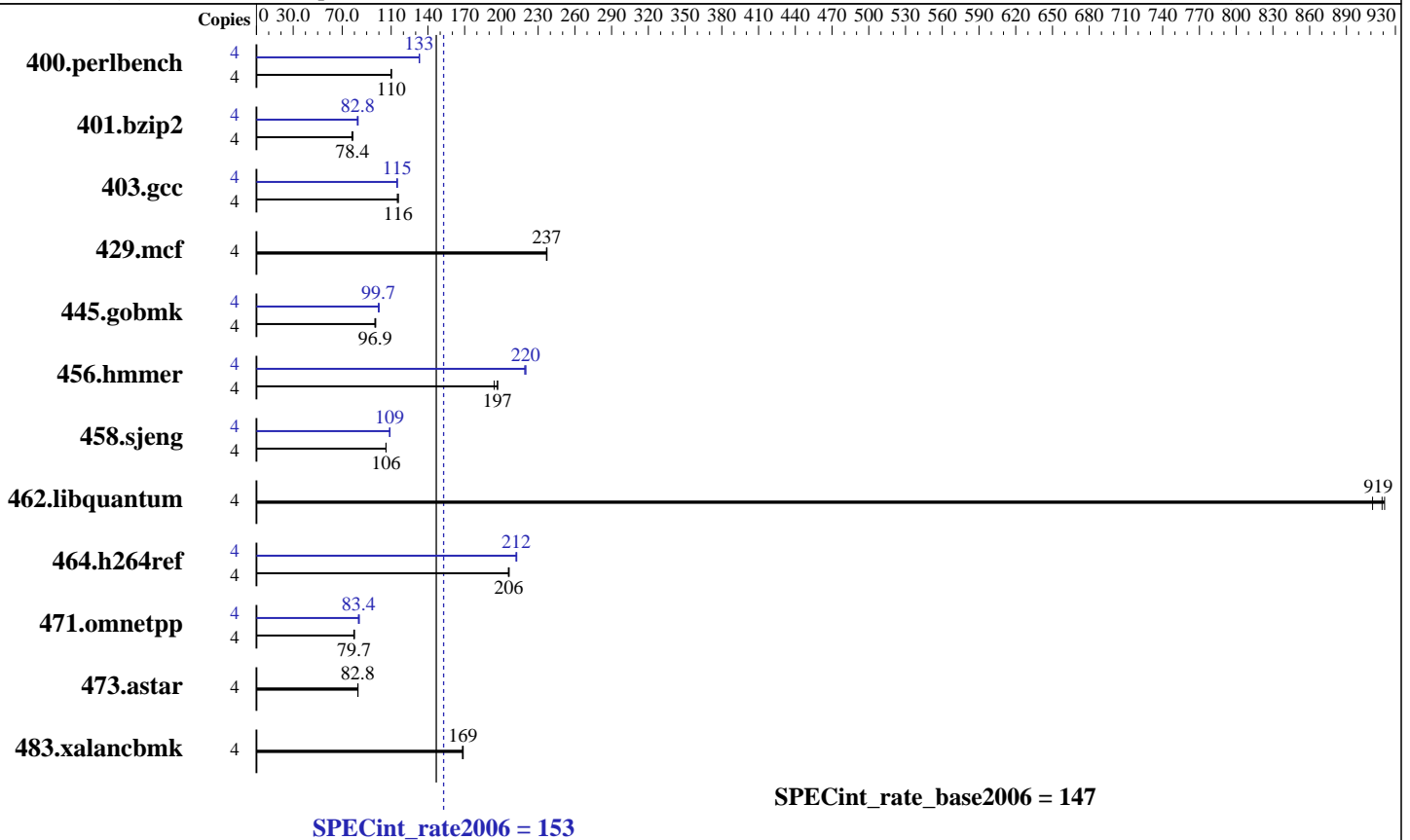
Test date: Jun-2012

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2012

Tested by: Acer Incorporated

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E3-1220 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 3100
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3L-10600E-9-11-E0, ECC)
 Disk Subsystem: 1 x 500 GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.0 (Santiago)
 2.6.32-71.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	355	110	<u>355</u>	<u>110</u>	356	110	4	<u>294</u>	<u>133</u>	294	133	294	133
401.bzip2	4	490	78.7	<u>493</u>	<u>78.4</u>	495	77.9	4	<u>466</u>	<u>82.8</u>	469	82.3	465	83.0
403.gcc	4	278	116	<u>278</u>	<u>116</u>	280	115	4	280	115	281	115	<u>280</u>	<u>115</u>
429.mcf	4	154	237	<u>154</u>	<u>237</u>	154	237	4	154	237	<u>154</u>	<u>237</u>	154	237
445.gobmk	4	431	97.4	<u>433</u>	<u>96.9</u>	433	96.8	4	<u>421</u>	<u>99.7</u>	421	99.6	419	100
456.hammer	4	<u>190</u>	<u>197</u>	192	194	189	197	4	<u>170</u>	<u>220</u>	170	219	170	220
458.sjeng	4	458	106	457	106	<u>457</u>	<u>106</u>	4	<u>445</u>	<u>109</u>	444	109	445	109
462.libquantum	4	90.0	921	<u>90.2</u>	<u>919</u>	90.9	911	4	90.0	921	<u>90.2</u>	<u>919</u>	90.9	911
464.h264ref	4	<u>430</u>	<u>206</u>	430	206	429	206	4	417	212	<u>417</u>	<u>212</u>	416	213
471.omnetpp	4	312	80.0	315	79.5	<u>314</u>	<u>79.7</u>	4	300	83.4	298	84.0	<u>300</u>	<u>83.4</u>
473.astar	4	<u>339</u>	<u>82.8</u>	339	82.9	339	82.7	4	<u>339</u>	<u>82.8</u>	339	82.9	339	82.7
483.xalancbmk	4	164	168	163	169	<u>164</u>	<u>169</u>	4	164	168	163	169	<u>164</u>	<u>169</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /user/cpu2006/config/sysinfo.rev6800
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3
running on localhost.localdomain Fri Jun 22 17:57:41 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E3-1220 V2 @ 3.10GHz
1 "physical id"s (chips)
4 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings : 4
```

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Platform Notes (Continued)

```
physical 0: cores 0 1 2 3
cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      16437540 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.0 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.0 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.0 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-71.el6.x86_64 #1 SMP Wed Sep 1 01:33:01
EDT 2010 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 22 17:56
```

```
SPEC is set to: /user/cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda2       ext3      197G  8.9G  179G   5% /
```

```
Additional information from dmidecode:
BIOS Acer D22 06/18/2012
Memory:
 2x 830B NT8GC72C8PB0NF-CG 8 GB 1333 MHz 2 rank
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/user/cpu2006/libs/32:/user/cpu2006/libs/64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
 -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
 -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

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