



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint®_rate2006 = 377

Servidor Itautec MX223+ (Intel Xeon X5660)

SPECint_rate_base2006 = 358

CPU2006 license: 9001

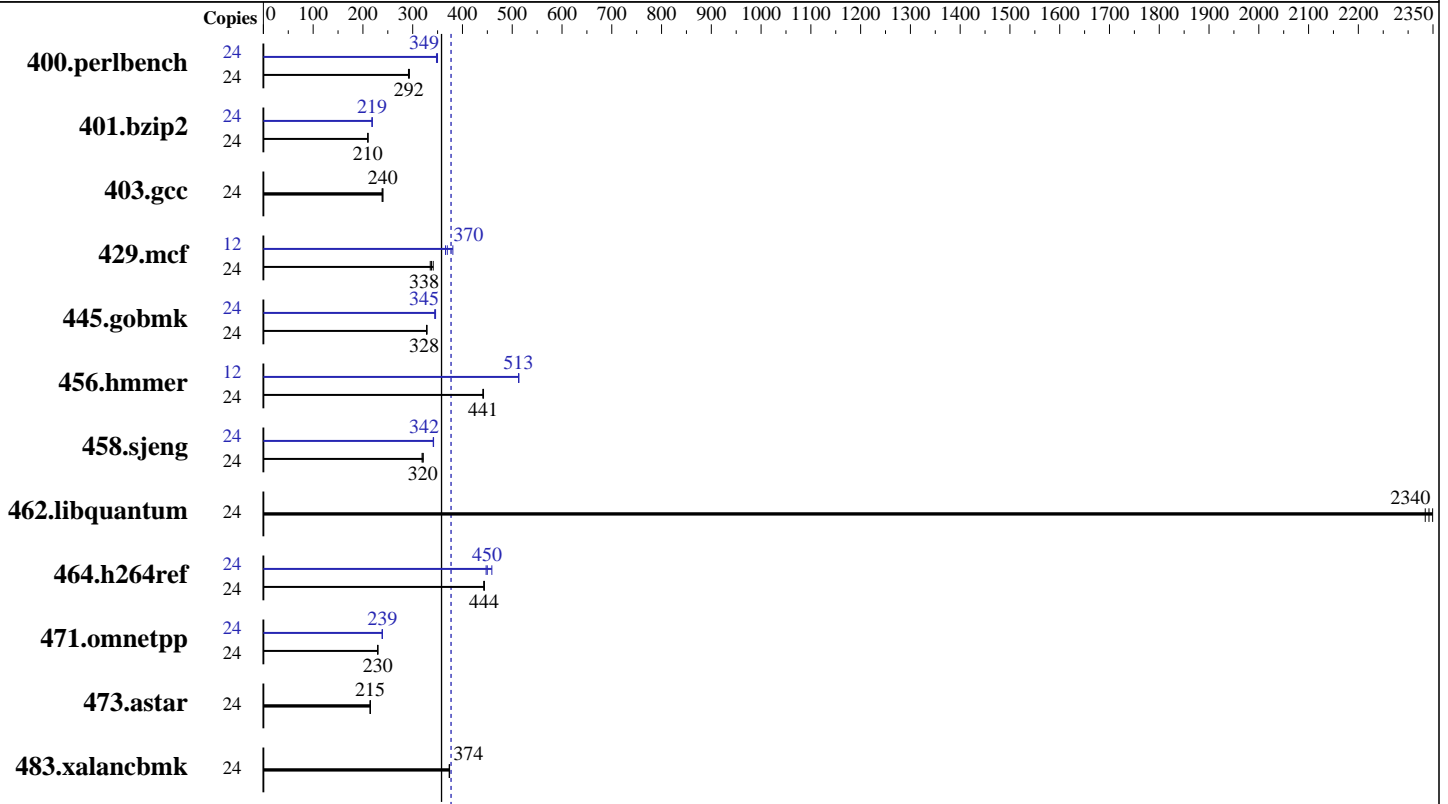
Test date: Dec-2011

Test sponsor: Itautec

Hardware Availability: Jul-2011

Tested by: Itautec

Software Availability: Aug-2011



SPECint_rate2006 = 377

SPECint_rate_base2006 = 358

Hardware

CPU Name: Intel Xeon X5660
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 500 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: C/C++: Version 12.1.0 of Intel Compiler XE Build 20110811
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 377

Servidor Itaotec MX223+ (Intel Xeon X5660)

SPECint_rate_base2006 = 358

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Dec-2011
Hardware Availability: Jul-2011
Software Availability: Aug-2011

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|-------------|------------|------------|------------|-------------|-------------|--------|------------|------------|-------------|------------|------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 24 | 802 | 292 | 804 | 292 | 799 | 293 | 24 | 674 | 348 | 670 | 350 | 671 | 349 |
| 401.bzip2 | 24 | 1103 | 210 | 1102 | 210 | 1104 | 210 | 24 | 1060 | 219 | 1060 | 219 | 1060 | 218 |
| 403.gcc | 24 | 806 | 240 | 810 | 238 | 805 | 240 | 24 | 806 | 240 | 810 | 238 | 805 | 240 |
| 429.mcf | 24 | 641 | 341 | 648 | 338 | 653 | 335 | 12 | 287 | 381 | 296 | 370 | 299 | 366 |
| 445.gobmk | 24 | 768 | 328 | 767 | 328 | 766 | 329 | 24 | 731 | 345 | 728 | 346 | 730 | 345 |
| 456.hammer | 24 | 507 | 441 | 507 | 441 | 506 | 442 | 12 | 218 | 513 | 218 | 513 | 218 | 513 |
| 458.sjeng | 24 | 911 | 319 | 904 | 321 | 906 | 320 | 24 | 849 | 342 | 851 | 341 | 850 | 342 |
| 462.libquantum | 24 | 213 | 2330 | 212 | 2350 | 212 | 2340 | 24 | 213 | 2330 | 212 | 2350 | 212 | 2340 |
| 464.h264ref | 24 | 1200 | 443 | 1196 | 444 | 1197 | 444 | 24 | 1157 | 459 | 1181 | 450 | 1187 | 448 |
| 471.omnetpp | 24 | 653 | 230 | 652 | 230 | 653 | 230 | 24 | 627 | 239 | 627 | 239 | 628 | 239 |
| 473.astar | 24 | 785 | 214 | 785 | 215 | 784 | 215 | 24 | 785 | 214 | 785 | 215 | 784 | 215 |
| 483.xalancbmk | 24 | 443 | 374 | 443 | 374 | 444 | 373 | 24 | 443 | 374 | 443 | 374 | 444 | 373 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
Large pages were not enabled for this run

Platform Notes

Data Reuse disabled in BIOS.

General Notes

This result was measured on the Servidor Itaotec MX224.
The Servidor Itaotec MX203+, Servidor Itaotec MX223+ and the Servidor Itaotec MX224 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icc -m32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 377

Servidor Itaotec MX223+ (Intel Xeon X5660)

SPECint_rate_base2006 = 358

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Dec-2011
Hardware Availability: Jul-2011
Software Availability: Aug-2011

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint_rate2006 = 377

Servidor Itaotec MX223+ (Intel Xeon X5660)

SPECint_rate_base2006 = 358

CPU2006 license: 9001
Test sponsor: Itaotec
Tested by: Itaotec

Test date: Dec-2011
Hardware Availability: Jul-2011
Software Availability: Aug-2011

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmarheap

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint_rate2006 = 377

Servidor Itautec MX223+ (Intel Xeon X5660)

SPECint_rate_base2006 = 358

CPU2006 license: 9001

Test date: Dec-2011

Test sponsor: Itautec

Hardware Availability: Jul-2011

Tested by: Itautec

Software Availability: Aug-2011

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Thu Jul 24 03:15:50 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 3 January 2012.