



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

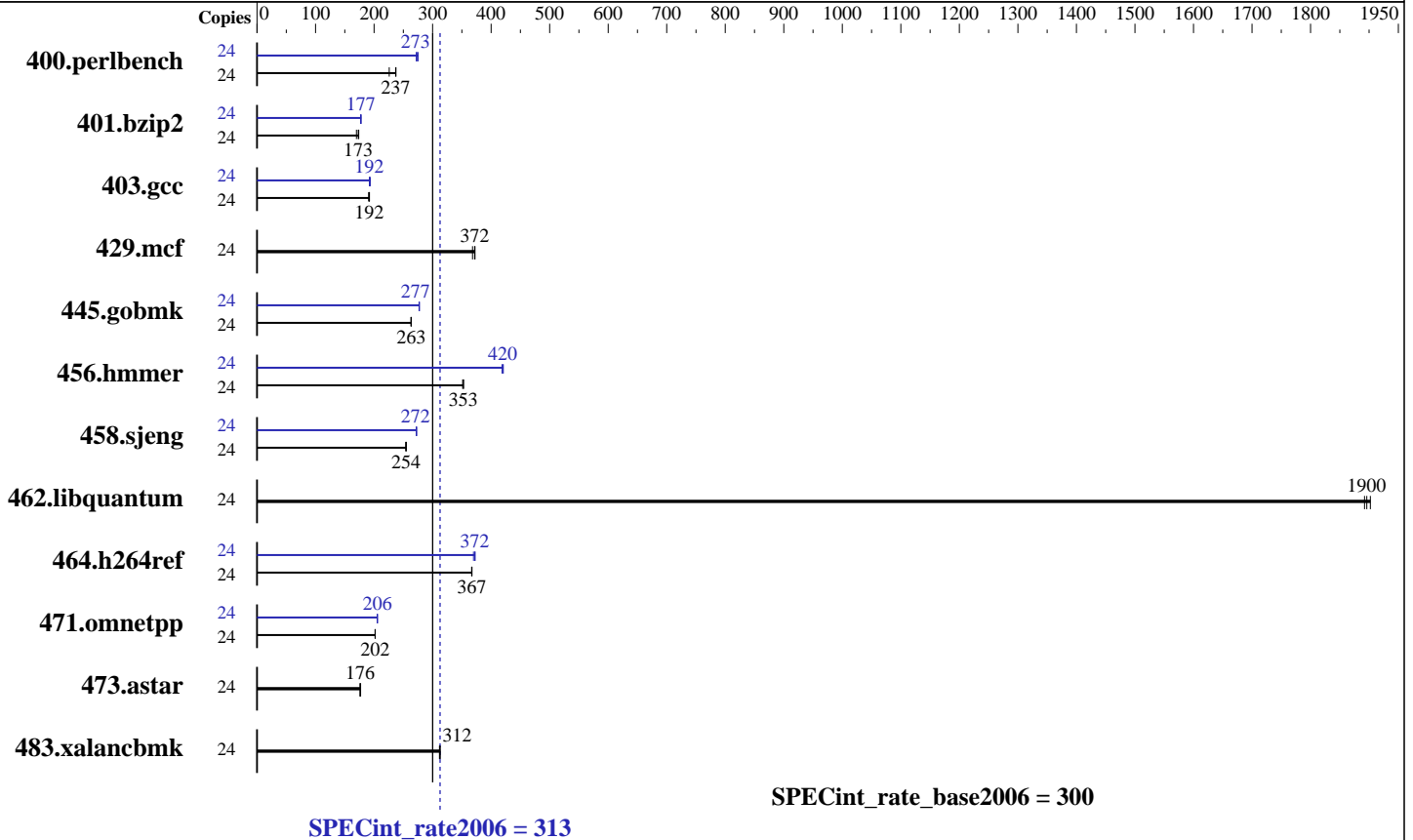
SPECint®_rate2006 = 313

ProLiant DL380 G7
(2.26 GHz, Intel Xeon L5640)

SPECint_rate_base2006 = 300

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Oct-2011
Hardware Availability: Mar-2010
Software Availability: Sep-2011



Hardware

CPU Name: Intel Xeon L5640
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 146 GB 15 K SAS
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel Compiler XE Build 20110803
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 313

ProLiant DL380 G7
(2.26 GHz, Intel Xeon L5640)

SPECint_rate_base2006 = 300

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Oct-2011
Hardware Availability: Mar-2010
Software Availability: Sep-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	990	237	988	237	1038	226	24	852	275	858	273	862	272
401.bzip2	24	1363	170	1340	173	1333	174	24	1302	178	1306	177	1309	177
403.gcc	24	1007	192	1014	191	1006	192	24	1005	192	1006	192	997	194
429.mcf	24	594	368	589	372	588	372	24	594	368	589	372	588	372
445.gobmk	24	956	263	956	263	956	263	24	908	277	908	277	909	277
456.hammer	24	637	351	635	353	635	353	24	532	421	535	419	533	420
458.sjeng	24	1138	255	1141	254	1143	254	24	1067	272	1067	272	1065	273
462.libquantum	24	261	1900	263	1890	262	1900	24	261	1900	263	1890	262	1900
464.h264ref	24	1447	367	1446	367	1448	367	24	1436	370	1428	372	1425	373
471.omnetpp	24	743	202	743	202	744	202	24	729	206	730	206	730	206
473.astar	24	957	176	956	176	952	177	24	957	176	956	176	952	177
483.xalancbmk	24	530	312	531	312	529	313	24	530	312	531	312	529	313

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Platform Notes

BIOS configuration:
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling
Data Reuse set to Disabled

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/cpu2006/smartheap/cpu2006/ic12.1-libs/ia32/:/cpu2006/ic12.1-libs/intel64"



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 313

ProLiant DL380 G7
(2.26 GHz, Intel Xeon L5640)

SPECint_rate_base2006 = 300

CPU2006 license: 3

Test date: Oct-2011

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2010

Tested by: Hewlett-Packard Company

Software Availability: Sep-2011

Base Compiler Invocation

C benchmarks:

`icc -m32`

C++ benchmarks:

`icpc -m32`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3`

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/smartheap -lsmartheap`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m32`

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 313

ProLiant DL380 G7
(2.26 GHz, Intel Xeon L5640)

SPECint_rate_base2006 = 300

CPU2006 license: 3

Test date: Oct-2011

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2010

Tested by: Hewlett-Packard Company

Software Availability: Sep-2011

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -auto-ilp32

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

SPECint_rate2006 = 313

ProLiant DL380 G7
(2.26 GHz, Intel Xeon L5640)

SPECint_rate_base2006 = 300

CPU2006 license: 3

Test date: Oct-2011

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2010

Tested by: Hewlett-Packard Company

Software Availability: Sep-2011

Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.html>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20111122.xml>

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Thu Jul 24 01:05:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 12 December 2011.