



SPEC[®] CINT2006 Result

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IBM Corporation

SPECint[®]_rate2006 = 139

IBM System x3250 M4 (Intel Xeon E3-1260L)

SPECint_rate_base2006 = 133

CPU2006 license: 11

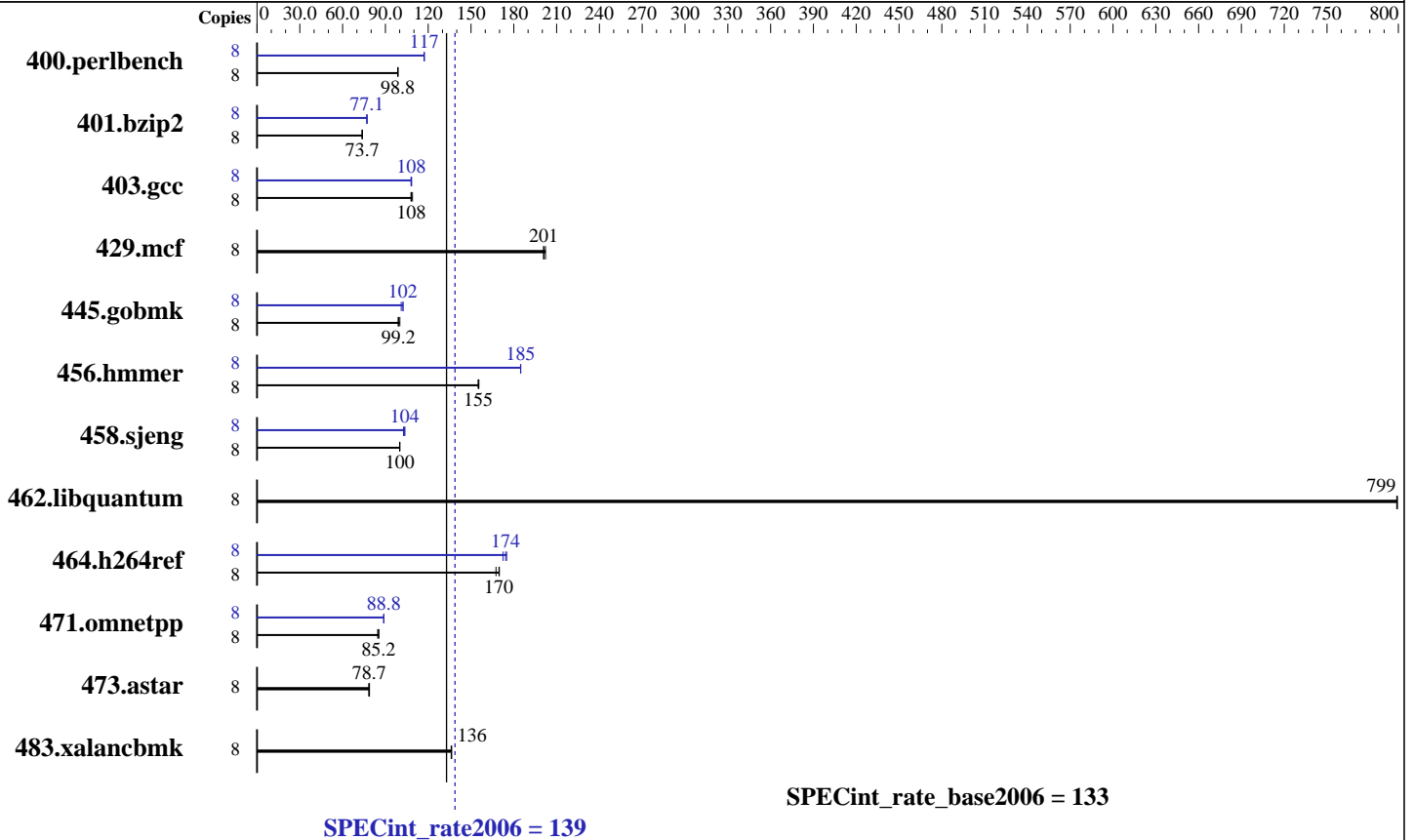
Test date: Oct-2011

Test sponsor: IBM Corporation

Hardware Availability: Oct-2011

Tested by: IBM Corporation

Software Availability: Aug-2011



Hardware

CPU Name: Intel Xeon E3-1260L
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x 146 GB SAS, 15000 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel Compiler XE Build 20110803
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	792	98.7	<u>791</u>	<u>98.8</u>	791	98.8	8	<u>667</u>	<u>117</u>	668	117	666	117
401.bzip2	8	1047	73.7	<u>1047</u>	<u>73.7</u>	1046	73.8	8	<u>1001</u>	<u>77.1</u>	1001	77.1	1003	77.0
403.gcc	8	596	108	<u>596</u>	<u>108</u>	591	109	8	<u>595</u>	<u>108</u>	594	108	597	108
429.mcf	8	<u>363</u>	<u>201</u>	361	202	363	201	8	<u>363</u>	<u>201</u>	361	202	363	201
445.gobmk	8	848	99.0	<u>846</u>	<u>99.2</u>	838	100	8	820	102	<u>821</u>	<u>102</u>	830	101
456.hammer	8	<u>481</u>	<u>155</u>	481	155	480	156	8	404	185	<u>404</u>	<u>185</u>	404	185
458.sjeng	8	967	100	<u>967</u>	<u>100</u>	968	100	8	943	103	934	104	<u>935</u>	<u>104</u>
462.libquantum	8	<u>207</u>	<u>799</u>	207	799	207	799	8	<u>207</u>	<u>799</u>	207	799	207	799
464.h264ref	8	<u>1044</u>	<u>170</u>	1042	170	1057	168	8	1012	175	1027	172	<u>1016</u>	<u>174</u>
471.omnetpp	8	585	85.4	<u>587</u>	<u>85.2</u>	591	84.6	8	<u>563</u>	<u>88.8</u>	562	89.0	563	88.8
473.astar	8	712	78.8	<u>713</u>	<u>78.7</u>	717	78.3	8	712	78.8	<u>713</u>	<u>78.7</u>	717	78.3
483.xalancbmk	8	<u>405</u>	<u>136</u>	406	136	405	136	8	<u>405</u>	<u>136</u>	406	136	405	136

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind copies to the cores

Platform Notes

BIOS Settings:
Turbo Mode enabled in BIOS
C-State enabled in BIOS

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/SPECcpu12.1/smartheap:/root/SPECcpu12.1/ic12.1-libs/ia32:/root/SPECcpu12.1/ic12.1-libs/intel64"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5 with binutils-2.17.50.0.6-14.el5
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>



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Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

-Wl,-z,muldefs -L/smartheap -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
 -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
 -opt-ra-region-strategy=block -Wl,-z,muldefs
 -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.html>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revB.20111206.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-linux64.xml>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revB.20111206.xml>

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For other inquiries, please contact webmaster@spec.org.

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