SPEC® CINT2006 Result

Itautec

Servidor Itautec MX214 (Intel Xeon X5660)

SPECint®_rate2006 = 194

SPECint_rate_base2006 = 183

CPU2006 license: 9001　　　　　　　　　　　　　　　　　　　　　**Test date:** Aug-2011
Test sponsor: Itautec　　　　　　　　　　　　　　　　　　　**Hardware Availability:** Jul-2011
Tested by: Itautec　　　　　　　　　　　　　　　　　　　　**Software Availability:** Jan-2011

Copies | 0 30.0 70.0 110 140 170 200 230 260 290 320 350 380 410 440 470 500 530 560 590 620 650 680 710 740 770 800 830 860 890 930

- 400.perlbench: 12 — 188; 12 — 155
- 401.bzip2: 12 — 116; 12 — 108
- 403.gcc: 12 — 131; 12 — 130
- 429.mcf: 6 — 198; 12 — 165
- 445.gobmk: 12 — 185; 12 — 175
- 456.hmmer: 6 — 263; 12 — 233
- 458.sjeng: 12 — 181; 12 — 170
- 462.libquantum: 12 — 925
- 464.h264ref: 12 — 247; 12 — 248
- 471.omnetpp: 12 — 122; 12 — 118
- 473.astar: 12 — 112
- 483.xalancbmk: 12 — 183

SPECint_rate_base2006 = 183

SPECint_rate2006 = 194

Hardware

CPU Name:	Intel Xeon X5660
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2800
FPU:	Integrated
CPU(s) enabled:	6 cores, 1 chip, 6 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	24 GB (6 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	1 x 500 GB SATA-2, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.2 Build 20110112
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

SPEC CINT2006 Result
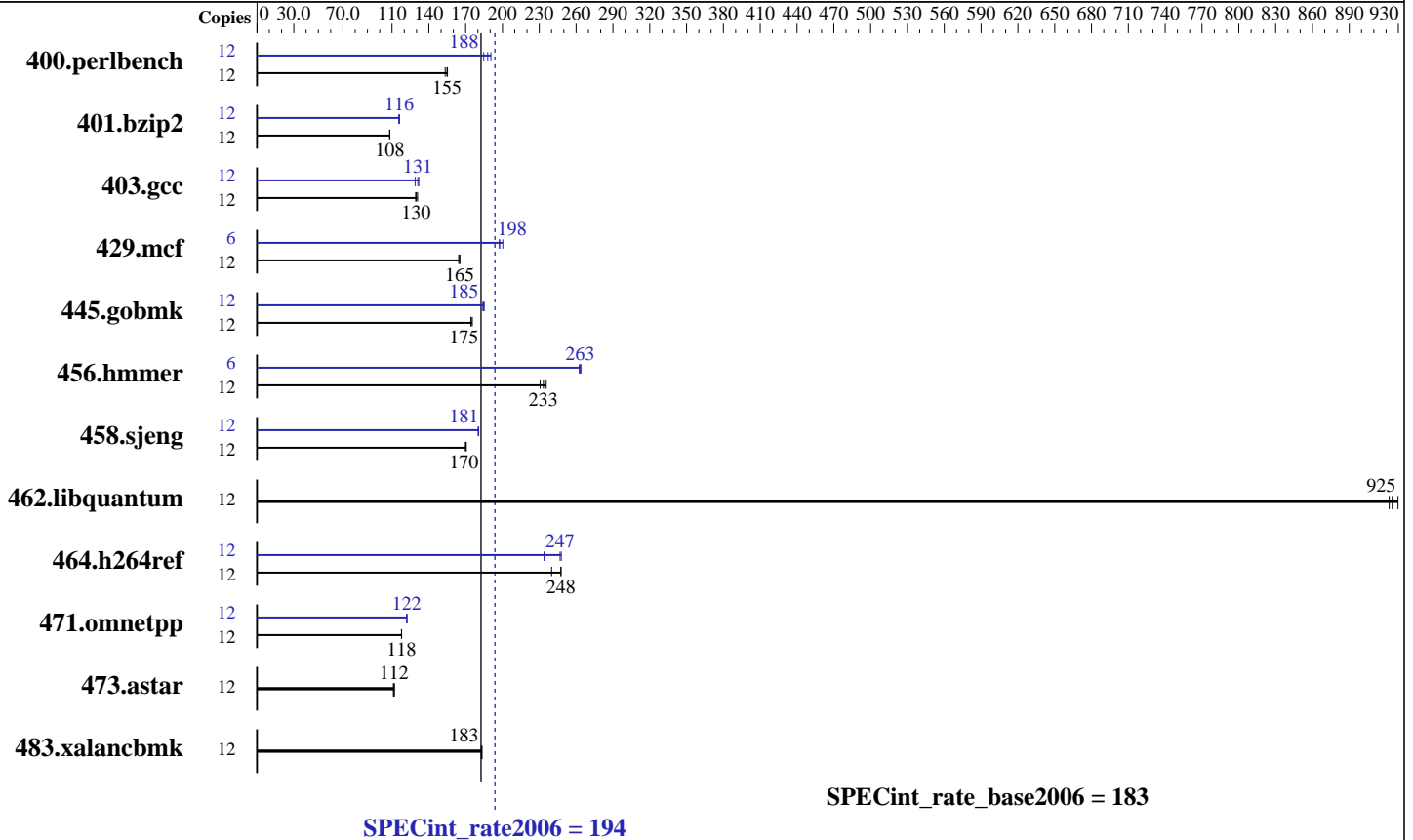Copyright 2006-2014 Standard Performance Evaluation Corporation

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Results Table

Benchmark	Base						Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	755	155	765	153	**757**	**155**	12	635	185	615	191	**624**	**188**
401.bzip2	12	1070	108	1072	108	**1070**	**108**	12	**1000**	**116**	1002	116	997	116
403.gcc	12	**745**	**130**	738	131	747	129	12	**737**	**131**	733	132	750	129
429.mcf	12	666	164	**663**	**165**	663	165	6	**277**	**198**	277	198	273	200
445.gobmk	12	718	175	**721**	**175**	722	174	12	**682**	**185**	680	185	684	184
456.hmmer	12	475	236	485	231	**480**	**233**	6	**213**	**263**	213	263	212	264
458.sjeng	12	855	170	**853**	**170**	852	171	12	804	181	**804**	**181**	806	180
462.libquantum	12	270	923	267	930	**269**	**925**	12	270	923	267	930	**269**	**925**
464.h264ref	12	1106	240	1072	248	**1072**	**248**	12	1071	248	**1075**	**247**	1135	234
471.omnetpp	12	637	118	637	118	**637**	**118**	12	614	122	**615**	**122**	615	122
473.astar	12	753	112	**754**	**112**	758	111	12	753	112	**754**	**112**	758	111
483.xalancbmk	12	453	183	**452**	**183**	452	183	12	453	183	**452**	**183**	452	183

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
     Large pages were not enabled for this run

Platform Notes

    Data Reuse disabled in BIOS.

Base Compiler Invocation

C benchmarks:
     icc -m32

C++ benchmarks:
     icpc -m32

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Base Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

        -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
        -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

        -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
        -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
        -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
        icc -m32

        400.perlbench: icc -m64

        401.bzip2: icc -m64

        456.hmmer: icc -m64

        458.sjeng: icc -m64

C++ benchmarks:
        icpc -m32

Peak Portability Flags

        400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
        401.bzip2: -DSPEC_CPU_LP64

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Tested by: Itautec		**Software Availability:**	Jan-2011

Peak Portability Flags (Continued)

```
   456.hmmer: -DSPEC_CPU_LP64
   458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
 400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

       403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
               -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

       429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

     445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

     456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

     458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
```

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Hardware Availability: Jul-2011

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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