



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECint®\_rate2006 = 43.8

PRIMERGY RX100 S7, Intel Celeron G530T, 2.0 GHz

SPECint\_rate\_base2006 = 41.4

CPU2006 license: 19

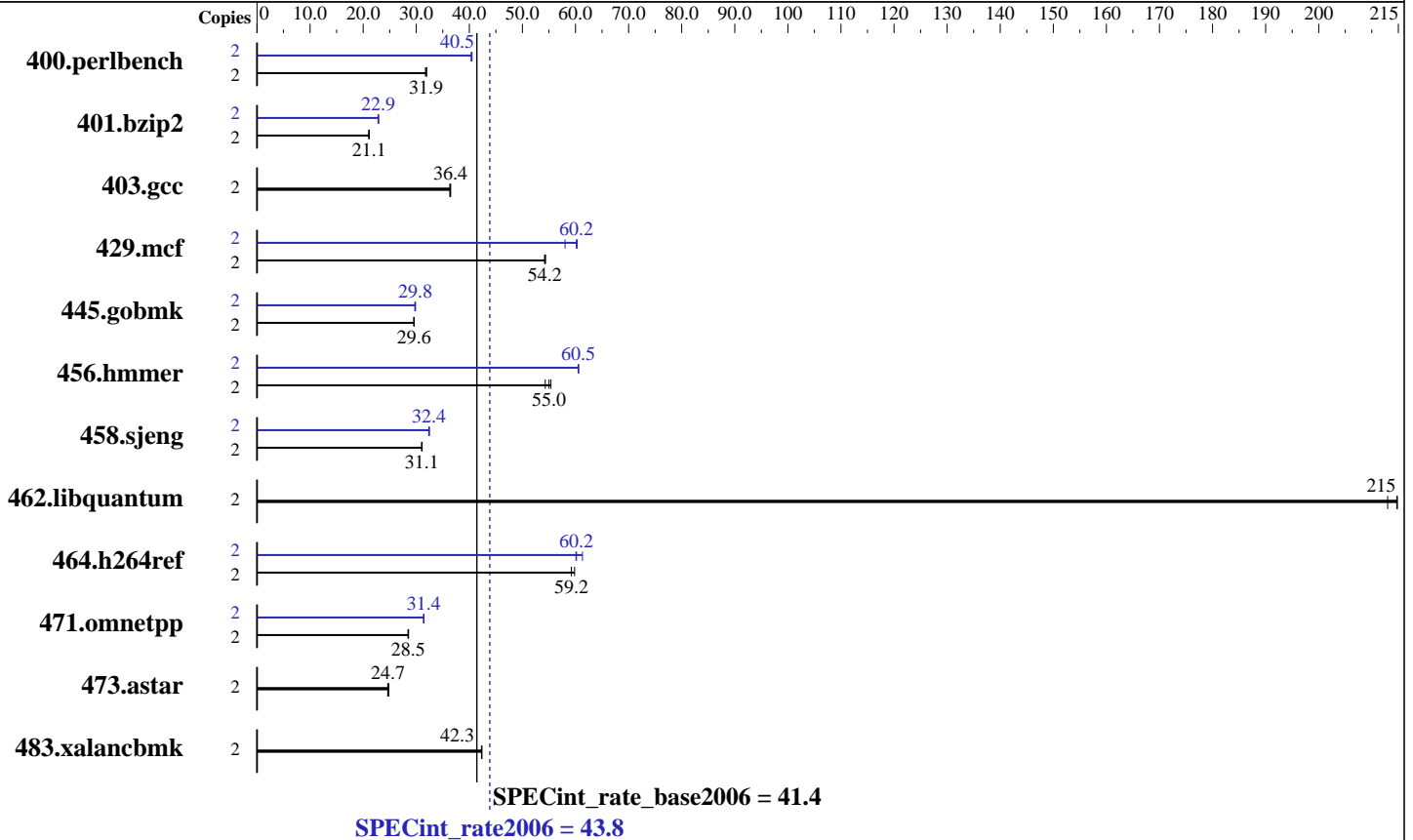
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2011

Hardware Availability: Sep-2011

Software Availability: Jul-2011



### Hardware

CPU Name: Intel Celeron G530T  
 CPU Characteristics: 2000  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 2 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC, running at 1067 MHz and CL7)  
 Disk Subsystem: 1 x SATA, 300 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Fujitsu

SPECint\_rate2006 = 43.8

PRIMERGY RX100 S7, Intel Celeron G530T, 2.0 GHz

SPECint\_rate\_base2006 = 41.4

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu

Test date: Sep-2011  
Hardware Availability: Sep-2011  
Software Availability: Jul-2011

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	612	32.0	<b>613</b>	<b>31.9</b>	616	31.7	2	483	40.5	485	40.3	<b>483</b>	<b>40.5</b>
401.bzip2	2	917	21.0	<b>915</b>	<b>21.1</b>	912	21.2	2	847	22.8	842	22.9	<b>844</b>	<b>22.9</b>
403.gcc	2	441	36.5	<b>442</b>	<b>36.4</b>	443	36.3	2	441	36.5	<b>442</b>	<b>36.4</b>	443	36.3
429.mcf	2	336	54.4	<b>336</b>	<b>54.2</b>	337	54.2	2	<b>303</b>	<b>60.2</b>	314	58.0	302	60.3
445.gobmk	2	<b>709</b>	<b>29.6</b>	709	29.6	710	29.5	2	706	29.7	<b>703</b>	<b>29.8</b>	703	29.8
456.hammer	2	344	54.3	<b>339</b>	<b>55.0</b>	337	55.4	2	308	60.6	<b>308</b>	<b>60.5</b>	308	60.5
458.sjeng	2	778	31.1	781	31.0	<b>779</b>	<b>31.1</b>	2	<b>746</b>	<b>32.4</b>	748	32.3	745	32.5
462.libquantum	2	193	215	195	213	<b>193</b>	<b>215</b>	2	193	215	195	213	<b>193</b>	<b>215</b>
464.h264ref	2	748	59.2	740	59.8	<b>747</b>	<b>59.2</b>	2	722	61.3	<b>735</b>	<b>60.2</b>	737	60.1
471.omnetpp	2	438	28.6	<b>439</b>	<b>28.5</b>	439	28.5	2	<b>398</b>	<b>31.4</b>	399	31.3	398	31.4
473.astar	2	<b>568</b>	<b>24.7</b>	566	24.8	570	24.6	2	<b>568</b>	<b>24.7</b>	566	24.8	570	24.6
483.xalancbmk	2	<b>326</b>	<b>42.3</b>	326	42.4	326	42.3	2	<b>326</b>	<b>42.3</b>	326	42.4	326	42.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Large pages were not enabled for this run

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on RHEL 5.5

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 43.8

PRIMERGY RX100 S7, Intel Celeron G530T, 2.0 GHz

SPECint\_rate\_base2006 = 41.4

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2011

Hardware Availability: Sep-2011

Software Availability: Jul-2011

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 43.8

PRIMERGY RX100 S7, Intel Celeron G530T, 2.0 GHz

SPECint\_rate\_base2006 = 41.4

CPU2006 license: 19  
Test sponsor: Fujitsu  
Tested by: Fujitsu

Test date: Sep-2011  
Hardware Availability: Sep-2011  
Software Availability: Jul-2011

## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzp2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/smartheap -lsmartheap

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

SPECint\_rate2006 = 43.8

PRIMERGY RX100 S7, Intel Celeron G530T, 2.0 GHz

SPECint\_rate\_base2006 = 41.4

CPU2006 license: 19

Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2011

Hardware Availability: Sep-2011

Software Availability: Jul-2011

## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20110705.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20110705.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 21:37:16 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 27 September 2011.