SPEC® CINT2006 Result
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Itautec

Servidor Itautec MX214 (Intel Xeon E5620)

SPECint®_rate2006 =   233

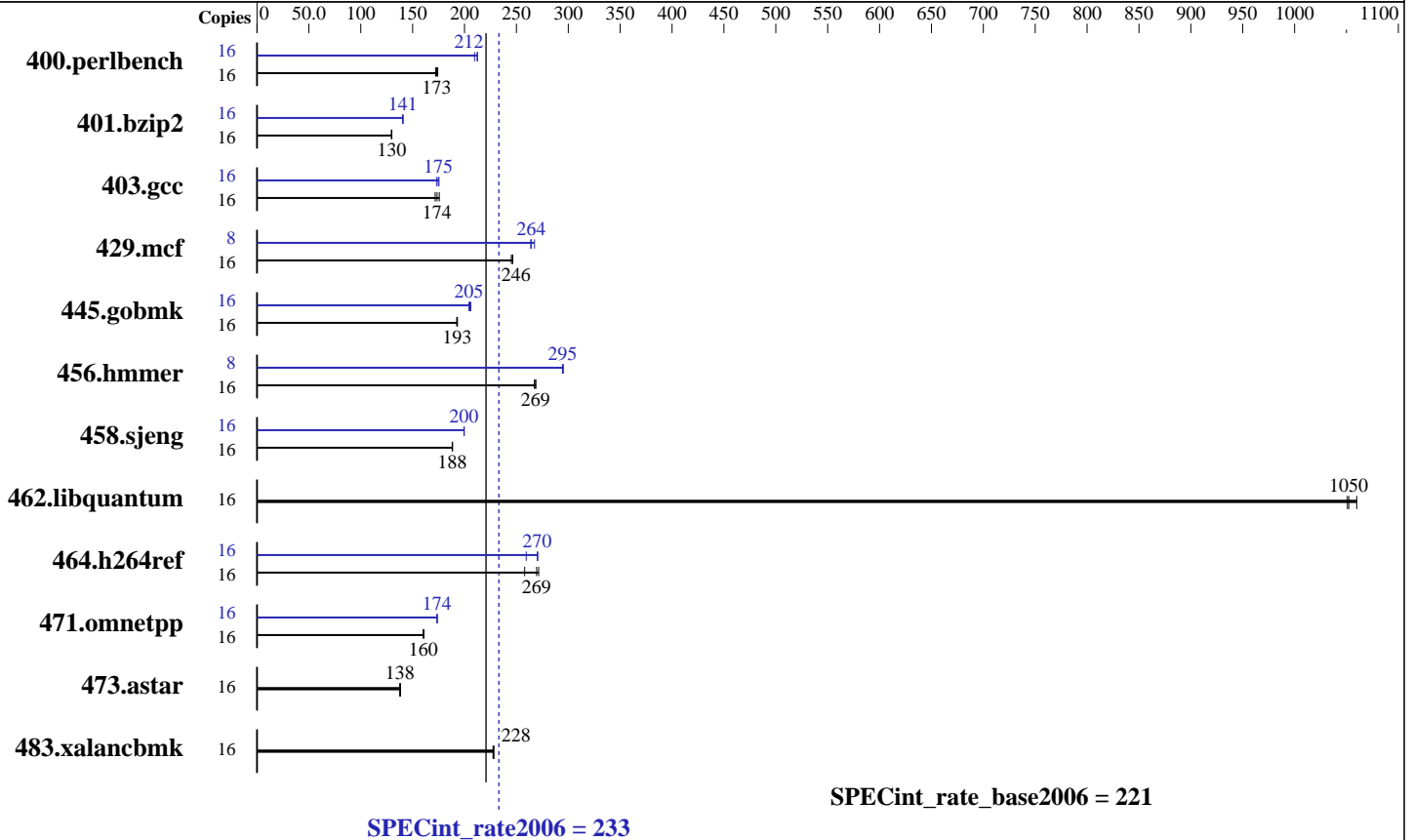SPECint_rate_base2006 = 221

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Mar-2011
Hardware Availability: Apr-2011
Software Availability: Jan-2011

Copies

Benchmark	Copies	Value
400.perlbench	16	212
	16	173
401.bzip2	16	141
	16	130
403.gcc	16	175
	16	174
429.mcf	8	264
	16	246
445.gobmk	16	205
	16	193
456.hmmer	8	295
	16	269
458.sjeng	16	200
	16	188
462.libquantum	16	1050
464.h264ref	16	270
	16	269
471.omnetpp	16	174
	16	160
473.astar	16	138
483.xalancbmk	16	228

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Hardware
CPU Name: Intel Xeon E5620
CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 12 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6 x 4 GB 2Rx4 PC3-8500R-7, ECC)
Disk Subsystem: 1 x 500 GB SATA-2, 7200 RPM
Other Hardware: None

Software
Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
Compiler: Intel C++ Compiler XE for applications running on IA-32
Version 12.0.2 Build 20110112
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	897	174	907	172	**903**	**173**	16	746	210	735	213	**737**	**212**
401.bzip2	16	1192	129	1188	130	**1189**	**130**	16	**1098**	**141**	1101	140	1096	141
403.gcc	16	750	172	733	176	**742**	**174**	16	**737**	**175**	734	175	743	173
429.mcf	16	595	245	592	246	**593**	**246**	8	**276**	**264**	277	264	273	267
445.gobmk	16	**871**	**193**	869	193	872	193	16	815	206	**818**	**205**	822	204
456.hmmer	16	558	267	**556**	**269**	556	269	8	254	294	**253**	**295**	253	295
458.sjeng	16	1027	188	**1028**	**188**	1029	188	16	**970**	**200**	970	200	970	200
462.libquantum	16	315	1050	313	1060	**315**	**1050**	16	315	1050	313	1060	**315**	**1050**
464.h264ref	16	1304	271	**1314**	**269**	1373	258	16	1308	271	**1311**	**270**	1365	259
471.omnetpp	16	622	161	**624**	**160**	624	160	16	576	174	**576**	**174**	576	174
473.astar	16	**815**	**138**	816	138	812	138	16	**815**	**138**	816	138	812	138
483.xalancbmk	16	484	228	**484**	**228**	485	227	16	484	228	**484**	**228**	485	227

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.
     Large pages were not enabled for this run
```

Platform Notes

```
    Data Reuse disabled in BIOS.
```

Base Compiler Invocation

C benchmarks:
```
     icc  -m32
```

C++ benchmarks:
```
     icpc -m32
```

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:
```
icpc -m32
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

      403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
               -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

      429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

    445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -auto-ilp32

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

    458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32
               -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

    464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/home/rcaneca/sh/SmartHeap_8.1/lib -lsmartheap
```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html
http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Itautec-Intel-Linux64-Platform.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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