



SPEC[®] CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon E5645, 2.40GHz)

SPECint[®]_rate2006 = 313

SPECint_rate_base2006 = 294

CPU2006 license: 97

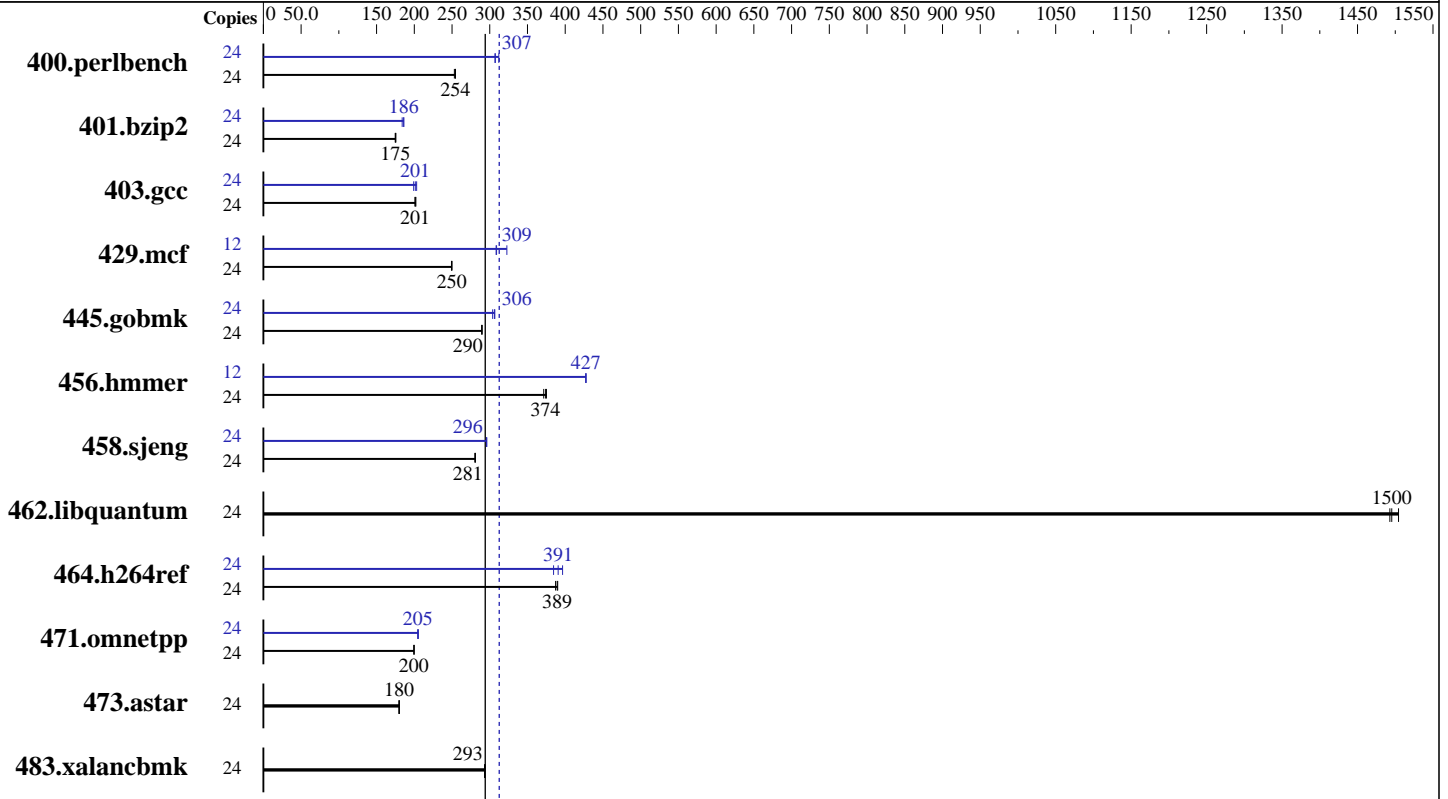
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jun-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011



SPECint_rate2006 = 313

SPECint_rate_base2006 = 294

Hardware

CPU Name: Intel Xeon E5645
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 1000 GB SATA 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon E5645, 2.40GHz)

SPECint_rate2006 = 313

SPECint_rate_base2006 = 294

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	926	253	921	255	<u>922</u>	<u>254</u>	24	<u>763</u>	<u>307</u>	764	307	752	312
401.bzip2	24	1319	176	1325	175	<u>1324</u>	<u>175</u>	24	1245	186	1259	184	<u>1245</u>	<u>186</u>
403.gcc	24	<u>960</u>	<u>201</u>	962	201	956	202	24	<u>959</u>	<u>201</u>	951	203	970	199
429.mcf	24	876	250	878	249	<u>877</u>	<u>250</u>	12	<u>354</u>	<u>309</u>	355	308	339	323
445.gobmk	24	<u>869</u>	<u>290</u>	869	290	870	289	24	<u>822</u>	<u>306</u>	821	307	829	304
456.hammer	24	597	375	603	372	<u>599</u>	<u>374</u>	12	<u>262</u>	<u>427</u>	262	427	262	428
458.sjeng	24	1034	281	<u>1035</u>	<u>281</u>	1035	280	24	981	296	982	296	<u>982</u>	<u>296</u>
462.libquantum	24	331	1500	<u>333</u>	<u>1500</u>	333	1490	24	331	1500	<u>333</u>	<u>1500</u>	333	1490
464.h264ref	24	<u>1364</u>	<u>389</u>	1362	390	1372	387	24	<u>1360</u>	<u>391</u>	1339	397	1381	385
471.omnetpp	24	<u>752</u>	<u>200</u>	751	200	752	200	24	733	205	730	205	<u>732</u>	<u>205</u>
473.astar	24	<u>937</u>	<u>180</u>	935	180	938	180	24	<u>937</u>	<u>180</u>	935	180	938	180
483.xalancbmk	24	565	293	<u>564</u>	<u>293</u>	564	293	24	565	293	<u>564</u>	<u>293</u>	564	293

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run

Platform Notes

BIOS Settings:
Fan speed = full speed (Default = Energy Saving)
Data Reuse = Disabled (Default = Enabled)

General Notes

Binaries compiled on RHEL5.5
The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1 and Gateway GW2000ht-GW170ht F1 are electronically equivalent. This result was measured on Gateway GW2000ht-GW170ht F1.

Base Compiler Invocation

C benchmarks:
icc -m32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon E5645,
2.40GHz)

SPECint_rate2006 = 313

SPECint_rate_base2006 = 294

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jun-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon E5645, 2.40GHz)

SPECint_rate2006 = 313

SPECint_rate_base2006 = 294

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Jun-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

Gateway GW2000ht-GW170ht F1 (Intel Xeon E5645, 2.40GHz)

SPECint_rate2006 = 313

SPECint_rate_base2006 = 294

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jun-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

Peak Optimization Flags (Continued)

471.omnetpp (continued):
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 23:53:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 2 August 2011.