



# SPEC® CINT2006 Result

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## Acer Incorporated

SPECint®\_rate2006 = 250

## Gateway GT350 F1 (Intel Xeon E5640)

SPECint\_rate\_base2006 = 237

CPU2006 license: 97

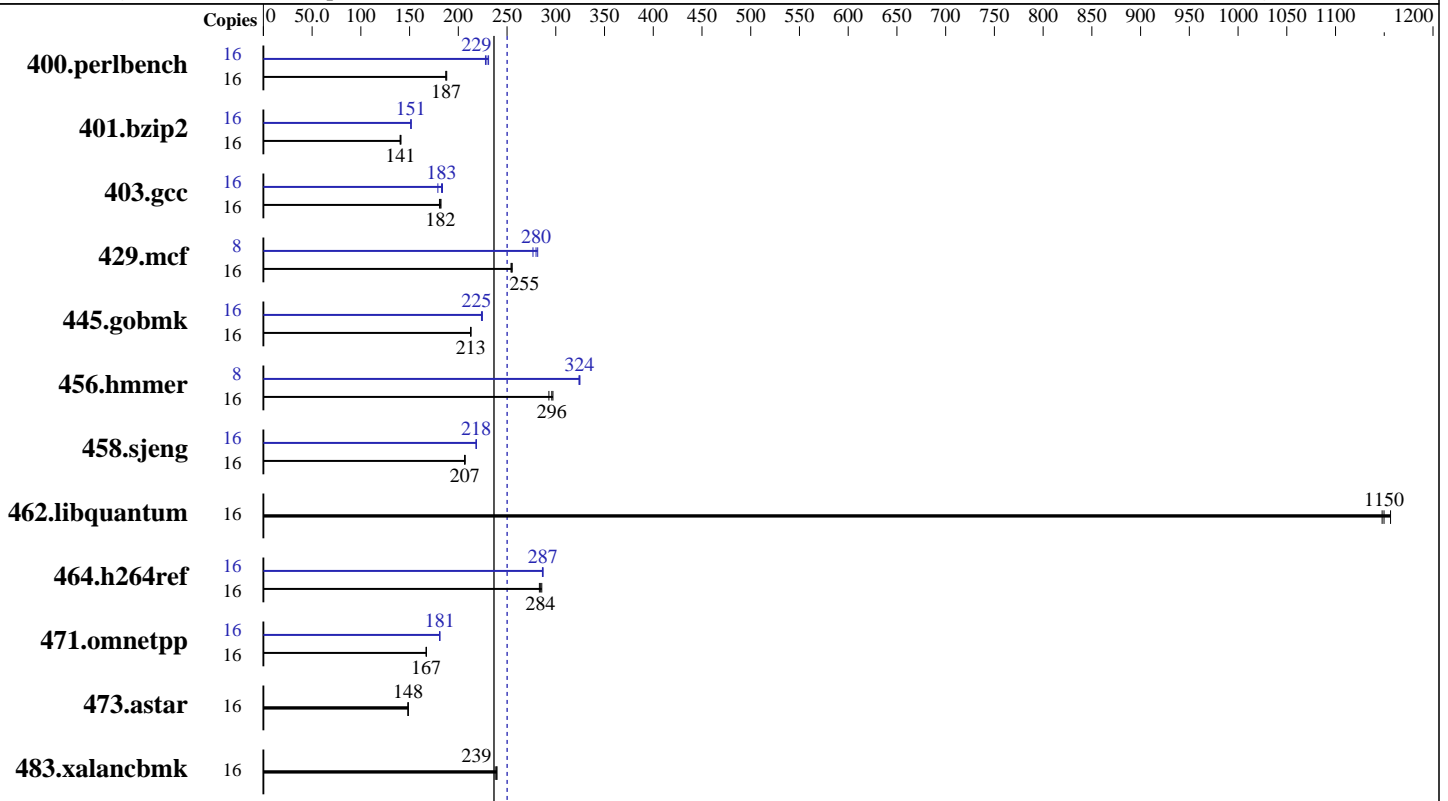
Test date: May-2011

Test sponsor: Acer Incorporated

Hardware Availability: Feb-2011

Tested by: Acer Incorporated

Software Availability: Nov-2010



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### Hardware

CPU Name: Intel Xeon E5640  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-1066R-9 ECC)  
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM SATA HDD  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	831	188	<b>834</b>	<b>187</b>	835	187	16	<b>682</b>	<b>229</b>	677	231	686	228
401.bzip2	16	1100	140	<b>1098</b>	<b>141</b>	1094	141	16	1018	152	<b>1020</b>	<b>151</b>	1021	151
403.gcc	16	713	181	<b>709</b>	<b>182</b>	707	182	16	701	184	<b>705</b>	<b>183</b>	719	179
429.mcf	16	<b>573</b>	<b>255</b>	572	255	574	254	8	259	281	264	277	<b>261</b>	<b>280</b>
445.gobmk	16	789	213	<b>789</b>	<b>213</b>	788	213	16	750	224	<b>748</b>	<b>225</b>	747	225
456.hammer	16	<b>505</b>	<b>296</b>	510	293	503	297	8	<b>230</b>	<b>324</b>	231	324	230	325
458.sjeng	16	<b>937</b>	<b>207</b>	935	207	938	206	16	<b>887</b>	<b>218</b>	886	218	887	218
462.libquantum	16	<b>288</b>	<b>1150</b>	289	1150	287	1160	16	<b>288</b>	<b>1150</b>	289	1150	287	1160
464.h264ref	16	<b>1245</b>	<b>284</b>	1240	286	1250	283	16	1234	287	<b>1235</b>	<b>287</b>	1237	286
471.omnetpp	16	598	167	<b>598</b>	<b>167</b>	598	167	16	553	181	552	181	<b>553</b>	<b>181</b>
473.astar	16	756	149	<b>756</b>	<b>148</b>	757	148	16	756	149	<b>756</b>	<b>148</b>	757	148
483.xalancbmk	16	464	238	<b>461</b>	<b>239</b>	461	240	16	464	238	<b>461</b>	<b>239</b>	461	240

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
Large pages were disabled for this run

## Platform Notes

BIOS settings:  
Fan speed = full speed (Default = Balanced)  
Data Reuse = Disabled (Default = Enabled)

## General Notes

Binaries compiled on RHEL5.5  
This result was measured on Gateway GT350 F1  
Acer AT350 F1 is electronically equivalent

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 21:50:36 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 5 July 2011.