



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = 1450

PRIMERGY RX900 S2, Intel Xeon E7-8837, 2.67 GHz

SPECint_rate_base2006 = 1370

CPU2006 license: 19

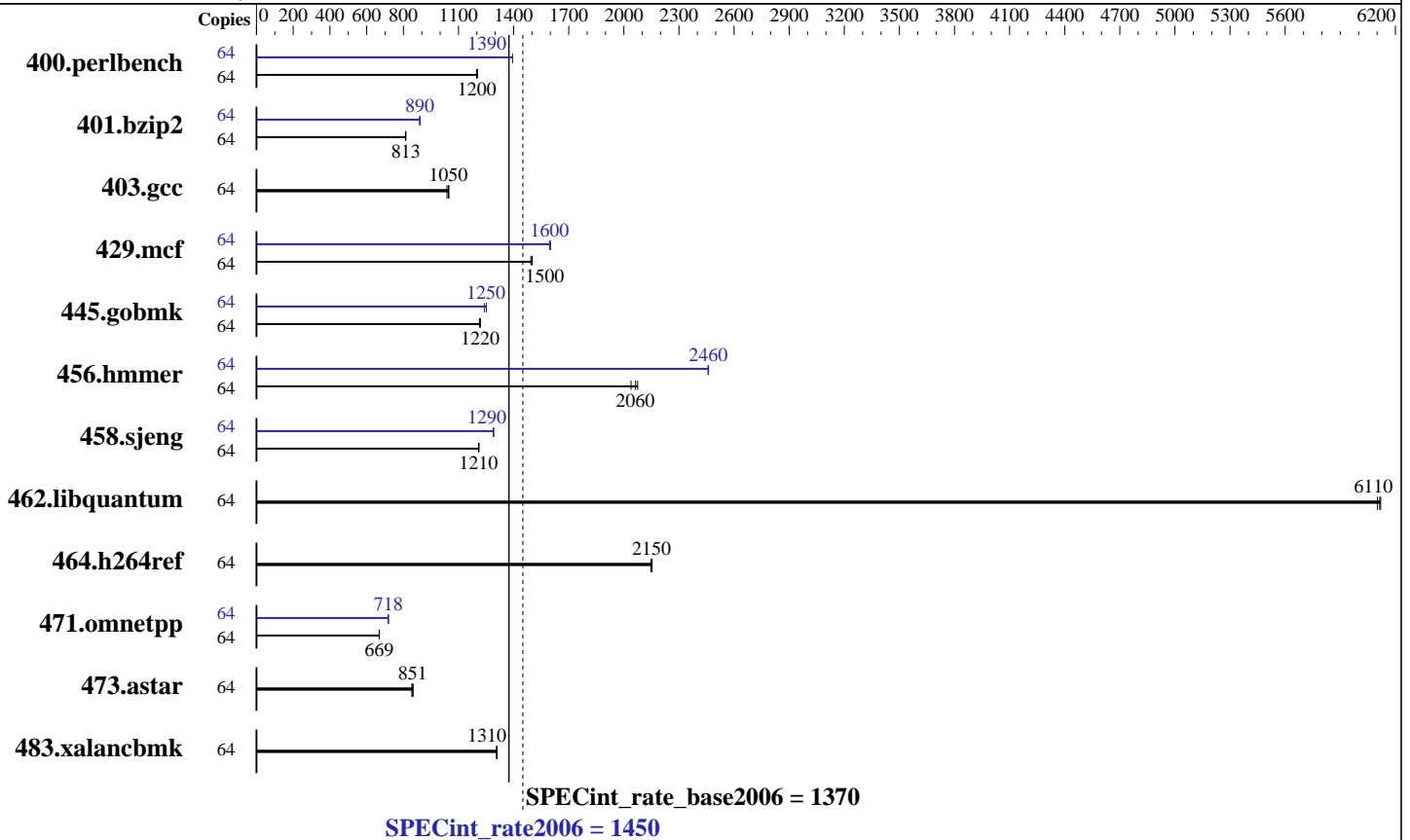
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2011

Hardware Availability: Jun-2011

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E7-8837
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 64 cores, 8 chips, 8 cores/chip
 CPU(s) orderable: 4,6,8 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 24 MB I+D on chip per chip
 Other Cache: None
 Memory: 1 TB (128 x 8 GB 4Rx8 PC3-10600R-9, ECC, running at 1066 MHz)
 Disk Subsystem: 2 x 147 GB (SAS, 15000RPM, RAID0)
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1(x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	520	1200	<u>521</u>	<u>1200</u>	522	1200	64	<u>448</u>	<u>1390</u>	448	1390	448	1390
401.bzip2	64	<u>760</u>	<u>813</u>	762	811	760	813	64	<u>694</u>	<u>890</u>	693	891	696	888
403.gcc	64	492	1050	497	1040	<u>493</u>	<u>1050</u>	64	492	1050	497	1040	<u>493</u>	<u>1050</u>
429.mcf	64	391	1490	<u>389</u>	<u>1500</u>	389	1500	64	366	1600	<u>365</u>	<u>1600</u>	365	1600
445.gobmk	64	551	1220	<u>551</u>	<u>1220</u>	553	1210	64	<u>537</u>	<u>1250</u>	536	1250	541	1240
456.hammer	64	<u>289</u>	<u>2060</u>	293	2040	288	2080	64	243	2460	<u>243</u>	<u>2460</u>	243	2460
458.sjeng	64	<u>640</u>	<u>1210</u>	639	1210	641	1210	64	601	1290	600	1290	<u>600</u>	<u>1290</u>
462.libquantum	64	217	6100	217	6120	<u>217</u>	<u>6110</u>	64	217	6100	217	6120	<u>217</u>	<u>6110</u>
464.h264ref	64	660	2150	<u>658</u>	<u>2150</u>	658	2150	64	660	2150	<u>658</u>	<u>2150</u>	658	2150
471.omnetpp	64	599	668	<u>598</u>	<u>669</u>	598	669	64	557	718	557	717	<u>557</u>	<u>718</u>
473.astar	64	<u>528</u>	<u>851</u>	527	852	531	846	64	<u>528</u>	<u>851</u>	527	852	531	846
483.xalancbmk	64	338	1310	<u>338</u>	<u>1310</u>	337	1310	64	338	1310	<u>338</u>	<u>1310</u>	337	1310

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

Large pages were not enabled for this run
The following command was used prior to run
ulimit -s unlimited
echo 1 > /proc/sys/vm/zone_reclaim_mode

General Notes

Binaries were compiled on RHEL5.5
For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m32
C++ benchmarks:
icpc -m32



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmcr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/smartheap -lsmartheap

473.astar: basepeak = yes

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Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

http://www.spec.org/cpu2006/flags/RX900S2_Platform.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

http://www.spec.org/cpu2006/flags/RX900S2_Platform.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 20:23:04 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 May 2011.