



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Acer Incorporated

SPECint®\_rate2006 = 323

## Acer AR380 F1 (Intel Xeon E5649)

SPECint\_rate\_base2006 = 303

CPU2006 license: 97

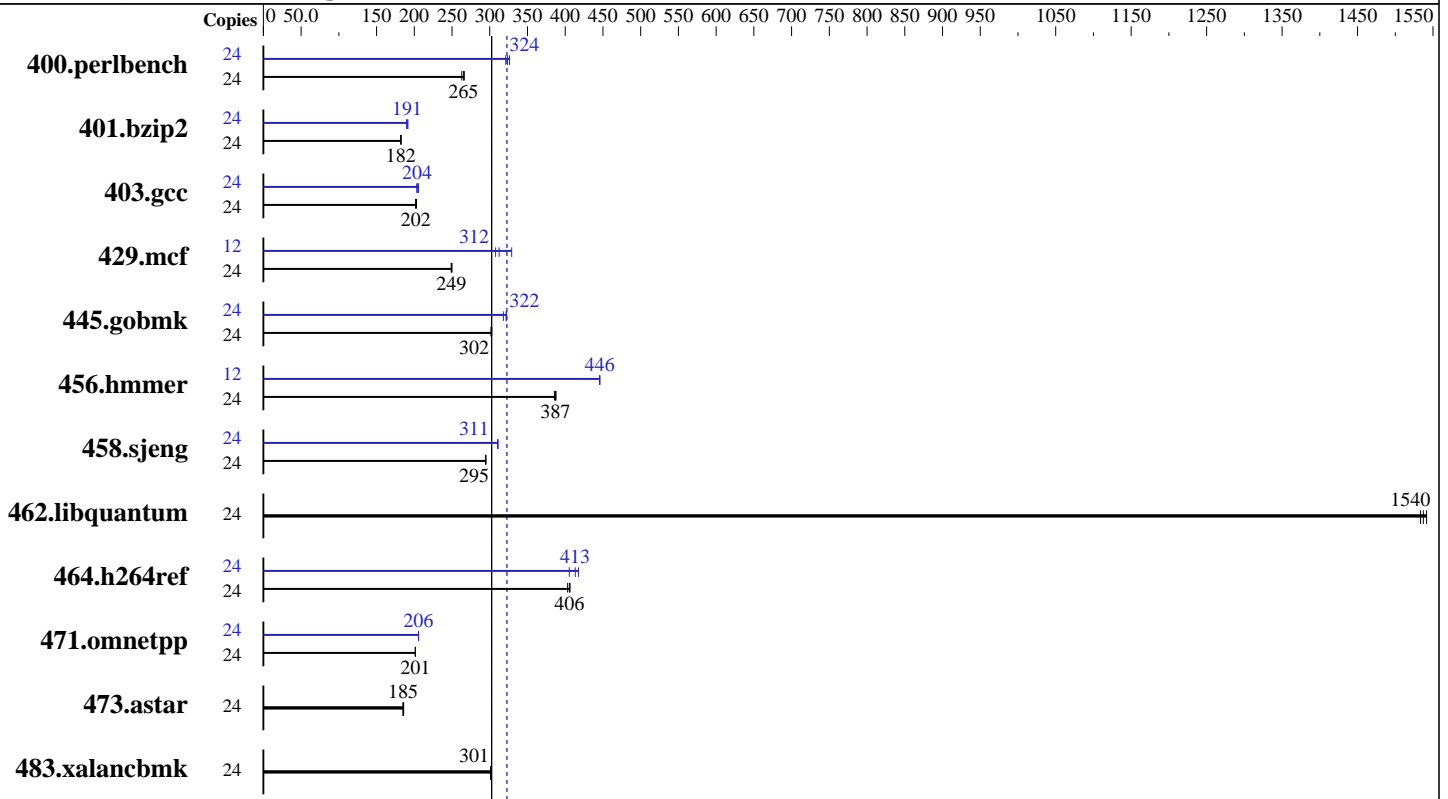
Test date: May-2011

Test sponsor: Acer Incorporated

Hardware Availability: Feb-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011



SPECint\_rate2006 = 323

SPECint\_rate\_base2006 = 303

### Hardware

CPU Name: Intel Xeon E5649  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.93 GHz  
 CPU MHz: 2533  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 1 x 300 GB SATA, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise server 11 (x86\_64) SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 323

Acer AR380 F1 (Intel Xeon E5649)

SPECint\_rate\_base2006 = 303

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	24	892	263	881	266	<b>884</b>	<b>265</b>	24	<b>724</b>	<b>324</b>	730	321	719	326
401.bzip2	24	1269	183	<b>1271</b>	<b>182</b>	1273	182	24	<b>1215</b>	<b>191</b>	1221	190	1209	192
403.gcc	24	<b>957</b>	<b>202</b>	957	202	952	203	24	940	206	950	203	<b>948</b>	<b>204</b>
429.mcf	24	876	250	879	249	<b>878</b>	<b>249</b>	12	<b>350</b>	<b>312</b>	356	308	333	329
445.gobmk	24	<b>834</b>	<b>302</b>	832	303	834	302	24	781	322	<b>783</b>	<b>322</b>	792	318
456.hammer	24	580	386	577	388	<b>578</b>	<b>387</b>	12	251	446	<b>251</b>	<b>446</b>	251	446
458.sjeng	24	985	295	<b>986</b>	<b>295</b>	986	295	24	934	311	<b>935</b>	<b>311</b>	936	310
462.libquantum	24	<b>324</b>	<b>1540</b>	324	1530	323	1540	24	<b>324</b>	<b>1540</b>	324	1530	323	1540
464.h264ref	24	1317	403	<b>1310</b>	<b>406</b>	1307	406	24	<b>1285</b>	<b>413</b>	1272	418	1310	405
471.omnetpp	24	<b>745</b>	<b>201</b>	745	201	744	202	24	730	206	<b>729</b>	<b>206</b>	729	206
473.astar	24	<b>909</b>	<b>185</b>	906	186	911	185	24	<b>909</b>	<b>185</b>	906	186	911	185
483.xalancbmk	24	550	301	<b>549</b>	<b>301</b>	549	301	24	550	301	<b>549</b>	<b>301</b>	549	301

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size  
Large pages were disabled for this run

## Platform Notes

BIOS Settings:  
Fan speed = full speed (Default = Balanced)  
Data Reuse = Disabled (Default = Enabled)

## General Notes

Binaries compiled on RHEL 5.5  
This result was measured on the Gateway GR380 F1.  
The Acer AR380 F1, AR360 F1, Gateway GR360 F1 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 323

Acer AR380 F1 (Intel Xeon E5649)

SPECint\_rate\_base2006 = 303

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 323

Acer AR380 F1 (Intel Xeon E5649)

SPECint\_rate\_base2006 = 303

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint\_rate2006 = 323

Acer AR380 F1 (Intel Xeon E5649)

SPECint\_rate\_base2006 = 303

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2011

Hardware Availability: Feb-2011

Software Availability: Jan-2011

## Peak Optimization Flags (Continued)

471.omnetpp (continued):  
-L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.html>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.xml>  
<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.  
Report generated on Wed Jul 23 18:21:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 7 June 2011.