



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint®\_rate2006 = 127

Servidor Itautec LX113 (Intel Xeon X3470)

SPECint\_rate\_base2006 = 119

CPU2006 license: 9001

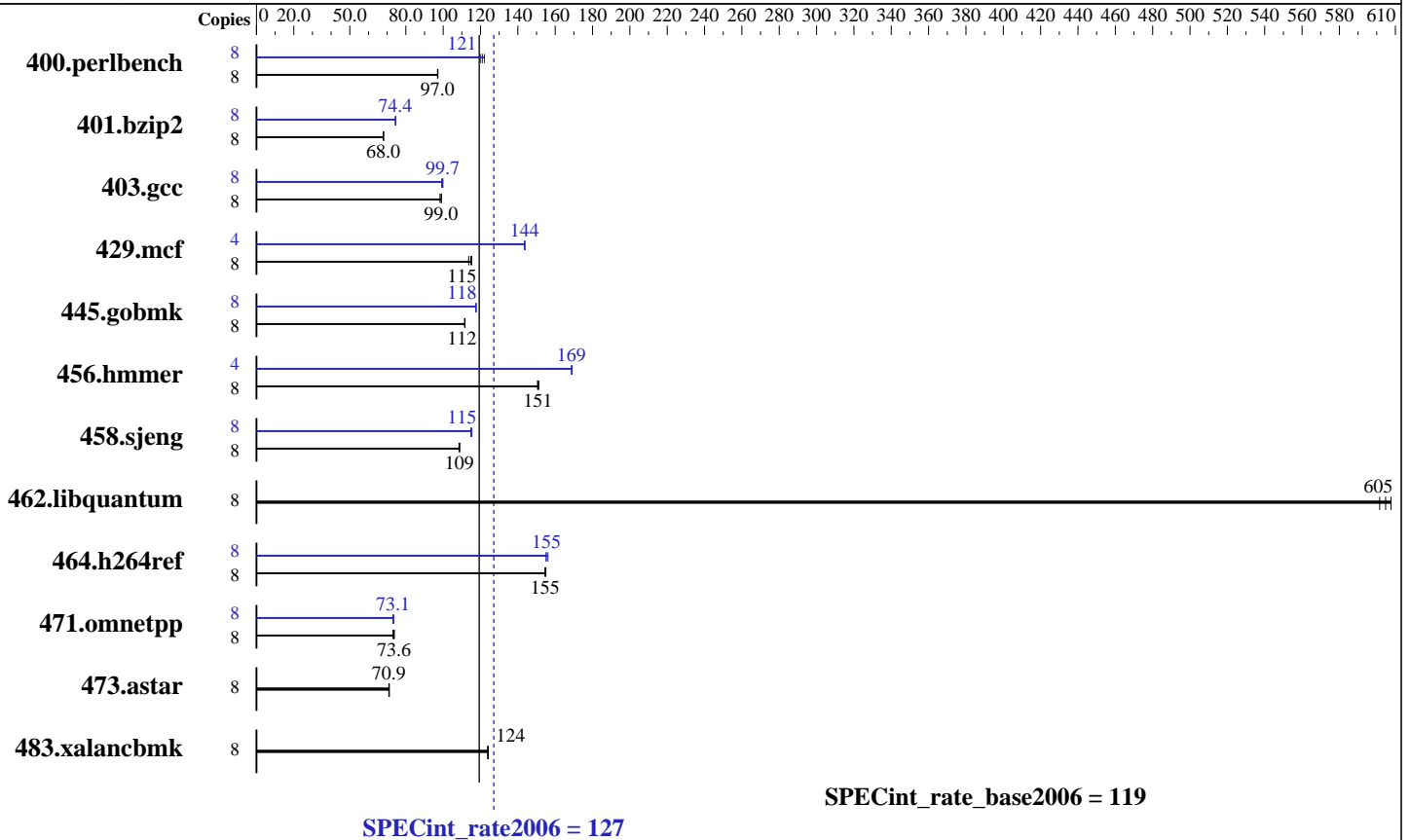
Test date: Mar-2011

Test sponsor: Itautec

Hardware Availability: Dec-2009

Tested by: Itautec

Software Availability: Jan-2011



### Hardware

CPU Name: Intel Xeon X3470  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2933  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 8 GB (4 x 2 GB 2Rx8 PC3-10600U-9, ECC)  
 Disk Subsystem: 1 x 160 GB SATA-2, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.2 Build 20110112  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itaotec

SPECint\_rate2006 = 127

Servidor Itaotec LX113 (Intel Xeon X3470)

SPECint\_rate\_base2006 = 119

CPU2006 license: 9001  
Test sponsor: Itaotec  
Tested by: Itaotec

Test date: Mar-2011  
Hardware Availability: Dec-2009  
Software Availability: Jan-2011

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	804	97.2	806	97.0	<b>806</b>	<b>97.0</b>	8	<b>646</b>	<b>121</b>	640	122	651	120
401.bzip2	8	1136	68.0	<b>1135</b>	<b>68.0</b>	1134	68.1	8	1038	74.4	<b>1038</b>	<b>74.4</b>	1035	74.6
403.gcc	8	<b>651</b>	<b>99.0</b>	655	98.3	650	99.0	8	645	99.9	649	99.2	<b>646</b>	<b>99.7</b>
429.mcf	8	642	114	633	115	<b>635</b>	<b>115</b>	4	<b>254</b>	<b>144</b>	254	143	254	144
445.gobmk	8	<b>752</b>	<b>112</b>	751	112	752	112	8	714	118	713	118	<b>714</b>	<b>118</b>
456.hammer	8	<b>495</b>	<b>151</b>	496	151	494	151	4	<b>221</b>	<b>169</b>	221	169	221	169
458.sjeng	8	<b>890</b>	<b>109</b>	890	109	892	108	8	839	115	843	115	<b>841</b>	<b>115</b>
462.libquantum	8	273	608	276	602	<b>274</b>	<b>605</b>	8	273	608	276	602	<b>274</b>	<b>605</b>
464.h264ref	8	1146	155	1142	155	<b>1145</b>	<b>155</b>	8	<b>1140</b>	<b>155</b>	1134	156	1141	155
471.omnetpp	8	684	73.1	<b>679</b>	<b>73.6</b>	677	73.8	8	<b>684</b>	<b>73.1</b>	680	73.5	684	73.1
473.astar	8	789	71.2	792	70.9	<b>792</b>	<b>70.9</b>	8	789	71.2	792	70.9	<b>792</b>	<b>70.9</b>
483.xalancbmk	8	445	124	446	124	<b>445</b>	<b>124</b>	8	445	124	446	124	<b>445</b>	<b>124</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run.  
Large pages were not enabled for this run

## General Notes

This result was measured on the Servidor Itaotec LX114.  
The Servidor Itaotec LX103, Servidor Itaotec LX113 and the Servidor Itaotec LX114 are electronically equivalent.

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 127

Servidor Itautec LX113 (Intel Xeon X3470)

SPECint\_rate\_base2006 = 119

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Mar-2011  
Hardware Availability: Dec-2009  
Software Availability: Jan-2011

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 127

Servidor Itautec LX113 (Intel Xeon X3470)

SPECint\_rate\_base2006 = 119

CPU2006 license: 9001  
Test sponsor: Itautec  
Tested by: Itautec

Test date: Mar-2011  
Hardware Availability: Dec-2009  
Software Availability: Jan-2011

## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/opt/sh/SmartHeap\_8.1/lib -lsmartheap

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Itautec

SPECint\_rate2006 = 127

Servidor Itautec LX113 (Intel Xeon X3470)

SPECint\_rate\_base2006 = 119

CPU2006 license: 9001

Test date: Mar-2011

Test sponsor: Itautec

Hardware Availability: Dec-2009

Tested by: Itautec

Software Availability: Jan-2011

## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.html>

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Platform-Settings.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.20110316.xml>

<http://www.spec.org/cpu2006/flags/Itautec-Intel-Platform-Settings.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 19:12:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 12 April 2011.