SPEC® CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX300 S6, Intel Xeon X5647, 2.93 GHz

SPECint®_rate2006 =   266

SPECint_rate_base2006 =   252

CPU2006 license: 19	**Test date:** Jan-2011
Test sponsor: Fujitsu	**Hardware Availability:** Feb-2011
Tested by: Fujitsu	**Software Availability:** Jan-2011

SPECint_rate2006 = 266

SPECint_rate_base2006 = 252

Hardware

CPU Name:	Intel Xeon X5647
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2933
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)
Disk Subsystem:	1 x SAS, 300 GB, 10000 RPM
Other Hardware:	--

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
Compiler:	Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
Auto Parallel:	No
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V9.01

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX300 S6, Intel Xeon X5647, 2.93 GHz

SPECint_rate2006 =   266
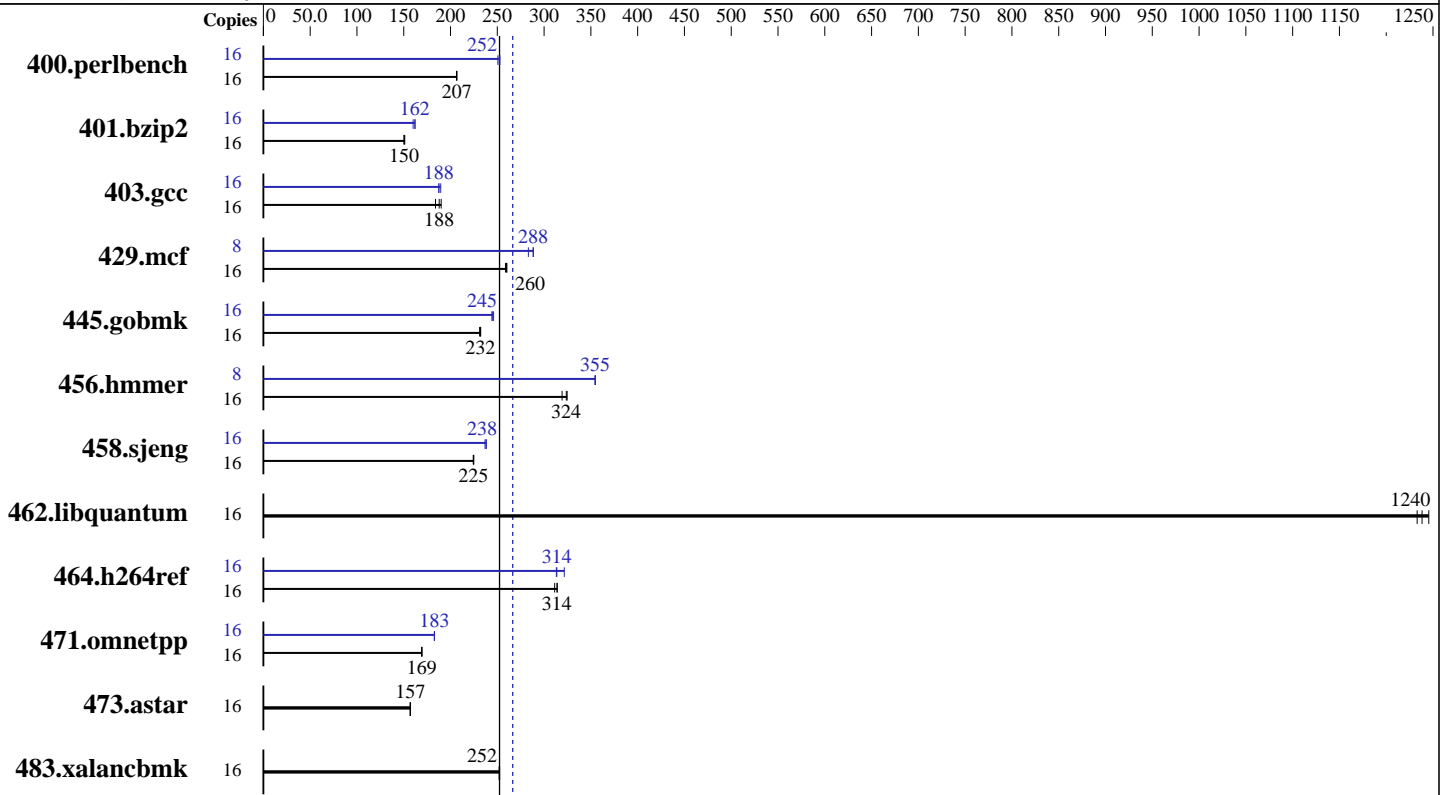
SPECint_rate_base2006 =   252

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	757	206	**757**	**207**	756	207	16	618	253	**620**	**252**	624	251
401.bzip2	16	**1026**	**150**	1027	150	1022	151	16	952	162	965	160	**954**	**162**
403.gcc	16	700	184	**685**	**188**	678	190	16	680	190	**687**	**188**	687	187
429.mcf	16	561	260	564	259	**561**	**260**	8	258	283	253	289	**253**	**288**
445.gobmk	16	**724**	**232**	726	231	723	232	16	687	244	682	246	**686**	**245**
456.hmmer	16	**461**	**324**	460	325	468	319	8	210	355	211	354	**210**	**355**
458.sjeng	16	861	225	863	224	**862**	**225**	16	**813**	**238**	817	237	813	238
462.libquantum	16	269	1230	**268**	**1240**	266	1250	16	269	1230	**268**	**1240**	266	1250
464.h264ref	16	1127	314	**1128**	**314**	1137	311	16	1131	313	**1129**	**314**	1101	322
471.omnetpp	16	592	169	590	169	**590**	**169**	16	547	183	**547**	**183**	547	183
473.astar	16	**715**	**157**	716	157	714	157	16	**715**	**157**	716	157	714	157
483.xalancbmk	16	438	252	**438**	**252**	437	252	16	438	252	**438**	**252**	437	252

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     numactl was used to bind copies to the cores

Operating System Notes

     'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
     Hugepages were not configured on the system

Platform Notes

     BIOS configuration:
     Data Reuse Optimization = Disable

General Notes

     This result was measured on the PRIMERGY RX300 S6. The PRIMERGY TX300 S6
     and the PRIMERGY RX300 S6 are electronically equivalent.

     For information about Fujitsu please visit: http://www.fujitsu.com
     Binaries were compiled on RHEL5.5 with binutils-2.17.50.0.6-14.el5

SPEC CINT2006 Result
Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX300 S6, Intel Xeon X5647, 2.93 GHz

SPECint_rate2006 = 266

SPECint_rate_base2006 = 252

CPU2006 license: 19		**Test date:**	Jan-2011
Test sponsor: Fujitsu		**Hardware Availability:**	Feb-2011
Tested by: Fujitsu		**Software Availability:**	Jan-2011

Base Compiler Invocation

C benchmarks:
```
icc  -m32
```

C++ benchmarks:
```
icpc -m32
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

400.perlbench: `icc -m64`

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

Continued on next page

Fujitsu

PRIMERGY RX300 S6, Intel Xeon X5647, 2.93 GHz

SPECint_rate2006 = 266

SPECint_rate_base2006 = 252

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Peak Compiler Invocation (Continued)

C++ benchmarks:
    icpc -m32

Peak Portability Flags

        400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
          401.bzip2: -DSPEC_CPU_LP64
          456.hmmer: -DSPEC_CPU_LP64
          458.sjeng: -DSPEC_CPU_LP64
       462.libquantum: -DSPEC_CPU_LINUX
      483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

        400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

          401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -opt-prefetch -auto-ilp32 -ansi-alias
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

          403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div
                  -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

          429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -ansi-alias -auto-ilp32

        445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                  -ansi-alias -auto-ilp32

          456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

          458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                  -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                  -unroll4 -auto-ilp32
                  -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

       462.libquantum: basepeak = yes

Fujitsu

PRIMERGY RX300 S6, Intel Xeon X5647, 2.93 GHz

SPECint_rate2006 =   266

SPECint_rate_base2006 = 252

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2011
Hardware Availability: Feb-2011
Software Availability: Jan-2011

Peak Optimization Flags (Continued)

464.h264ref: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-xSSE4.2`(pass 2) `-prof-gen`(pass 1) `-ipo`(pass 2)
`-O3`(pass 2) `-no-prec-div`(pass 2) `-prof-use`(pass 2)
`-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
`-L/smartheap -lsmartheap`

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 16:14:02 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 March 2011.