



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®\_rate2006 = 163

PRIMERGY BX924 S2, Intel Xeon E5606, 2.13 GHz

SPECint\_rate\_base2006 = 154

CPU2006 license: 19

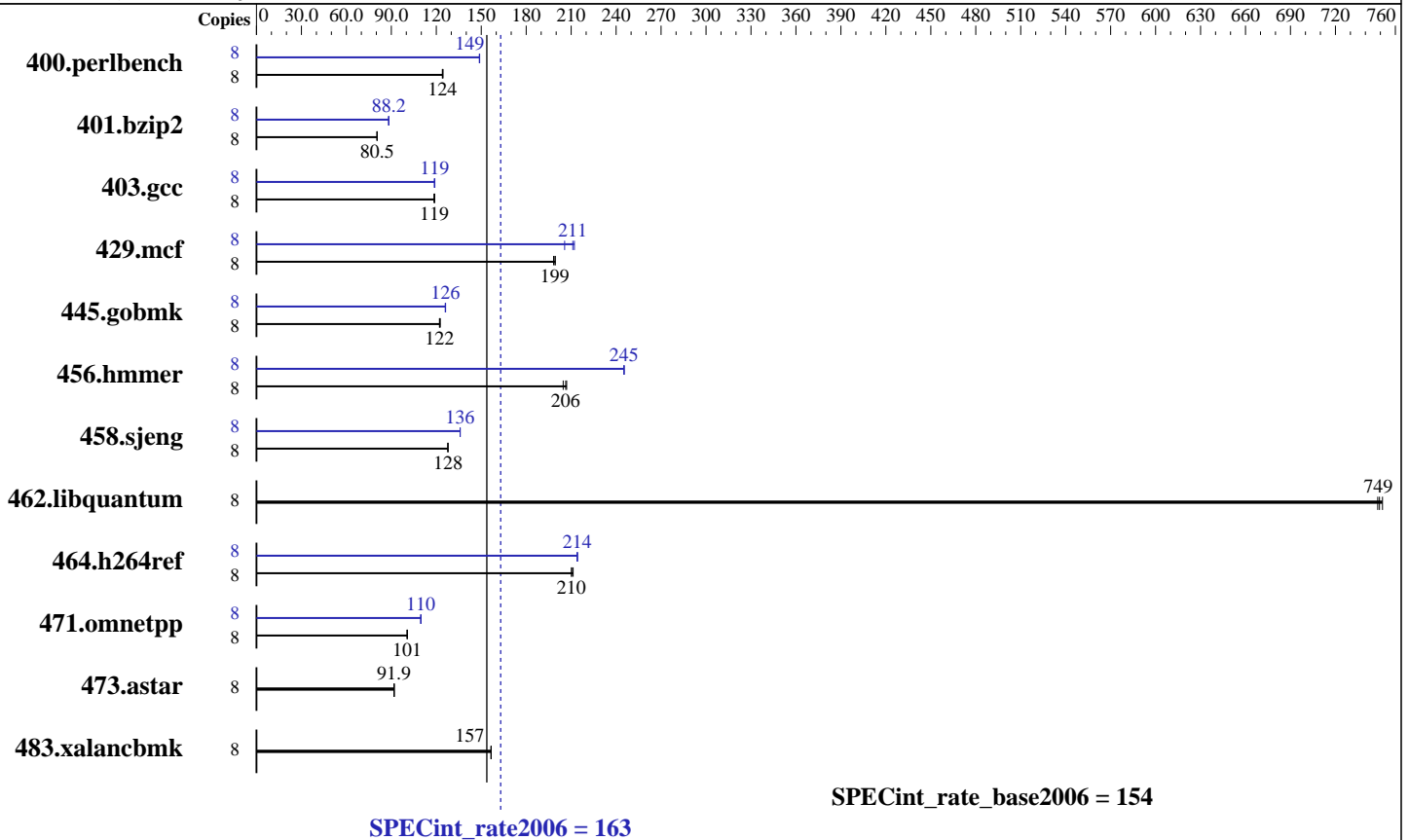
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jan-2011

Hardware Availability: Feb-2011

Software Availability: Nov-2010



### Hardware

CPU Name: Intel Xeon E5606  
 CPU Characteristics:  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz and CL7)  
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM  
 Other Hardware: --

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64) with SP1, Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.0.082 Build 20101006  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	628	124	<b><u>629</u></b>	<b><u>124</u></b>	630	124	8	<b><u>525</u></b>	<b><u>149</u></b>	525	149	525	149
401.bzip2	8	958	80.6	<b><u>959</u></b>	<b><u>80.5</u></b>	961	80.3	8	<b><u>875</u></b>	<b><u>88.2</u></b>	875	88.2	874	88.3
403.gcc	8	<b><u>542</u></b>	<b><u>119</u></b>	542	119	544	118	8	541	119	543	119	<b><u>541</u></b>	<b><u>119</u></b>
429.mcf	8	368	198	<b><u>366</u></b>	<b><u>199</u></b>	366	199	8	355	206	344	212	<b><u>346</u></b>	<b><u>211</u></b>
445.gobmk	8	<b><u>686</u></b>	<b><u>122</u></b>	684	123	687	122	8	<b><u>665</u></b>	<b><u>126</u></b>	666	126	665	126
456.hammer	8	361	207	365	205	<b><u>362</u></b>	<b><u>206</u></b>	8	<b><u>304</u></b>	<b><u>245</u></b>	304	245	304	245
458.sjeng	8	<b><u>758</u></b>	<b><u>128</u></b>	757	128	758	128	8	712	136	<b><u>712</u></b>	<b><u>136</u></b>	712	136
462.libquantum	8	221	751	<b><u>221</u></b>	<b><u>749</u></b>	222	748	8	221	751	<b><u>221</u></b>	<b><u>749</u></b>	222	748
464.h264ref	8	842	210	838	211	<b><u>842</u></b>	<b><u>210</u></b>	8	826	214	828	214	<b><u>827</u></b>	<b><u>214</u></b>
471.omnetpp	8	<b><u>497</u></b>	<b><u>101</u></b>	496	101	498	100	8	<b><u>456</u></b>	<b><u>110</u></b>	455	110	456	110
473.astar	8	<b><u>611</u></b>	<b><u>91.9</u></b>	611	92.0	612	91.8	8	<b><u>611</u></b>	<b><u>91.9</u></b>	611	92.0	612	91.8
483.xalancbmk	8	352	157	353	157	<b><u>353</u></b>	<b><u>157</u></b>	8	352	157	353	157	<b><u>353</u></b>	<b><u>157</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Large pages were not enabled for this run

## Platform Notes

BIOS configuration:  
Data Reuse Optimization = Disable  
Performance/Power Setting = Traditional

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on SLES 10 SP1 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m32

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## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:  
icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div  
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/smartheap -lsmartheap

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## Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revA.20110222.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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