SPEC® CINT2006 Result
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Acer Incorporated

Gateway GW2000h-GW170h F1 (Intel Xeon L5609, 1.86 GHz)

SPECint®_rate2006 =   146
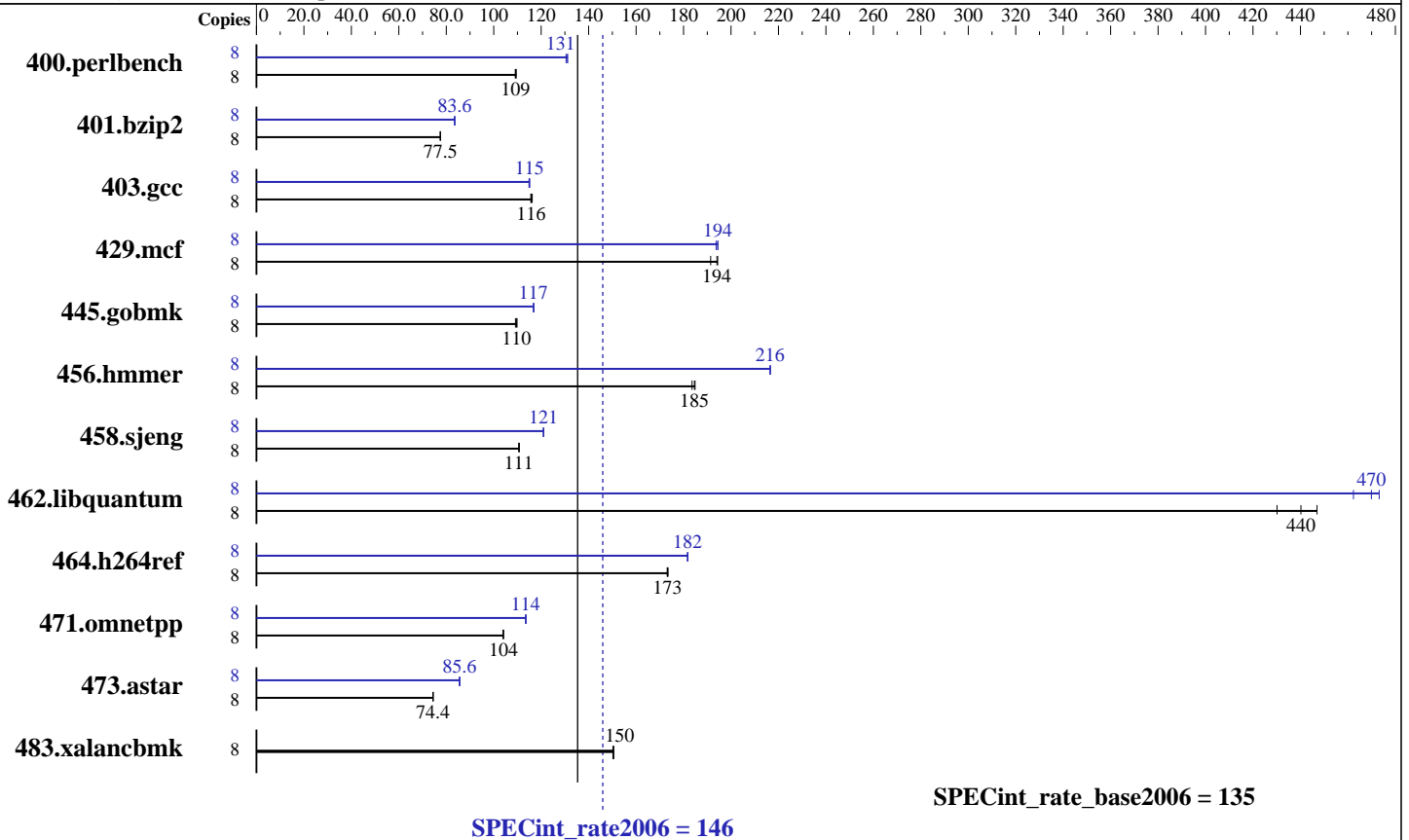
SPECint_rate_base2006 = 135

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Jul-2010

Hardware Availability: Aug-2010

Software Availability: Jan-2010

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Hardware

CPU Name:	Intel Xeon L5609
CPU Characteristics:	
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	48 GB (12 x 4 GB 2Rx8 PC3-10600R-9, ECC, memory runs at 1066 MHz)
Disk Subsystem:	1 x 1000 GB SATA II, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) Kernel 2.6.27.19-5-default
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel:	No
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	717	109	**715**	**109**	714	109	8	**597**	**131**	599	130	596	131
401.bzip2	8	996	77.5	996	77.5	**996**	**77.5**	8	924	83.6	925	83.5	**924**	**83.6**
403.gcc	8	557	116	**556**	**116**	554	116	8	559	115	561	115	**560**	**115**
429.mcf	8	381	191	375	194	**376**	**194**	8	377	194	375	194	**375**	**194**
445.gobmk	8	769	109	764	110	**766**	**110**	8	719	117	**719**	**117**	718	117
456.hmmer	8	407	184	**404**	**185**	404	185	8	345	217	345	216	**345**	**216**
458.sjeng	8	874	111	**875**	**111**	875	111	8	800	121	800	121	**800**	**121**
462.libquantum	8	385	430	371	447	**377**	**440**	8	350	473	**353**	**470**	359	462
464.h264ref	8	**1022**	**173**	1021	173	1023	173	8	975	182	**974**	**182**	974	182
471.omnetpp	8	480	104	481	104	**480**	**104**	8	**440**	**114**	440	114	441	113
473.astar	8	755	74.4	**755**	**74.4**	754	74.5	8	**656**	**85.6**	655	85.7	657	85.5
483.xalancbmk	8	**367**	**150**	367	150	367	151	8	**367**	**150**	367	150	367	151

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
    The config file option 'submit' was used.
     numactl was used to bind copies to the cores
```

Operating System Notes

```
     'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

General Notes

```
     Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

     The Acer AW2000h-AW170h F1, Gateway GW2000h-GW170h F1, Acer AW2000ht-AW170ht F1
     and Gateway GW2000ht-GW170ht F1 are electronically equivalent.
     This result was measured on GW2000ht-GW170ht F1.
```

Base Compiler Invocation

C benchmarks:

```
      icc  -m32
```

C++ benchmarks:

```
      icpc -m32
```

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Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
```

C++ benchmarks:

```
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

```
    403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
    icc  -m32
```

```
      401.bzip2: icc -m64
```

```
     456.hmmer: icc -m64
```

```
      458.sjeng: icc -m64
```

```
   462.libquantum: icc -m64
```

C++ benchmarks (except as noted below):

```
    icpc -m32
```

```
       473.astar: icpc -m64
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
       401.bzip2: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

```
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                 -prof-use(pass 2) -ansi-alias

     401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

       403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

       429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

     445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                -ipo -no-prec-div -ansi-alias

    456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
               -ansi-alias -auto-ilp32

     458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
                -opt-prefetch

   464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

     473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
```

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Peak Optimization Flags (Continued)

473.astar (continued):
```
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revG.20101027.xml

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